TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSⅢ)

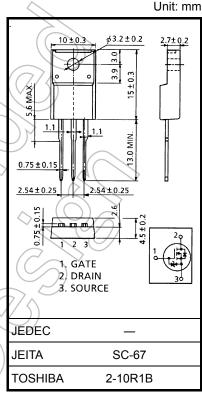
# 2SK3844

Switching Regulator, DC-DC Converter Applications Motor Drive Applications

- Low drain-source ON-resistance:  $R_{DS(ON)} = 4.1 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: |Yfs| = 63 S (typ.)
- Low leakage current:  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 60 \text{ V)}$
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics			Symbol	Rating	Unit	
Drain-source voltage			$V_{DSS}$	60	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)			$V_{DGR}$	60	У	
Gate-source voltage			$V_{GSS}$	±20	> v	
Drain current	DC	(Note 1)	ID	45	Α	
	Pulse	(Note 1)	$I_{DP}$	180		
Drain power dissipation (Tc=25°C)			P <sub>D</sub> <	45	W	
Single pulse avalanche energy (Note 2)			EAS	527	m)	
Avalanche current			IAR	45	Α	
Repetitive avalanche energy (Note 3)			(EAR \	4.0	mJ	
Channel temperature			Tch	150	-2°C	
Storage temperature range			7/⟨T <sub>stg</sub>	-55 to 150	√,°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: V  $_{DD}$  = 25 V, T  $_{Ch}$  = 25  $^{\circ}C$  (initial), L = 353  $\mu H,~I_{AR}$  = 45 A, R  $_{G}$  = 25  $\Omega$
- Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

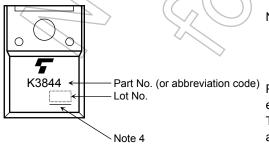
### **Electrical Characteristics (Ta = 25°C)**

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	35	_	_	V
Gate threshold voltage		$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	) /_	4.0	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 23 A	77	4.1	5.8	mΩ
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 23 A	32	63	_	S
Input capacitance		C <sub>iss</sub>			12400	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	700	_	pF
Output capacitance		Coss		_	1100	_	
Switching time	Rise time	t <sub>r</sub>	Vgs 10 V	- (	18	<u> </u>	
	Turn-on time	t <sub>on</sub>		7	45	) —	
	Fall time	t <sub>f</sub>	VDD ≈ 30 V — 35				ns
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>w</sub> = 10 μs		200	_	
Total gate charge (gate-source plus		Qg		) —	196	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$	_	148	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	_	48	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	(7)\( \) -	_	_	45	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>		_	_	180	Α
Forward voltage (diode)	VDSF	$I_{DR} = 45 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.5	V
Reverse recovery time	tir	I <sub>DR</sub> = 45 A, V <sub>GS</sub> = 0 V,	_	67	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 50 A/μs	_	70	_	nC

## Marking

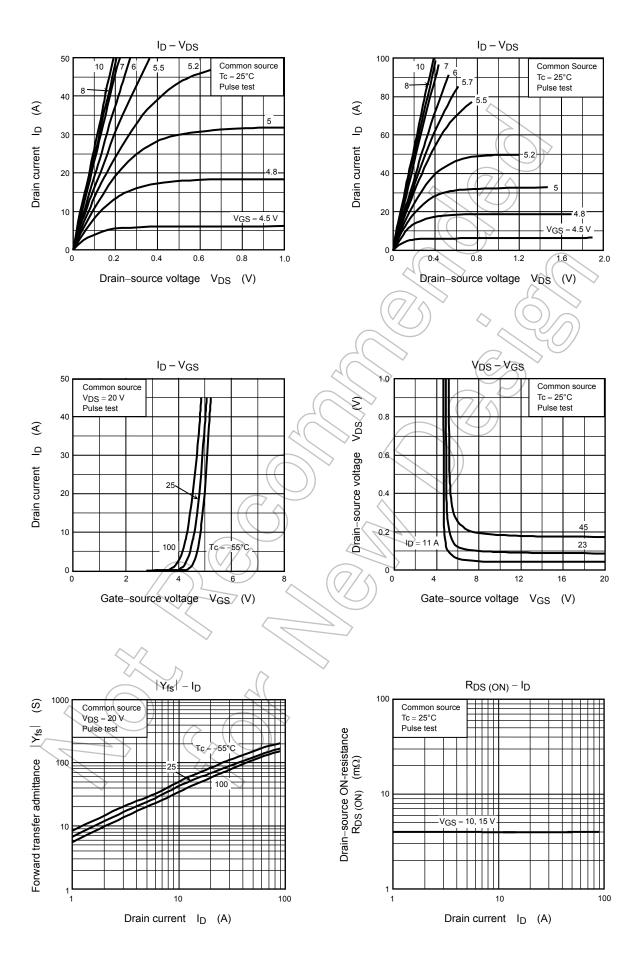


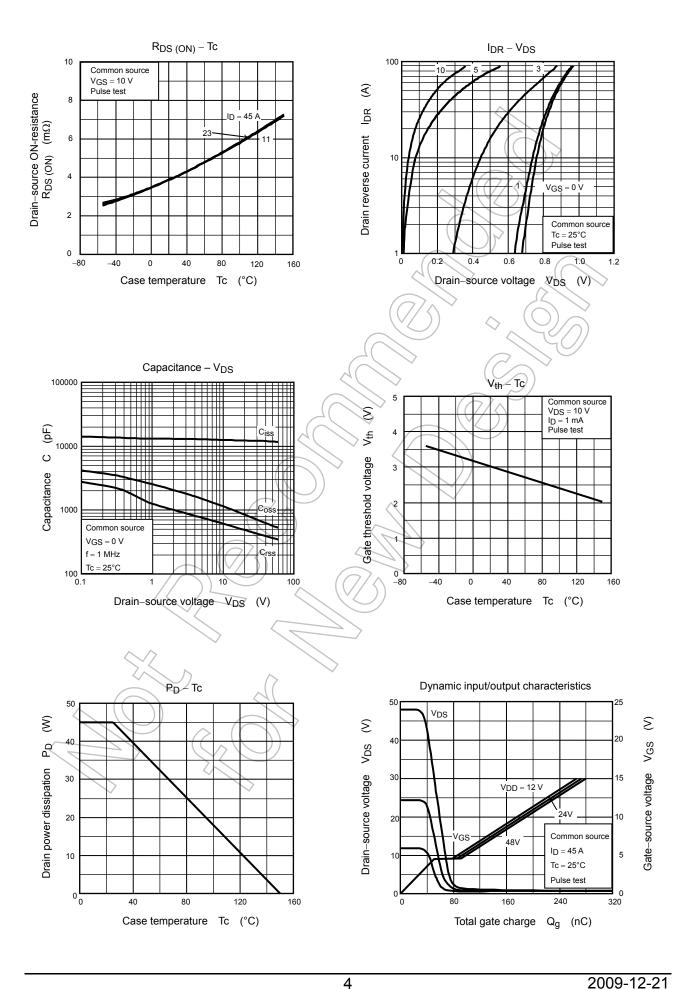
Note 4: A line under a Lot No. identifies the indication of product Labels.

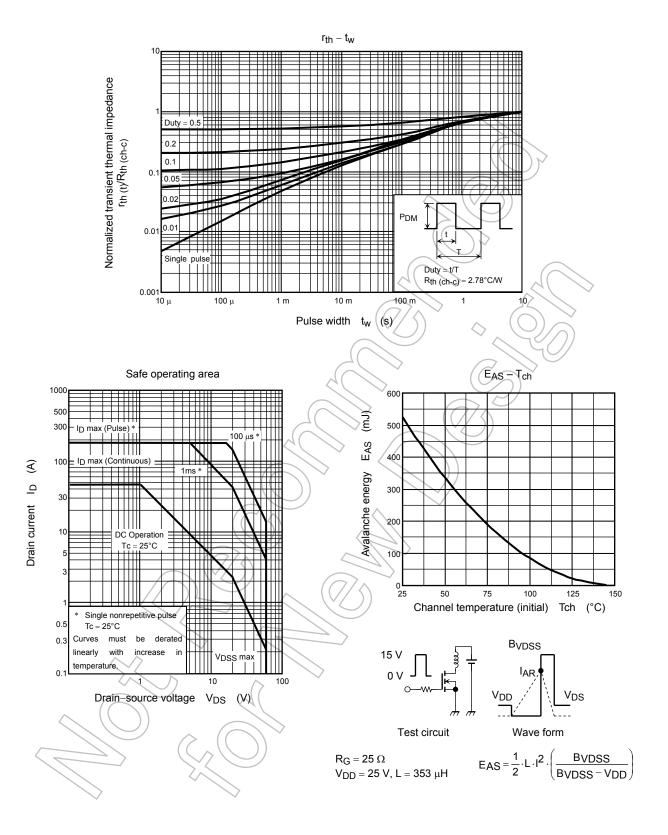
Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.







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