MOSFETs Silicon N-Channel MOS (π-MOSVII)

# TK10X40D

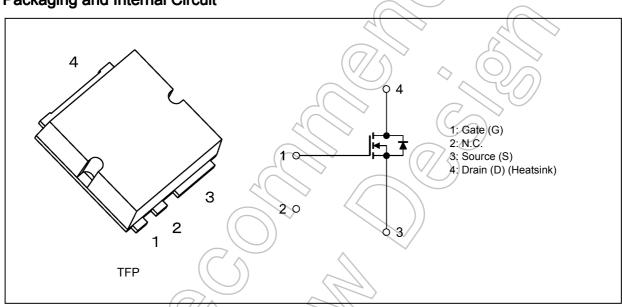
### 1. Applications

Switching Voltage Regulators

#### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 0.46 \Omega$  (typ.)
- (2) High forward transfer admittance:  $|Y_{fs}| = 5.0 \text{ S}$  (typ.)
- (3) Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 400 \ V)$
- (4) Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	$\bigcirc$	Symbol	Rating	Unit
Drain-source voltage	$\rangle$	V <sub>DSS</sub>	400	V
Gate-source voltage		V <sub>GSS</sub>	±30	
Drain current (DC)	(Note 1)	Ι <sub>D</sub>	10	A
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	40	
Power dissipation $(T_c = 25^{\circ}C)$		PD	125	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	337	mJ
Avalanche current		I <sub>AR</sub>	10	Α
Repetitive avalanche energy	(Note 3)	E <sub>AR</sub>	12.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2009-10

## 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	1.00	°C/W
lote 1: Ensure that the channel temperature does not exceed 150°C. lote 2: $V_{DD}$ = 90 V, $T_{ch}$ = 25°C (initial), L = 5.47 mH, $R_G$ = 25 $\Omega$ , $I_{AR}$ = 10 A lote 3: Repetitive rating; pulse width limited by maximum channel temperature			
lote: This transistor is sensitive to electrostatic discharge and should be handled	with care.		
	>		
		$\sum_{i=1}^{n}$	

#### 6. Electrical Characteristics

### 6.1. Static Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±30 V, $V_{DS}$ = 0 V	_	_	±1	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V	$\langle \rangle$		10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	400		—	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	(	4.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A		0.46	0.55	Ω
Forward transfer admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	1,3	5.0	_	S

## 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1050	$\rightarrow$	pF
Reverse transfer capacitance	C <sub>rss</sub>			5	-	
Output capacitance	C <sub>oss</sub>		((	100	_	
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1.	X	25	) _	ns
Switching time (turn-on time)	t <sub>on</sub>		$\sim$	60	_	
Switching time (fall time)	t <sub>f</sub>			× 10	—	
Switching time (turn-off time)	t <sub>off</sub>			75	—	

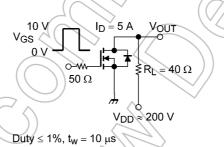


Fig. 6.2.1 Switching Time Test Circuit

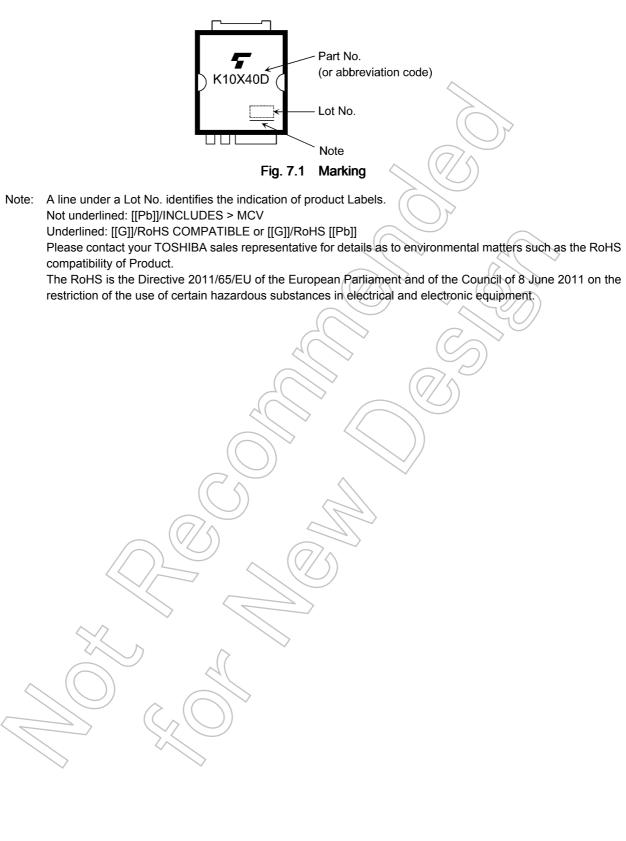
## 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 320 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	_	20	—	nC
Gate-source charge	Q <sub>gs</sub>			13	_	
Gate-drain charge	Q <sub>gd</sub>			7		

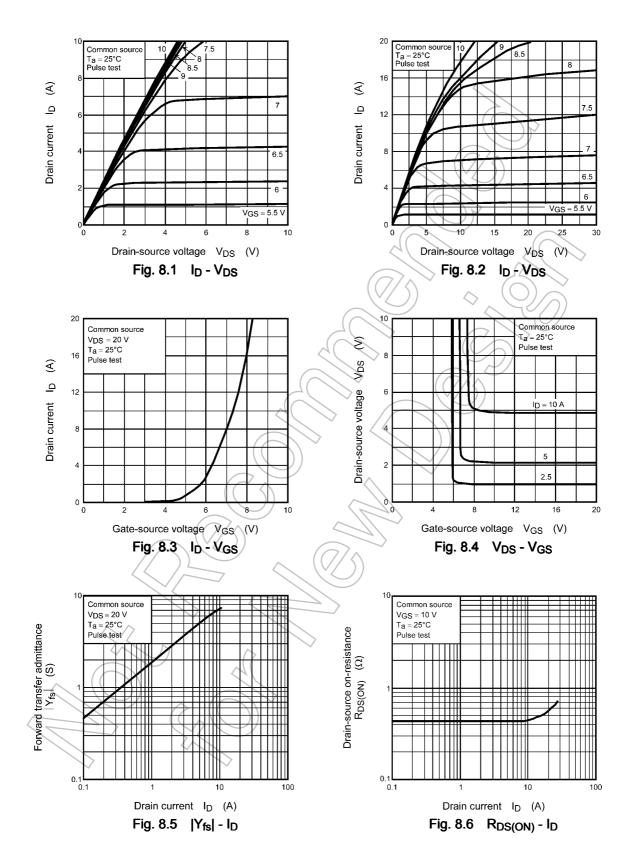
## 6.4. Source-Drain Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

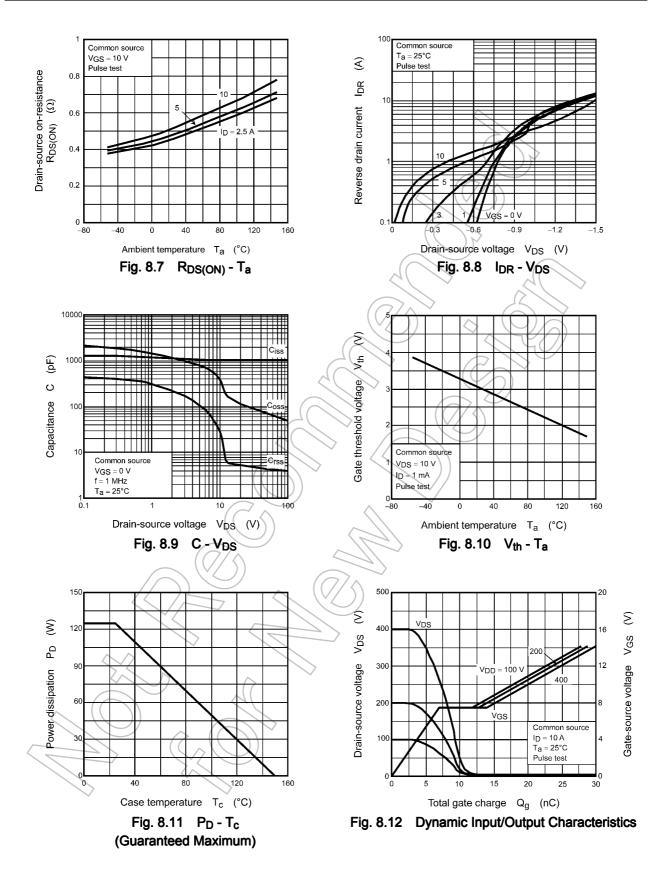
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 1)	I <sub>DR</sub>	—	_	_	10	A
Reverse drain current (pulsed)	(Note 1)	I <sub>DRP</sub>	—		—	40	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR1</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	1300	_	ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 100 A/μs	-	12.4	_	μC

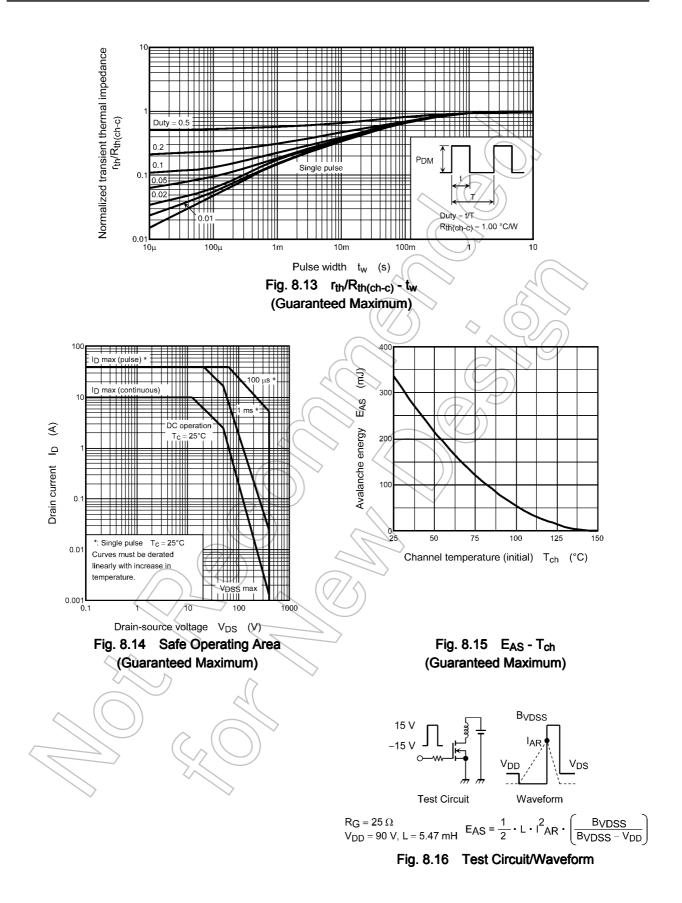
### 7. Marking (Note)



### 8. Characteristics Curves (Note)





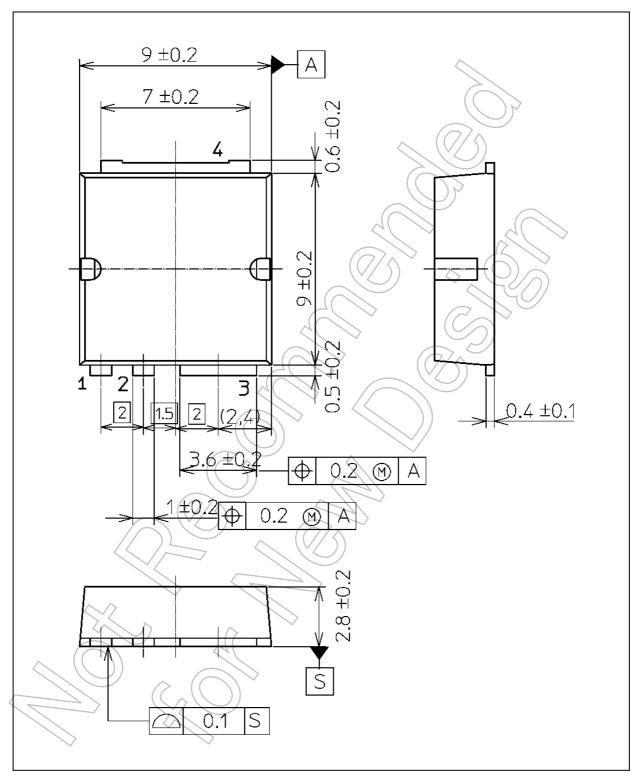


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### Package Dimensions

Unit: mm

TK10X40D



#### Weight: 0.74 g (typ.)

Pac	kage Name(s)
JEITA: SC-97	
TOSHIBA: 2-9F1S	
Nickname: TFP	

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