

# TMP91CW40FG

16-bit microcontroller with LCD driver supporting 160 pixels

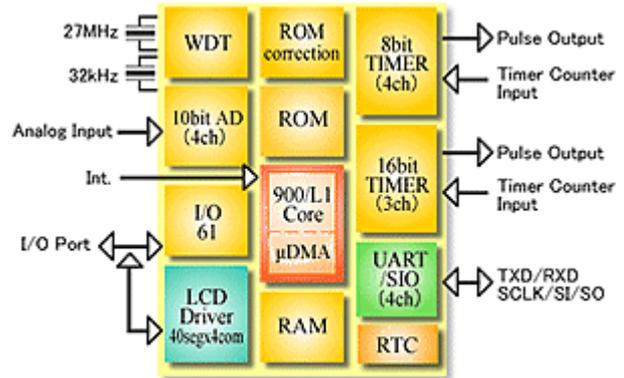
## Features

### TLCS-900/L1 CPU Core

- ▶ Operating voltage:  
1.8 to 3.6V (Mask)
- ▶ Minimum instruction execution time:  
148 ns (at 27 MHz, 2.7 to 3.6 V)  
250 ns (at 16 MHz, 2.2 to 3.6 V)  
400 ns (at 10 MHz, 1.8 to 3.6 V, except LCDD operation)

### Built-in Functions

- ▶ LCD driver : 40 seg. × 4 com.(On-chip voltage booster)
- ▶ SIO/UART : 4 channels
- ▶ 16-bit timer : 3 channels
- ▶ 8-bit timer : 4 channels
- ▶ 10-bit AD converter : 4 channels
- ▶ Key-on wake-up : 4 channels
- ▶ Program patch logic : 6 banks



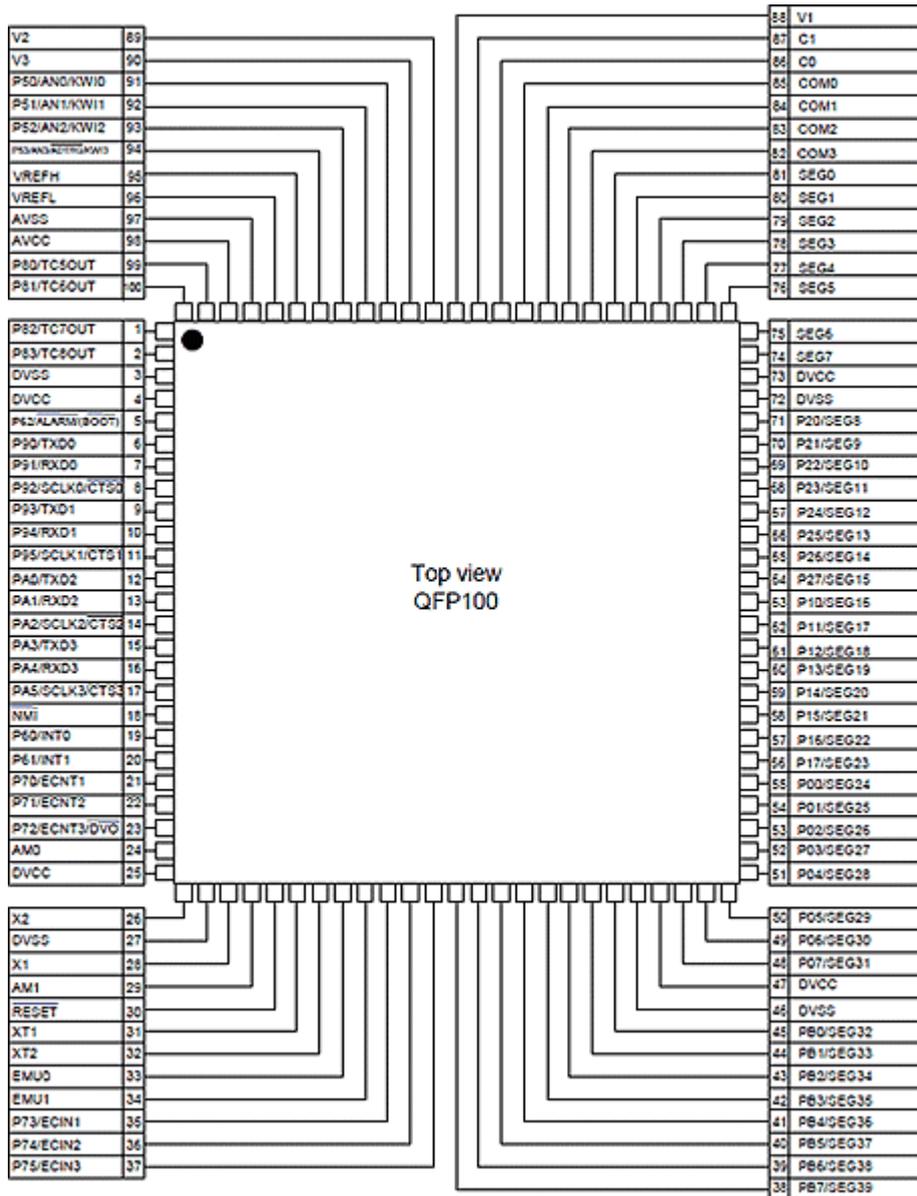
### Memory Size

Part number	ROM	RAM
TMP91CW40FG	128 Kbytes	4 Kbytes

\*Products with flash memory are also available.

## Package Information

### Pin Assignments



Package name: LQFP100-P-1414-0.50F

## Development Systems

### Software Products

Toshiba Integrated Development Environment	C Compiler	SW96CN0-ZCC: 1 license SW96CN3-ZCC: 10 licenses
	Integrated Development Environment	SW00MN0-ZCC: 1 license SW00MN3-ZCC: 10 licenses The controller comes with a single-seat download license.
Real-Time OS ( $\mu$ ITRON 3.0)		SW96RN2-ZCC: Object code can be freely copied SW96RNC-ZCC: Object code can be freely copied. With source code.

### Hardware Products Emulator

model 25 system	Controller	BM1055R0C
	Emulation pod	BM91CW40F0A-M15
model 15 system	Controller	BM1040R0B-G
	Emulation pod	BM91CW40F0A-M15

\* Select either model 25 system or model 15 system.

\* As to hardware products, additional accessories are also needed.

» For further information about Toshiba microcomputers and Toshiba microcomputer development systems, please visit <http://www.semicon.toshiba.co.jp/eng/product/micro/index.html>

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