

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX126F, TC74LCX126FK

Low-Voltage Quad Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX126 is a high-performance CMOS quad bus buffers. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

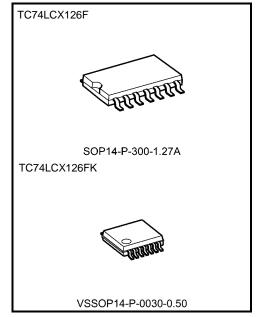
The device is designed for low-voltage (3.3 V) $V_{\rm CC}$ applications, but it could be used to interface to 5-V supply environment for inputs.

This device requires the 3-state control input (OE) to be set high to place the output into the high impedance state.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 6.0 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Available in JEDEC SOP, JEITA SOP, VSSOP (US)
- · Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 126 type



Weight

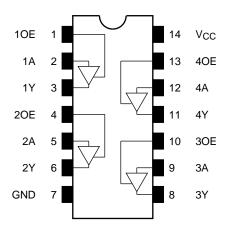
SOP14-P-300-1.27A : 0.18 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Note: The Electrical Characteristics of V_{CC} = 1.8 ± 0.15 V is only applicable for products which manufactured from January 2009 onward.

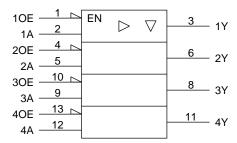
Start of commercial production 1994-10



Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs
OE	А	Υ
L	Х	Z
Н	L	L
Н	Н	Н

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5 to 7.0	V
DC input voltage	VIN	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	lık	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC Vcc/ground current	ICC/IGND	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating range (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: Vout < GND, Vout > Vcc



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Device overhoveltene	\/	1.65 to 3.6	.,	
Power supply voltage	Vcc	1.5 to 3.6 (Note 2)	V	
Input voltage	VIN	0 to 5.5	V	
Output valtage	Vout	0 to 5.5 (Note 3)	V	
Output voltage	VOUT	0 to Vcc (Note 4)		
Output ourropt	lou/lou	±24 (Note 5)	mA	
Output current	IOH/IOL	±12 (Note 6)	MA	
Operating temperature	Topr	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

- Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

 Unused inputs must be tied to either VCC or GND.
- Note 2: Data retention only Note 3: Output in OFF state
- Note 4: High or low state Note 5: VCC = 3.0 to 3.6 V Note 6: VCC = 2.7 to 3.0 V
- Note 7: VIN = 0.8 to 2.0 V, VCC = 3.0 V



Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteris	otion	Symbol	Toot Cond	Test Condition Vcc (V)		ondition Min		May	Unit	
Characteris	SIICS	Symbol	rest Cond			IVIII I	Max	Offic		
							1.65 to 2.3	V _{CC} × 0.9	_	
	H-level	VIH	_	— <u> </u>		1.7 —				
Input voltage					2.7 to 3.6	2.0		V		
input voitage					1.65 to 2.3	_	Vcc × 0.1	V		
	L-level	VIL	_		2.3 to 2.7		0.7			
					2.7 to 3.6	_	0.8			
				IOH = -100 μA	1.65 to 3.6	Vcc-0.2				
				I _{OH} = -4 mA	1.65	1.05	_			
	H-level	Vau	Mar Mar or Ma	I _{OH} = -8 mA	2.3	1.7	_	V		
	n-ievei	Voн	VIN = VIH or VIL	IOH = -12 mA	2.7	2.2	_			
				I _{OH} = -18 mA	3.0	2.4	_			
Output valtage				IOH = -24 mA	3.0	2.2	_			
Output voltage				I _O L = 100 μA	1.65 to 3.6	_	0.2			
				I _{OL} = 4 mA	1.65	_	0.45			
	Llovel			IOL = 8 mA	2.3	_	0.7			
	L-level	VoL	VIN = VIH or VIL	I _{OL} = 12 mA	2.7	_	0.4			
				I _{OL} = 16 mA	3.0	_	0.4			
				I _{OL} = 24 mA	3.0	_	0.55			
Input leakage currer	leakage current I _{IN} V _{IN} = 0 to 5.5 V		V _{IN} = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μΑ		
3-state output OFF	tput OFF state current I _{OZ}		V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μА		
Power-off leakage c	er-off leakage current lOFF		$V_{IN}/V_{OUT} = 5.5 V$		0	_	10.0	μА		
Out a seed assemble		laa	V _{IN} = V _{CC} or GND		1.65 to 3.6		10.0			
Quiescent supply cu	ment	Icc	V _{IN} /V _{OUT} = 3.6 to 5.5 V		1.65 to 3.6	_	±10.0	μΑ		
Increase in Icc per in	nput	Δlcc	VIH = VCC - 0.6 V (per	r 1 input)	2.7 to 3.6		500			



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition		Min	Max	Unit
			V _{CC} (V)		22.2	
			1.8 ± 0.15	_	20.0	
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.5 ± 0.2	_	7.5	ns
Tropagation delay time	tpHL	rigure 1, rigure 2	2.7	_	6.5	113
			3.3 ± 0.3	1.5	6.0	
					30.0	
Output anable time	t _P ZL tPZH	Figure 1, Figure 3	2.5 ± 0.2		15.0	ns
Output enable time			2.7		8.0	
			3.3 ± 0.3	1.5	7.0	
			1.8 ± 0.15		28.0	
Output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 3	2.5 ± 0.2		14.0	ns
Output disable time			2.7		7.0	115
			3.3 ± 0.3	1.5	6.0	
Output to output skew	tosLH	(Note)	2.7	_	_	ns
	tosHL	(INOIE)	3.3 ± 0.3	_	1.0	

Note: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic VOL	VOLP	V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	V
Quiet output minimum dynamic VoL	Volv	V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Vcc (V)	Тур.	Unit
Input capacitance	CIN	_	3.3	7	pF
Output capacitance	Cout	_	3.3	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (No	e) 3.3	25	pF

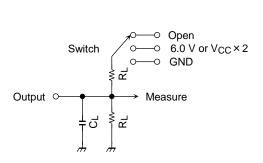
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/4 (per gate)



AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
tpLZ, tpZL	6.0 V @ V _{CC} = 3.3 ± 0.3 V @ V _{CC} = 2.7 V		
بهدی بهدد	V _{CC} × 2 @ V _{CC} = 2.5 ± 0.2 V @ V _{CC} = 1.8 ± 0.15 V		
tpHZ, tpZH	GND		

Figure 1



AC Waveform

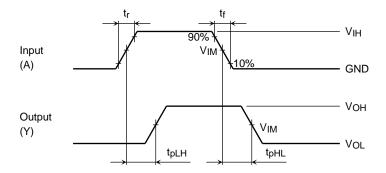


Figure 2 t_{pLH} , t_{pHL}

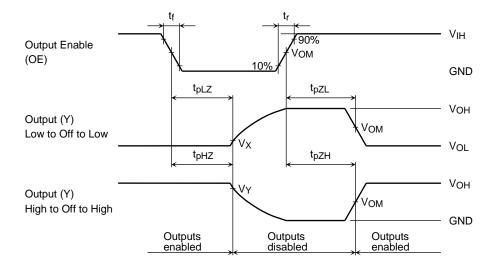


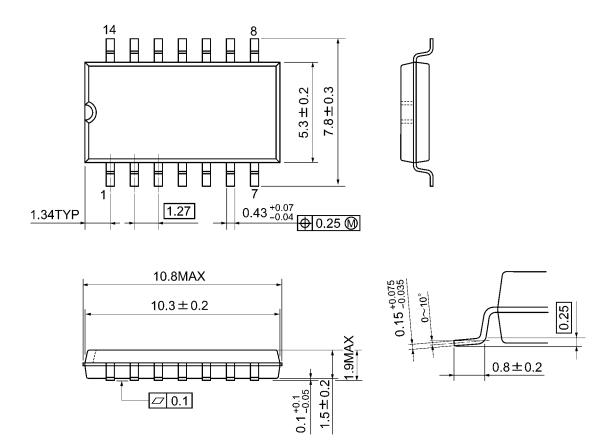
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

			Vcc	
	Symbol	$3.3 \pm 0.3 \text{ V}$ 2.7 V	2.5 ± 0.2 V	1.8 ± 0.15 V
Input	VIH	2.7 V	Vcc	Vcc
	VIM	1.5 V	Vcc/2	V _{CC} /2
	t _r , t _f	2.5 ns	2.0 ns	2.0 ns
Output	Vом	1.5 V	V _{OH} /2	V _{OH} /2
	Vx	VoL +0.3 V	VoL +0.15 V	VoL +0.15 V
	VY	V _{OH} -0.3 V	V _{OH} -0.15 V	V _{OH} -0.15 V
Load	CL	50 pF	30 pF	30 pF
	R_{L}	500 Ω	500 Ω	1 kΩ



Package Dimensions

SOP14-P-300-1.27A Unit: mm

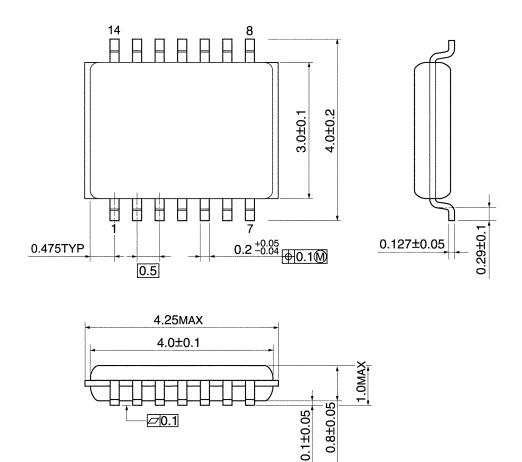


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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