

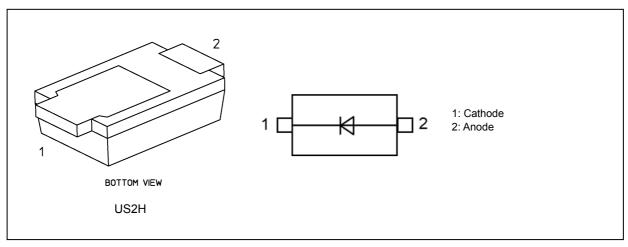
Schottky Barrier Diode Silicon Epitaxial

CUHS10F60

1. Applications

· High-Speed Switching

2. Packaging and Internal Circuit



3. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Reverse voltage	V _R		60	V
Average rectified current	I _O	(Note 1)	1.0	Α
Non-repetitive peak forward surge current	I _{FSM}	(Note 2)	10	Α
Junction temperature	Tj		150	°C
Storage temperature	T _{stg}		-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ mm}, \text{Cu Pad: } 645 \text{ mm}^2)$

Note 2: Pulse width 10 ms

4. Thermal Characteristics

Characteristics		Symbol	Max	Unit
Junction-to-ambient thermal resistance	(Note 1)	R _{th(j-a)}	105	°C/W

Note 1: Mounted on an FR4 board (25.4 mm \times 25.4 mm \times 1.6 mm, Cu pad: 645 mm²)

Start of commercial production

2018-04



5. Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _F (1)	(Note 1)	I _F = 100 mA	_	0.36	0.41	V
	V _F (2)		I _F = 500 mA	_	0.46	0.51	
	V _F (3)		I _F = 1 A	_	0.56	0.62	
Reverse current	I _R (1)	(Note 1)	V _R = 10 V	_	0.7	_	μА
	I _R (2)		V _R = 60 V	_	6.0	40	
Total capacitance	Ct		V _R = 0 V, f = 1 MHz	_	130	_	pF

Note 1: Pulse measurement.

6. Marking

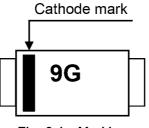


Fig. 6.1 Marking

Marking Code	Part Number	
9G	CUHS10F60	

7. Usage Considerations

Schottky barrier diodes (SBDs) have reverse leakage greater than other types of diodes. This makes SBDs
more susceptible to thermal runaway under high-temperature and high-voltage conditions. Thus, both
forward and reverse power losses of SBDs should be considered for thermal and safety design.

8. Land Pattern Dimensions (for reference only)

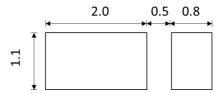
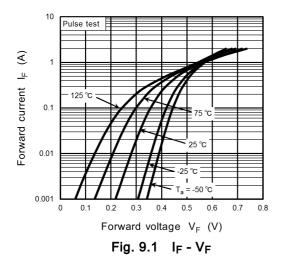


Fig. 8.1 Land Pattern Dimensions for Reference Only (Unit: mm)



9. Characteristics Curves (Note)



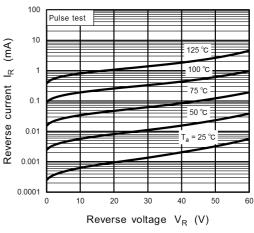


Fig. 9.2 I_R - V_R

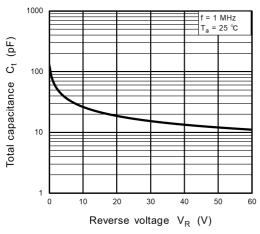


Fig. 9.3 $C_t - V_R$

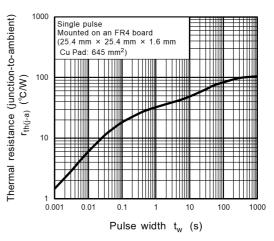


Fig. 9.4 r_{th} - t_w

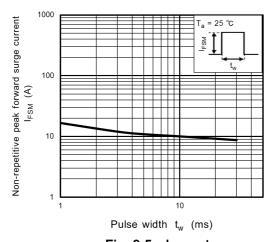


Fig. 9.5 I_{FSM} - t_w

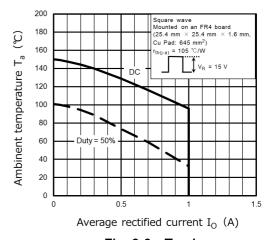


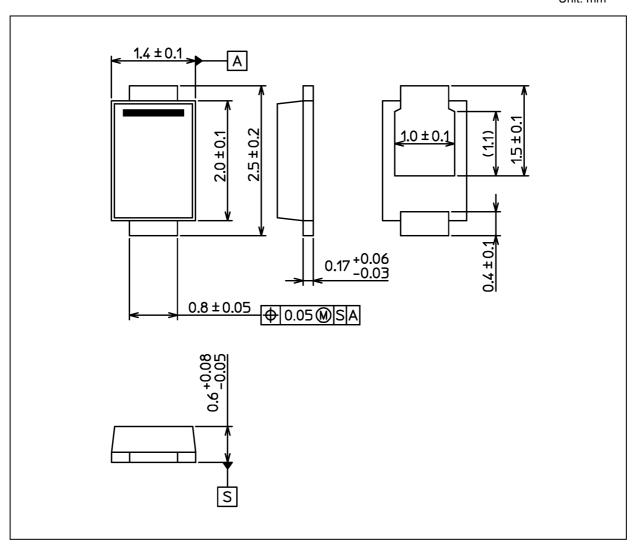
Fig. 9.6 $T_a - I_O$

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 5.4 mg (typ.)

	Package Name(s)
Nickname: US2H	



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