TOSHIBA
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November, 2017

Datasheet Correction : TMPM375FSDMG

We would like to inform customers the corrections of the following datasheets. If you have any questions or require any further information, please contact your local sales office.

1. Products

   TMPM375FSDMG

2. Data sheet

   TMPM375FSDMG_datasheet_en_20141126.pdf

3. Correction

   [Correction(1)]
   Table 1-3 ··· Page-8
   before)

Pin Name	Pin No.
VOUT15	**19**
VOUT3	**17**

   after)

Pin Name	Pin No.
VOUT15	**24**
VOUT3	**22**

   [Correction(2)]
   Tabel 13-2 ··· Page 315
   Tabel 13-3 ··· Page 322
   Tabel 13-4 ··· Page 326
   Tabel 13-5 ··· Page 330
   Pin Name of ADC Unit B
   before)

0_1010	: AINA10

   after)

0_1010	: AINB10

[Correction(3)]

19.2.2 ▪▪▪ Page 504

Value of after reset(VDCR<VDLVL>)

before)

	VDLVL		VDEN
	0	**0**	0

after)

	VDLVL		VDEN
	0	1	0

[Correction(4)]

20.3.3 ▪▪▪ Page 518

before)

・ In case of rounding down OFDMXPLLON/OFDMXPLLOFF and **rouding down** OFDMNPLLON/OFDMNPLLOFF
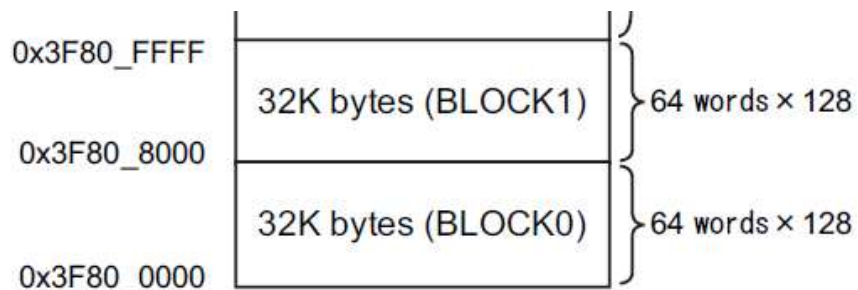
after)

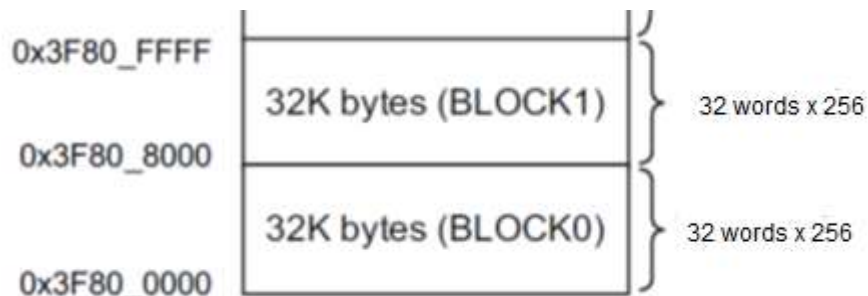・ In case of rounding down OFDMXPLLON/OFDMXPLLOFF and **rounding up** OFDMNPLLON/OFDMNPLLOFF

[Correction(5)]

Figure 22-1 ▪▪▪ Page 531

before)

after)

[Correction(6)]

22.3.3 Restrictions on Internal Memories ▪▪▪ Page 551

Table 22-11 Content of Lower box

before)

Internal flash memory	The following addresses are assigned for storing software ID information and **passowerds**. Storing program in below addresses is not recommendable. **0x3F81_FFF0 to 0x3F81_FFFF**

after)

Internal flash memory	The following addresses are assigned for storing software ID information and **passwords**. Storing program in below addresses is not recommendable. **0x3F80_FFF0 to 0x3F80_FFFF**

[Correction(7)]

22.3.6 Trasnfer Format at RAM Transfer ▪▪▪ Page 558

Information of Password area in the tabel

before)

5to16	C→T	Password data (12-byte) **0x3F80_FF04 to 0x3F80_FF0F**	(Omitted) receive data with **0x3F81_FFF0 to 0x3F81_FFFF** of (the rest omitted)

after)

5to16	C→T	Password data(12-byte)	(Omitted) receive data with **0x3F80_FFF4 to 0x3F80_FFFF** of (the rest omitted)

[Correction(8)]

22.3.7 Transfer Format of Flash memory Chip Erase and Protect Bit Erase ▪▪▪ Page 560

Information of Password area in the tabel

before)

5to16	C→T	Password data (12-byte) **0x3F80_FF04 ～ 0x3F80_FF0F**	(Omittted) receive data with **0x3F80_FFF0 to 0x3F80_FFFF** o (the rest omitted)

after)

5to16	C→T	Password data (12-byte)	(Omittted) receive data with **0x3F80_FFF4 to 0x3F80_FFFF** o (the rest omitted)