TOSHIBA

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MICROCONTROLLER

VECTOR ENGINE (VE) Toshiba original motor control technology

In motor control technology, vector control which requires complicated high-speed calculation and high-level software developments is a new trend.

A new Toshiba original vector engine accomplishes easy and low-cost vector control. The vector engine is a coprocessor exclusive for motor control. The vector engine executes the typical calculation including transformation from a three-phase motor current to a two-phase, and transformation/counter transformation of the rotational coordinates. These functions reduce a CPU utilization of the software while user-specified position estimation and speed control, which varies depending on the system configuration and control method, are executed by software.

The task of the vector engine can be selected up to 16 types. With combining the vector engine to users' system, a high level of flexibility in motor control can be achieved.



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APPLICATIONS

- Washing machines
- Air conditioners
- Refrigerators
- Pumps Industrial motors
- Other rotating devices

*PMD: Programmable Motor Driver

FEATURES ADVANTAGES BENEFITS Since the resources of the CPU can Reduction of the CPU overhead. Since the motor process time is be devoted to PFC*, sensor reduced, a commanding share of the CPU held by the software is Coprocessor exclusive for processing, or communication decreased by 72% when two motors are operating. motor control systems, system performance can 1) Another process can be handled. be improved. 2) High-speed PWM carrier frequency can be used. With a high speed PWM frequency, quiet and low-vibration operation Supporting various As a coprocessor the vector engine has Toshiba original scheduling function. It select tasks and their combinations. can be possible. scheduling Combining with users' software, Many tasks are prepared The impact of the difference between development environments various operation can be allowed. for various types of can be reduced. Short development terms can be calculation Compile options are not susceptible. achieved. * PFC: Power Factor Correction

PRODUCT LINEUP

型番	Freq. (MHz)	ROM Size (KB)	RAM Size (KB)	PMD* (ch)	Package	Features	型番	Freq. (MHz)	ROM Size (KB)	RAM Size (KB)	PMD* (ch)	Package	Features
TMPM370FYFG	80	256	10	2	LQFP100 (14 x 14 mm)	Built-n various analog circuit. System cost can be reduced.	TMPM37AFSQG	40	64	1	1	VQFN32 (5 x 5 mm)	Pre-driver for 3-phase sine wave drive
TMPM370FYDFG	80	256	10	2	QFP100 (14 x 20 mm)			400	= 10			LQFP100 (14 x 14 mm)	Two units of the vector engine, Built-in CAN controller
TMPM372FWUG	80	128	6	1	LQFP64 (10 x 10 mm)	Small scale pin package	TMPM470FDFG	120	512	34	2		
TMPM373FWDUG	80	128	6	1	LQFP48 (7 x 7 mm)		TMPM470FZFG	120	384	34	2		
TMPM374FWUG	80	128	6	1	LQFP44 (10 x 10 mm)		TMPM470FYFG	120	256	18	2		
TMPM375FSDMG	40	64	4	1	SSOP30 (5.6 x 9.7 mm)		TMPM475FDFG	120	512	34	2		Two units of the vector engine, Built-in CAN controller CAN controller
TMPM376FDFG	80	512	32	2	LQFP100 (14 x 14 mm)	Built-in I ² C	TMPM475FZFG	120	384	34	2		
TMPM376FDDFG	80	512	32	2	QFP100 (14 x 20 mm)		TMPM475FYFG	120	256	18	2		

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ADVANTAGE: REMARKABLY SHORTENED MOTOR CONTROL PROCESSING TIME



ADVANTAGE: FLEXIBLE SCHEDULING BY THE COPROCESSOR VECTOR ENGINE

Position estimation and speed control, which vary depending on the system configuration and control method, are left to software processing. The vector engine handles the typical calculation including transformation from a three-phase motor current to a two-phase, or transformation/counter transformation of the rotational coordinates. The tasks of the vector engine are configured as a schedule. The combination of the tasks is up to 16 types. These various types of scheduling provide a high level of flexibility in motor control and the vector engine can support many types of motor operation.



Good new for Software developers. Reduction of Developers' load

Since the typical calculation is handled by the vector engine, software programs can be reduced. Therefore, the amount of the program to be complied can be reduced. It means that the impact of the compiler performance or optimization options can be reduced.





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Note: The above data is not guaranteed value but reference data.

Exec. Time

40

[µs]

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