TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

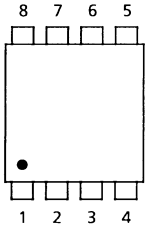
TA4101F

UHF VHF MIX Application

Features

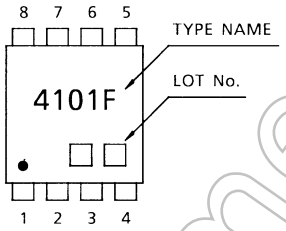
- Double balance circuit

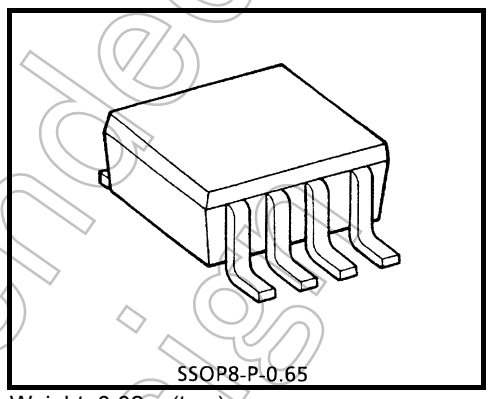
Pin Assignment (top view)



1. IF OUT
2. V_{CC}
3. OSC IN
4. Base
5. Base
6. Base
7. GND
8. Collector

Marking





Weight: 0.02 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V_{CC}	6	V
Total power dissipation	P_D (Note 1)	300	mW
Operating temperature	T_{opr}	−40 to 85	°C
Storage temperature range	T_{stg}	−55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: When mounted on the glass epoxy board of 2.5 cm^2 × 1.6 t

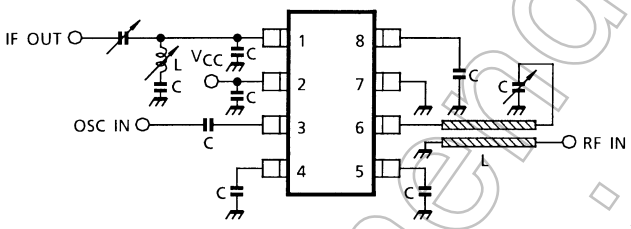
Start of commercial production
1992-11

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Circuit current	I_{CC}	—	V_{CC} = 5 V	3.9	5.7	7.5	mA
MIXER gain	G_{MIX}	1	V_{CC} = 5 V (Note 2)	−6.0	−3.5	—	dB
MIXER noise figure	NF_{MIX}	1	V_{CC} = 5 V (Note 2)	—	9.0	12.0	dB
Maximum output level	P_O	1	V_{CC} = 5 V (Note 2)	−12	−9	—	dBmW

Note 2: f_{RF} = 800 MHz, f_{LO} = 860 MHz (0dBm), f_{IF} = 60 MHz

Measurement Circuit 1



Notice

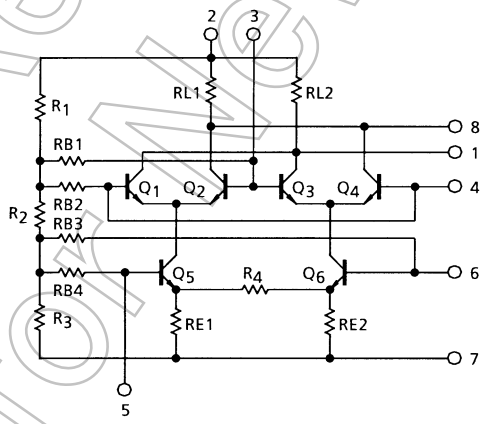
The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions.

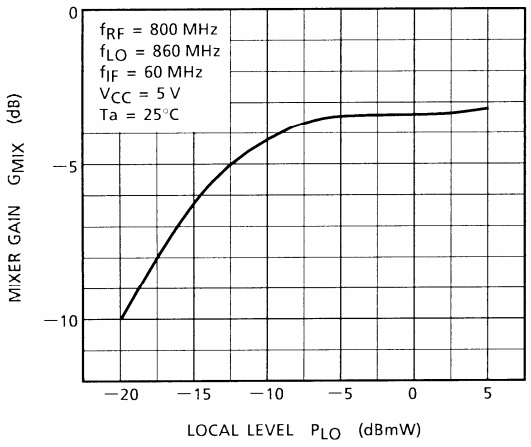
It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

TOSHIBA assume no responsibility for the integrity of customer circuit designs or applications.

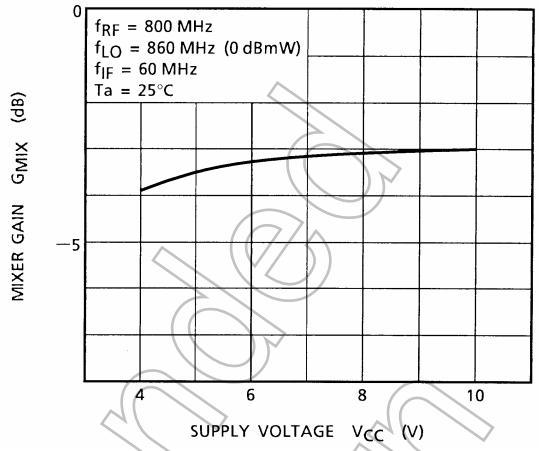
Equivalent Circuit



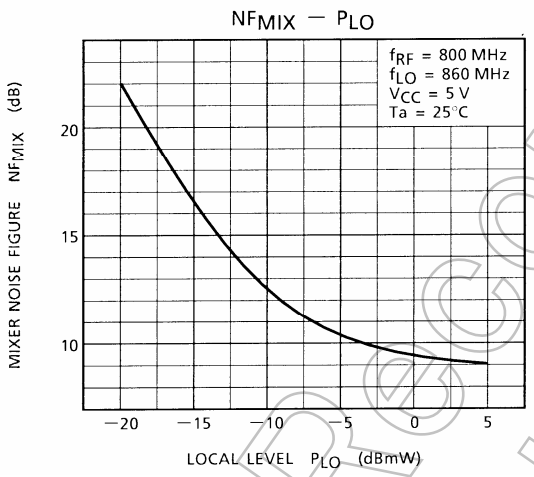
G_{MIX} — P_{LO}



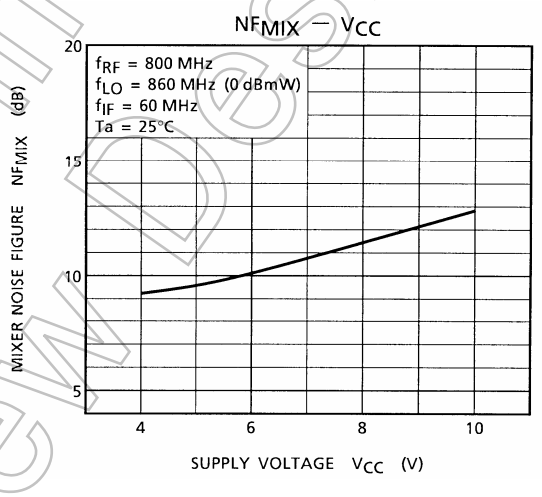
G_{MIX} — V_{CC}



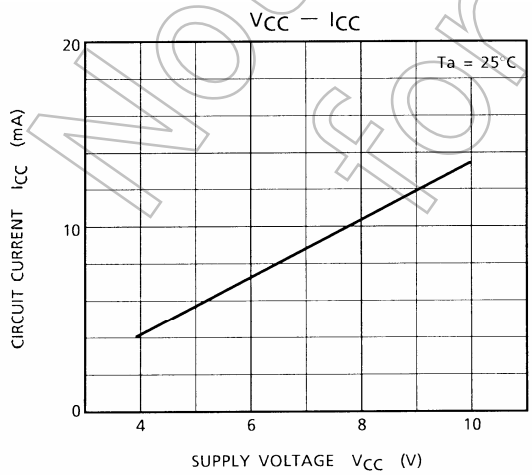
NF_{MIX} — P_{LO}



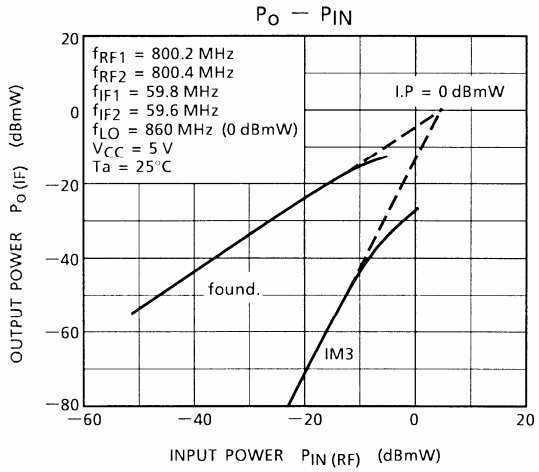
NF_{MIX} — V_{CC}



V_{CC} — I_{CC}

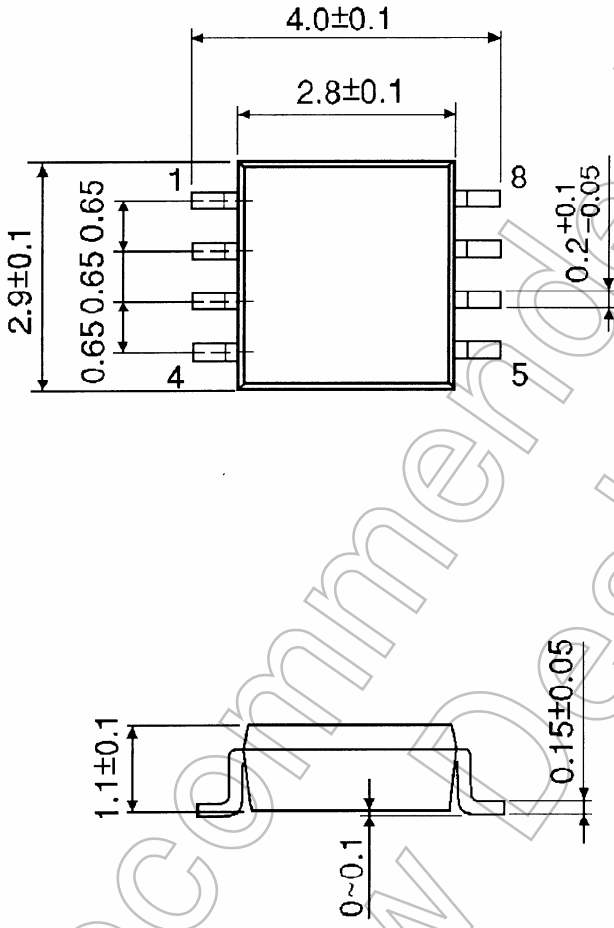


P_O — P_{IN}



Package Dimensions

SSOP8-P-0.65 Unit : mm



Weight : 0.02 g (Typ.)

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