

# TLP2261

## 1. Applications

- Factory Networking
- High-Speed Digital Interfacing for Instrumentation and Control Devices
- I/O Interface Boards

## 2. General

The Toshiba TLP2261 consists of a high-output infrared LED coupled with a high-speed photo-diode-transistor chip. It is housed in the SO8L package. It is housed in a thin SO8L package of 2.35 mm (max). The TLP2261 guarantees isolation voltage 5000 Vrms and compliant with international safety standards for reinforced insulation. It has two circuits built into a single SO8L package, which can reduce the mounting area.

This photocoupler guarantees operation at up to 125 °C and on supplies from 2.7 V to 5.5 V. Since TLP2261 guarantees 2 mA low supply current ( $I_{DDI}/I_{DDH}$ ), and 1.6 mA ( $T_a = 125\text{ °C}$ ) low threshold input current ( $I_{FHL}$ ), it contributes to energy saving of devices. It can be driven directly from a microcomputer by a low input current. The TLP2261 has an internal Faraday shield that provides a guaranteed common-mode transient immunity of  $\pm 20\text{ kV}/\mu\text{s}$ .

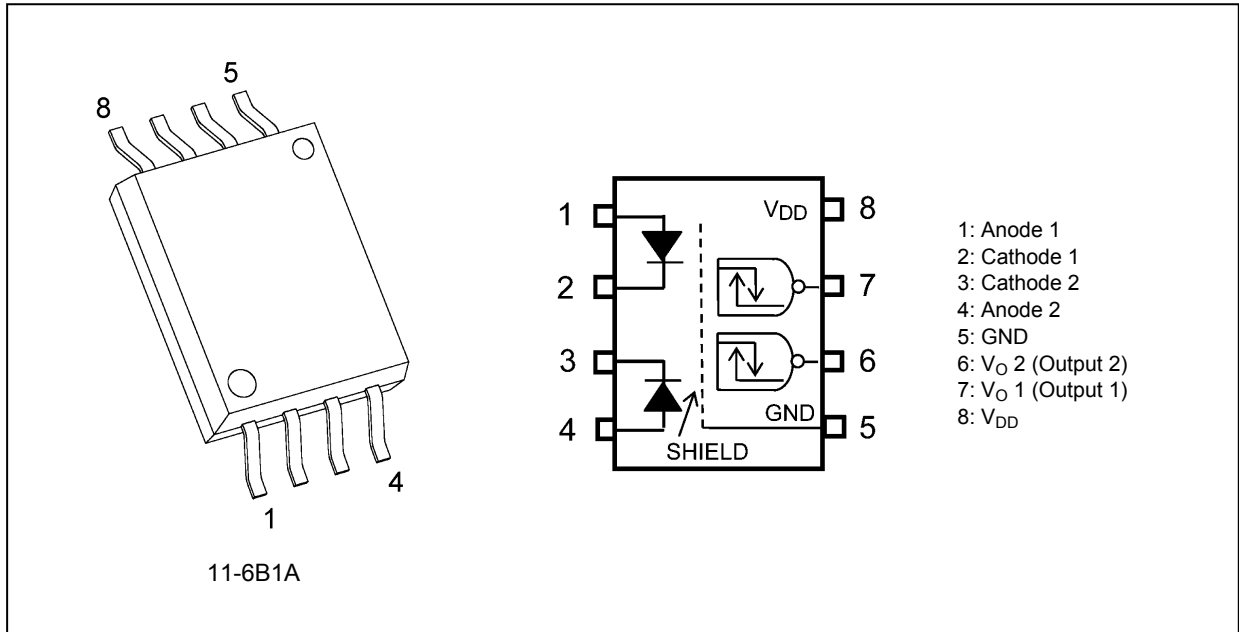
## 3. Features

- (1) Inverter logic type (totem pole output)
- (2) Package: SO8L
- (3) Operating temperature: -40 to 125 °C
- (4) Supply voltage: 2.7 to 5.5 V
- (5) Data transfer rate: 15 MBd (typ.) (NRZ)
- (6) Threshold input current: 1.3 mA (max) (@ $T_a = 105\text{ °C}$ )  
: 1.6 mA (max) (@ $T_a = 125\text{ °C}$ )
- (7) Supply current: 2 mA (max)
- (8) Common-mode transient immunity:  $\pm 20\text{ kV}/\mu\text{s}$  (min)
- (9) Isolation voltage: 5000 Vrms (min)
- (10) Safety standards
  - UL-recognized: UL 1577, File No.E67349
  - cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
  - VDE-approved: EN 60747-5-5, EN 62368-1 (**Note 1**)
  - CQC-approved: GB4943.1, GB8898 Japan Factory

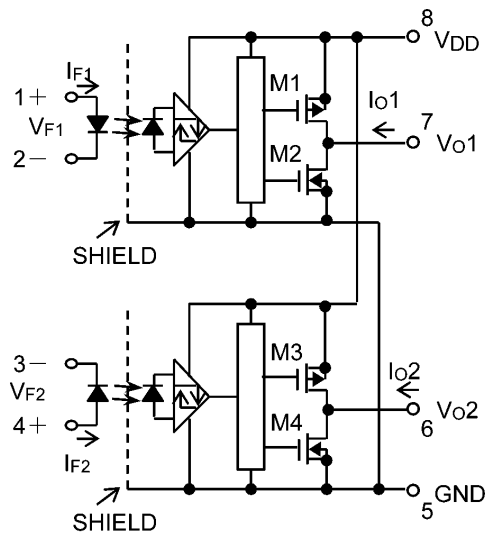
Note 1: When a VDE approved type is needed, please designate the **Option (D4)**.

Start of commercial production  
2016-08

## 4. Packaging and Pin Assignment



## 5. Internal Circuit (Note)



Note: A 0.1- $\mu$ F bypass capacitor must be connected between pin 8 and pin 5.

## 6. Principle of Operation

### 6.1. Truth Table

Input	LED1 (2)	M1 (3)	M2 (4)	Output 1 (2)
H	ON	OFF	ON	L
L	OFF	ON	OFF	H

### 6.2. Mechanical Parameters

Characteristics	Min	Unit
Creepage distances	8.0	mm
Clearance	8.0	
Internal isolation thickness	0.4	

## 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$ )

	Characteristics	Symbol	Note	Rating	Unit
LED	Input forward current	$I_F$	(Note 1)	10	mA
	Input forward current derating ( $T_a \geq 110\text{ }^\circ\text{C}$ )	$\Delta I_F/\Delta T_a$	(Note 1)	-0.4	mA/ $^\circ\text{C}$
	Input forward current (pulsed)	$I_{FP}$	(Note 1), (Note 2)	20	mA
	Input forward current derating (pulsed) ( $T_a \geq 110\text{ }^\circ\text{C}$ )	$\Delta I_{FP}/\Delta T_a$	(Note 1)	-0.8	mA/ $^\circ\text{C}$
	Peak transient input forward current	$I_{FPT}$	(Note 1), (Note 3)	1	A
	Peak transient input forward current derating ( $T_a \geq 110\text{ }^\circ\text{C}$ )	$\Delta I_{FPT}/\Delta T_a$	(Note 1)	-40	mA/ $^\circ\text{C}$
	Input power dissipation	$P_D$	(Note 1)	20	mW
	Input power dissipation derating ( $T_a \geq 110\text{ }^\circ\text{C}$ )	$\Delta P_D/\Delta T_a$	(Note 1)	-0.8	mW/ $^\circ\text{C}$
	Input reverse voltage	$V_R$	(Note 1)	5	V
Detector	Output current	$I_O$	(Note 1)	10	mA
	Output voltage	$V_O$	(Note 1)	6	V
	Supply voltage	$V_{DD}$		6	
	Output power dissipation	$P_O$	(Note 1)	40	mW
	Output power dissipation derating ( $T_a \geq 110\text{ }^\circ\text{C}$ )	$\Delta P_O/\Delta T_a$	(Note 1)	-1.6	mW/ $^\circ\text{C}$
Common	Operating temperature	$T_{opr}$		-40 to 125	$^\circ\text{C}$
	Storage temperature	$T_{stg}$		-55 to 125	
	Lead soldering temperature (10 s)	$T_{sol}$		260	
	Isolation voltage (AC, 60 s, R.H. $\leq 60\%$ )	$BV_S$	(Note 4)	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Each channel

Note 2: Pulse width (PW)  $\leq 1\text{ ms}$ , duty = 50 %

Note 3: Pulse width (PW)  $\leq 1\text{ }\mu\text{s}$ , 300 pps

Note 4: This device is considered as a two-terminal device: Pins 1, 2, 3 and 4 are shorted together, and pins 5, 6, 7 and 8 are shorted together.

## 8. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Typ.	Max	Unit
Input on-state current	$I_{F(ON)}$	(Note 1), (Note 2)	2	—	6	mA
Input off-state voltage	$V_{F(OFF)}$	(Note 1)	0	—	0.8	V
Supply voltage	$V_{DD}$	(Note 3)	2.7	3.3 / 5.0	5.5	
Operating temperature	$T_{opr}$	(Note 3)	-40	—	125	$^\circ\text{C}$

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this data sheet should also be considered.

Note: A ceramic capacitor (0.1  $\mu\text{F}$ ) should be connected between pin 8 and pin 5 to stabilize the operation of a high-gain linear amplifier. Otherwise, this photocoupler may not switch properly. The bypass capacitor should be placed within 1 cm of each pin.

Note 1: Each channel

Note 2: The rise and fall times of the input on-current should be less than 0.5  $\mu\text{s}$ .

Note 3: Denotes the operating range, not the recommended operating condition.

## 9. Electrical Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to $125$ °C, $V_{DD} = 2.7$ to $5.5$ V)

Characteristics	Symbol	Note	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Input forward voltage	$V_F$	(Note 1)		$I_F = 2$ mA, $T_a = 25$ °C	1.35	1.50	1.65	V
Input forward voltage temperature coefficient	$\Delta V_F / \Delta T_a$	(Note 1)		$I_F = 2$ mA	—	-2.0	—	mV/°C
Input reverse current	$I_R$	(Note 1)		$V_R = 5$ V, $T_a = 25$ °C	—	—	10	μA
Input capacitance	$C_t$	(Note 1)		$V = 0$ V, $f = 1$ MHz, $T_a = 25$ °C	—	20	—	pF
Low-level output voltage	$V_{OL}$	(Note 1)	Fig. 12.1.1	$I_F = 2$ mA, $I_O = 20$ μA	—	0.0016	0.1	V
				$I_F = 2$ mA, $I_O = 3.2$ mA	—	0.12	0.4	
High-level output voltage	$V_{OH}$	(Note 1)	Fig. 12.1.2	$V_F = 0.8$ V, $I_O = -20$ μA, $V_{DD} = 3.3$ V	3.2	3.29	—	
				$V_F = 0.8$ V, $I_O = -20$ μA, $V_{DD} = 5$ V	4.9	4.99	—	
				$V_F = 0.8$ V, $I_O = -3.2$ mA, $V_{DD} = 3.3$ V	2.3	3.15	—	
				$V_F = 0.8$ V, $I_O = -3.2$ mA, $V_{DD} = 5$ V	4.0	4.87	—	
Low-level supply current	$I_{DDL}$		Fig. 12.1.3	$I_{F1} = I_{F2} = 2$ mA	—	1.3	2	mA
High-level supply current	$I_{DDH}$		Fig. 12.1.4	$I_{F1} = I_{F2} = 0$ mA	—	1.3	2	
Threshold input current (H/L)	$I_{FHL}$	(Note 1)		$I_O = 3.2$ mA, $V_O < 0.4$ V, $T_a = -40$ to $105$ °C	—	0.5	1.3	
				$I_O = 3.2$ mA, $V_O < 0.4$ V, $T_a = -40$ to $125$ °C	—	0.5	1.6	

Note: All typical values are at  $V_{DD} = 5$  V,  $T_a = 25$  °C, unless otherwise noted.

Note 1: Each channel

## 10. Isolation Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
Total capacitance (input to output)	$C_S$	(Note 1)	$V = 0$ V, $f = 1$ MHz	—	0.8	—	pF
Isolation resistance	$R_S$	(Note 1)	$V = 500$ V, R.H. $\leq 60$ %	$10^{12}$	$10^{14}$	—	Ω
Isolation voltage	$BV_S$	(Note 1)	AC, 60 s	5000	—	—	Vrms

Note 1: This device is considered as a two-terminal device: Pins 1, 2, 3 and 4 are shorted together, and pins 5, 6, 7 and 8 are shorted together.

## 11. Switching Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to $125$ °C, $V_{DD} = 2.7$ to $5.5$ V)

Characteristics	Symbol	Note	Test Circuit	Test Condition	Min	Typ.	Max	Unit		
Propagation delay time (H/L)	$t_{pHL}$	(Note 1), (Note 3)	Fig. 12.1.5	$V_{IN} = 3.3$ V, $R_T = 820$ $\Omega$ , $C_L = 15$ pF	—	54	80	ns		
Propagation delay time (L/H)	$t_{pLH}$				—	45	80			
Pulse width distortion	$ t_{pHL} - t_{pLH} $				—	9	25			
Propagation delay skew (device to device)	$t_{psk}$	(Note 1), (Note 2), (Note 3)		—	—	30				
Propagation delay time (H/L)	$t_{pHL}$	(Note 1), (Note 3)		Fig. 12.1.6	$V_{IN} = 5$ V, $R_T = 1.68$ k $\Omega$ , $C_L = 15$ pF	—	51		80	kV/ $\mu$ s
Propagation delay time (L/H)	$t_{pLH}$					—	43		80	
Pulse width distortion	$ t_{pHL} - t_{pLH} $					—	8		25	
Propagation delay skew (device to device)	$t_{psk}$	(Note 1), (Note 2), (Note 3)			—	—	30			
Fall time	$t_f$	(Note 1), (Note 3)			$V_{IN} = 0 \rightarrow 5$ V, $R_T = 1.68$ k $\Omega$ , $C_L = 15$ pF	—	3		—	
Rise time	$t_r$		$V_{IN} = 5 \rightarrow 0$ V, $R_T = 1.68$ k $\Omega$ , $C_L = 15$ pF			—	3	—		
High-level common-mode transient immunity	$CM_H$	(Note 3)	Fig. 12.1.6		$I_F = 0$ mA, $R_T = 820$ $\Omega$ / $1.68$ k $\Omega$ , $V_{CM} = 1000$ V <sub>p-p</sub> , $T_a = 25$ °C	$\pm 20$	$\pm 45$	—		
Low-level common-mode transient immunity	$CM_L$					$I_F = 2$ mA, $R_T = 820$ $\Omega$ / $1.68$ k $\Omega$ , $V_{CM} = 1000$ V <sub>p-p</sub> , $T_a = 25$ °C	$\pm 20$	$\pm 45$	—	

Note: All typical values are at  $V_{DD} = 5$  V,  $T_a = 25$  °C, unless otherwise noted.

Note: Each channel

Note 1:  $f = 5$  MHz, duty = 50 %, input current  $t_r = t_f =$  less than 5 ns,  $C_L$  is less than 15 pF which includes probe and stray wiring capacitance.

Note 2: The propagation delay skew,  $t_{psk}$ , is equal to the magnitude of the worst-case difference in  $t_{pHL}$  and/or  $t_{pLH}$  that will be seen between units at the same given conditions (supply voltage, input current, temperature, etc.).

Note 3:  $R_T = R_1 + R_2 = 820$   $\Omega$  /  $1.68$  k $\Omega$

Recommendation input resistance conditions :  $R_1 = R_2 = 420$   $\Omega$  /  $840$   $\Omega$

## 12. Test Circuits and Characteristics Curves

### 12.1. Test Circuits

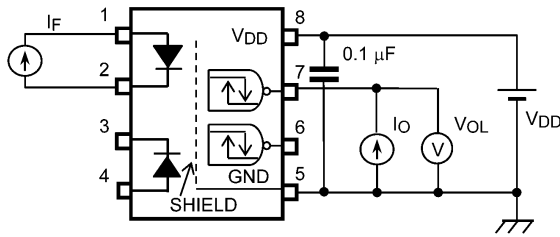


Fig. 12.1.1 VOL Test Circuit

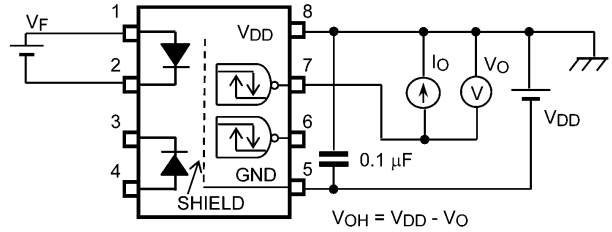


Fig. 12.1.2 VOH Test Circuit

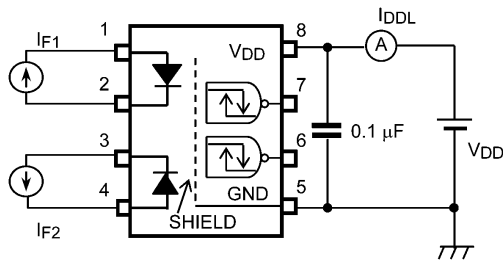


Fig. 12.1.3 IDDL Test Circuit

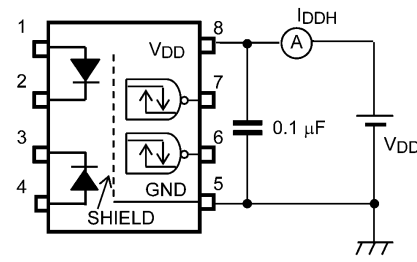
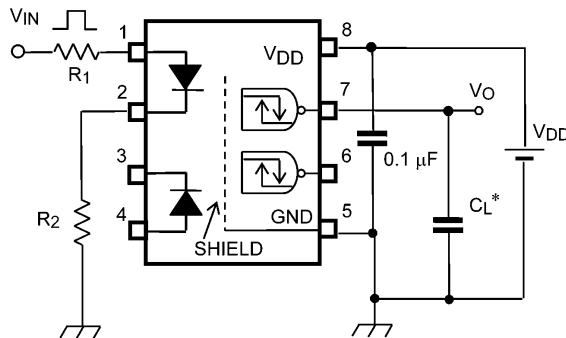


Fig. 12.1.4 IDDH Test Circuit

$V_{IN} = 3.3\text{ V} / 5\text{ V (P.G.)}$

$(f = 5\text{ MHz, duty} = 50\%, \text{less than } t_r = t_f = 5\text{ ns})$



\*CL is approximately 15 pF which includes probe and stray wiring capacitance.

P.G.: Pulse generator

$R_T = R_1 + R_2$

Fig. 12.1.5 Switching Time Test Circuit and Waveform

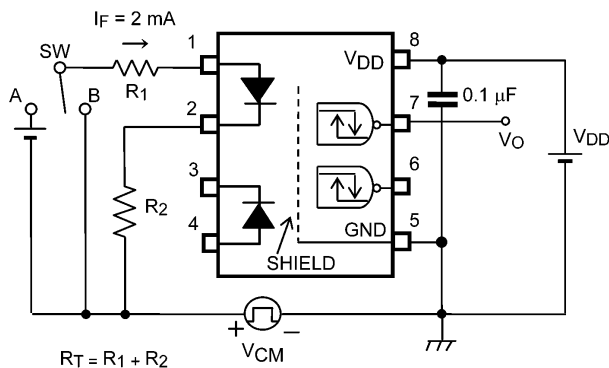
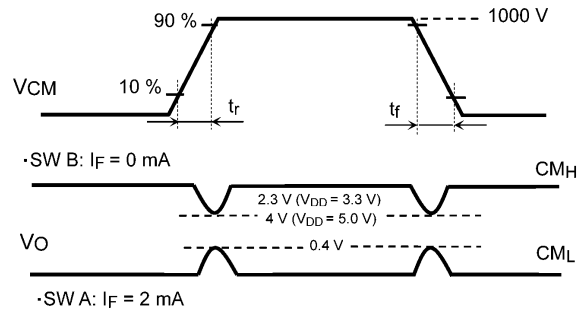
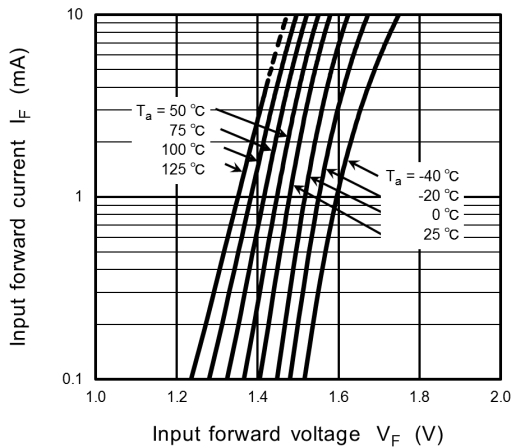


Fig. 12.1.6 Common-Mode Transient Immunity Test Circuit and Waveform

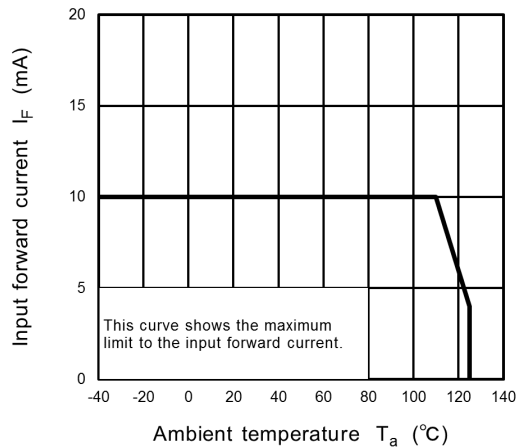


$$CM_H = \frac{800(V)}{t_r(\mu s)} \text{ or } \frac{800(V)}{t_f(\mu s)} \quad CM_L = \frac{800(V)}{t_r(\mu s)} \text{ or } \frac{800(V)}{t_f(\mu s)}$$

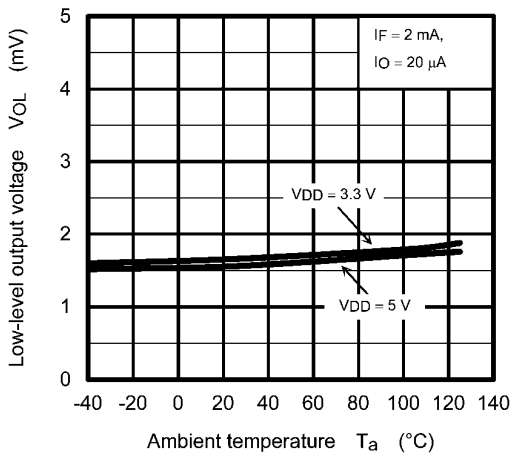
## 12.2. Characteristics Curves (Note)



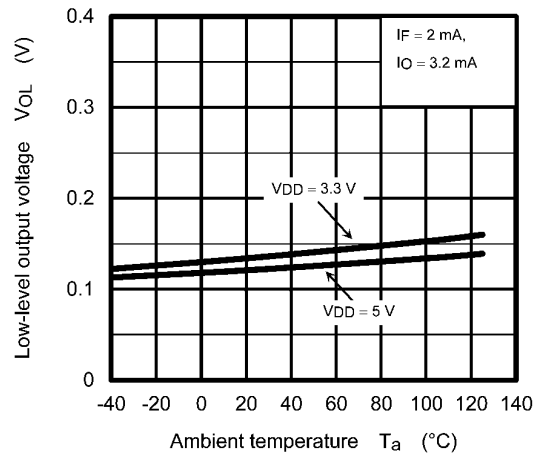
**Fig. 12.2.1  $I_F - V_F$**



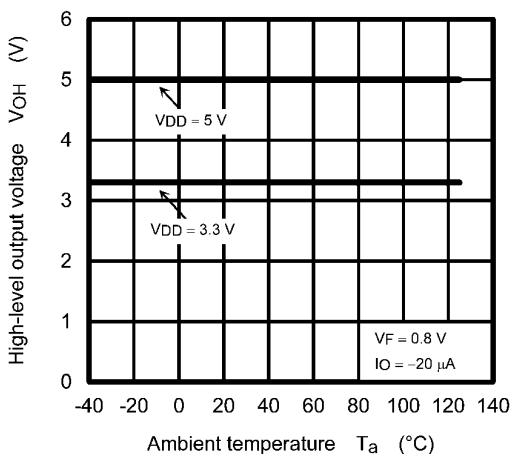
**Fig. 12.2.2  $I_F - T_a$**



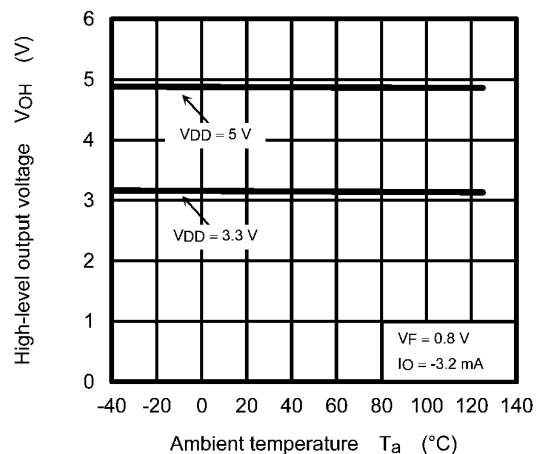
**Fig. 12.2.3  $V_{OL} - T_a$**



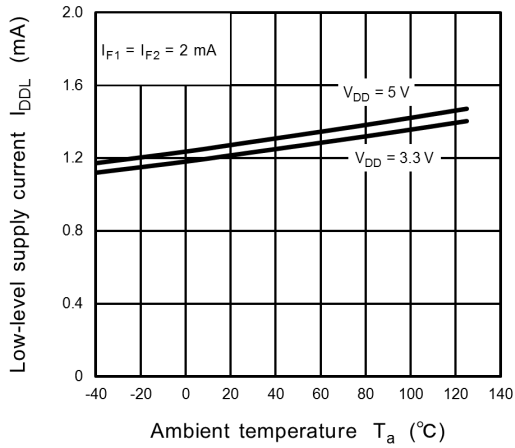
**Fig. 12.2.4  $V_{OL} - T_a$**



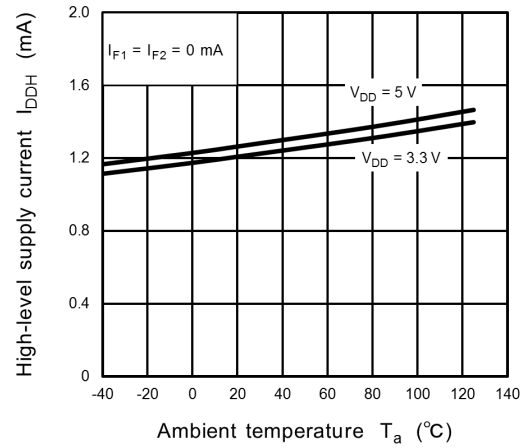
**Fig. 12.2.5  $V_{OH} - T_a$**



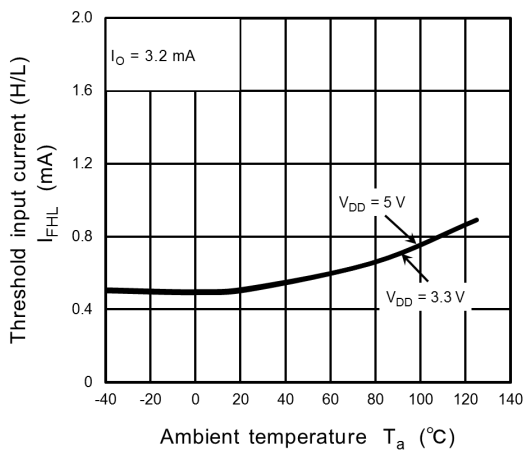
**Fig. 12.2.6  $V_{OH} - T_a$**



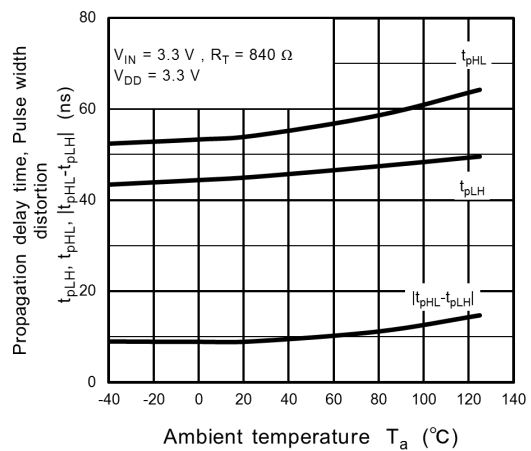
**Fig. 12.2.7**  $I_{DDL} - T_a$



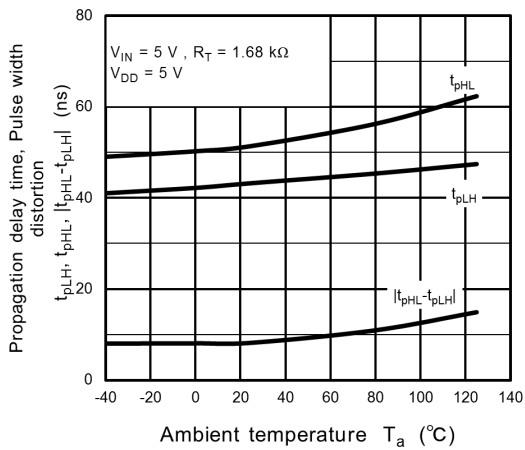
**Fig. 12.2.8**  $I_{DDH} - T_a$



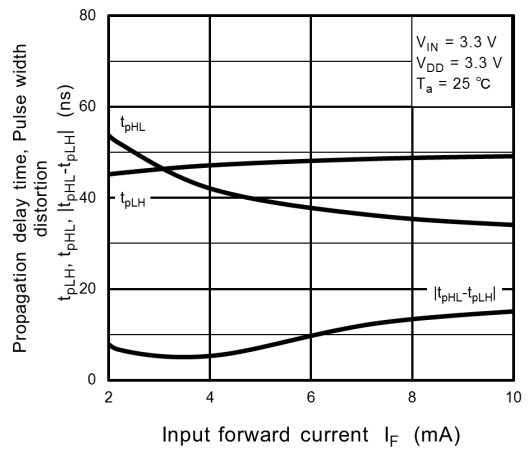
**Fig. 12.2.9**  $I_{FHL} - T_a$



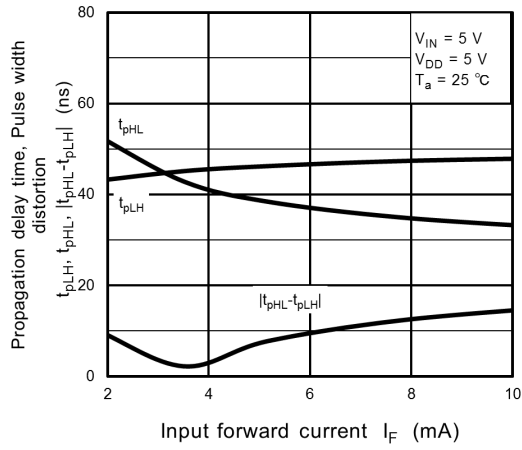
**Fig. 12.2.10**  $t_{pLH}, t_{pHL}, |t_{pHL} - t_{pLH}| - T_a$



**Fig. 12.2.11**  $t_{pLH}, t_{pHL}, |t_{pHL} - t_{pLH}| - T_a$



**Fig. 12.2.12**  $t_{pLH}, t_{pHL}, |t_{pHL} - t_{pLH}| - I_F$



**Fig. 12.2.13**  $t_{pLH}$ ,  $t_{pHL}$ ,  $|t_{pHL} - t_{pLH}| - I_F$

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

## 13. Soldering and Storage

### 13.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

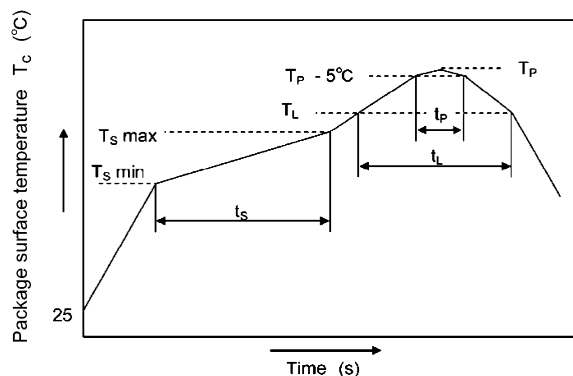
- When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	$T_S$	150	200	°C
Preheat time	$t_S$	60	120	s
Ramp-up rate ( $T_L$ to $T_P$ )			3	°C/s
Liquidus temperature	$T_L$	217		°C
Time above $T_L$	$t_L$	60	150	s
Peak temperature	$T_P$		260	°C
Time during which $T_c$ is between ( $T_P - 5$ ) and $T_P$	$t_P$		30	s
Ramp-down rate ( $T_P$ to $T_L$ )			6	°C/s

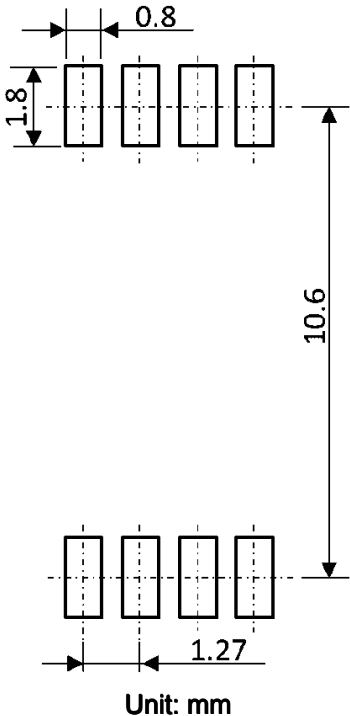
**An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used**

- When using soldering flow  
Preheat the device at a temperature of 150 °C (package surface temperature) for 60 to 120 seconds.  
Mounting condition of 260 °C within 10 seconds is recommended.  
Flow soldering must be performed once.
- When using soldering Iron  
Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C  
Heating by soldering iron must be done only once per lead.

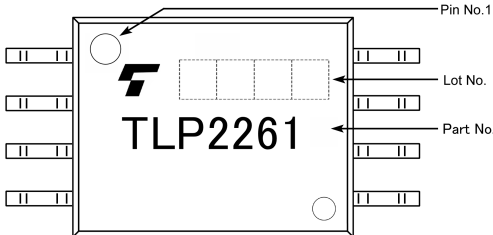
### 13.2. Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- When restoring devices after removal from their packing, use anti-static containers.
- Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

14. Land Pattern Dimensions (for reference only)



15. Marking



## 16. EN 60747-5-5 Option (D4) Specification

- Part number: TLP2261 (Note 1)
- The following part naming conventions are used for the devices that have been qualified according to option (D4) of EN 60747.

Example: TLP2261(D4-TP4,E)

D4: EN 60747 option

TP4: Tape type

E: [[G]]/RoHS COMPATIBLE (Note 2)

Note 1: Use TOSHIBA standard type number for safety standard application.

e.g., TLP2261(D4-TP4,E → TLP2261

Note 2: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Description	Symbol	Rating	Unit
Application classification for rated mains voltage $\leq 150$ Vrms for rated mains voltage $\leq 300$ Vrms		I-IV I-III	—
Climatic classification		40 / 125 / 21	—
Pollution degree		2	—
Maximum operating insulation voltage	VIORM	1230	Vpeak
Input to output test voltage, Method A $V_{pr} = 1.6 \times VIORM$ , type and sample test $t_p = 10$ s, partial discharge $< 5$ pC	$V_{pr}$	1970	Vpeak
Input to output test voltage, Method B $V_{pr} = 1.875 \times VIORM$ , 100 % production test $t_p = 1$ s, partial discharge $< 5$ pC	$V_{pr}$	2310	Vpeak
Highest permissible overvoltage (transient overvoltage, $t_{pr} = 60$ s)	VTR	8000	Vpeak
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current $I_F$ , $P_{so} = 0$ ) power (output or total power dissipation) temperature	$I_{si}$ $P_{so}$ $T_s$	300 700 150	mA mW °C
Insulation resistance $V_{IO} = 500$ V, $T_a = 25$ °C $V_{IO} = 500$ V, $T_a = 100$ °C $V_{IO} = 500$ V, $T_a = T_s$	$R_{si}$	$\geq 10^{12}$ $\geq 10^{11}$ $\geq 10^9$	$\Omega$

Fig. 16.1 EN 60747 Isolation Characteristics

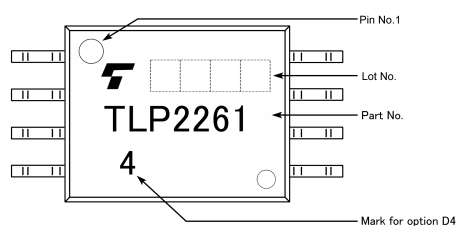
Minimum creepage distance	Cr	8.0 mm
Minimum clearance	Cl	8.0 mm
Minimum insulation thickness	ti	0.4 mm
Comparative tracking index	CTI	175

**Fig. 16.2 Insulation Related Specifications (Note)**

Note: This photocoupler is suitable for **safe electrical isolation** only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.



**Fig. 16.3 Marking on Packing**



**Fig. 16.4 Marking Example (Note)**

Note: The above marking is applied to the photocouplers that have been qualified according to option (D4) of EN 60747.

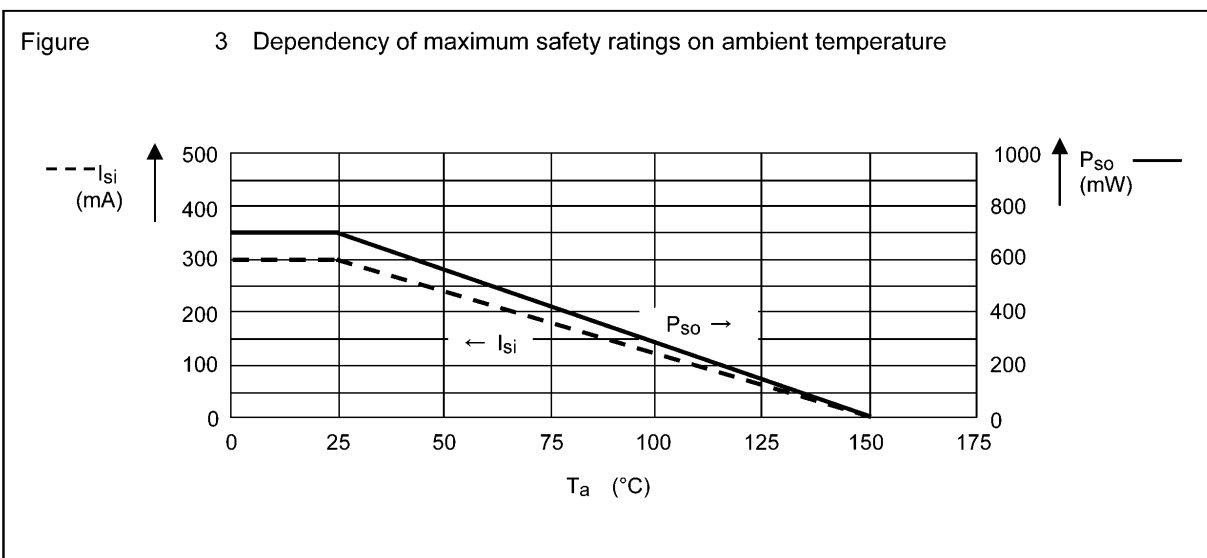
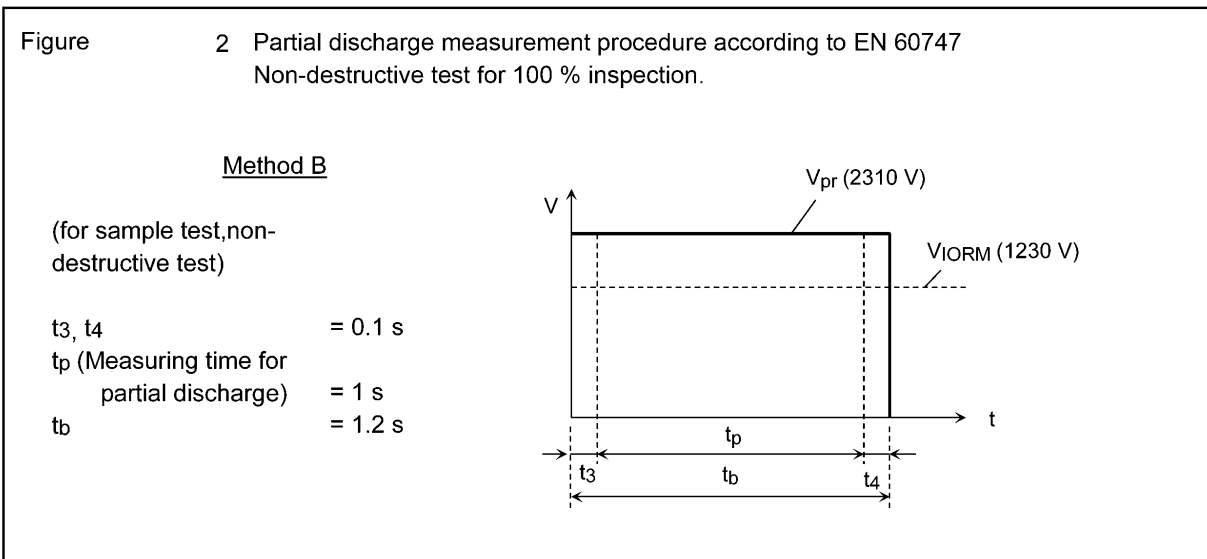
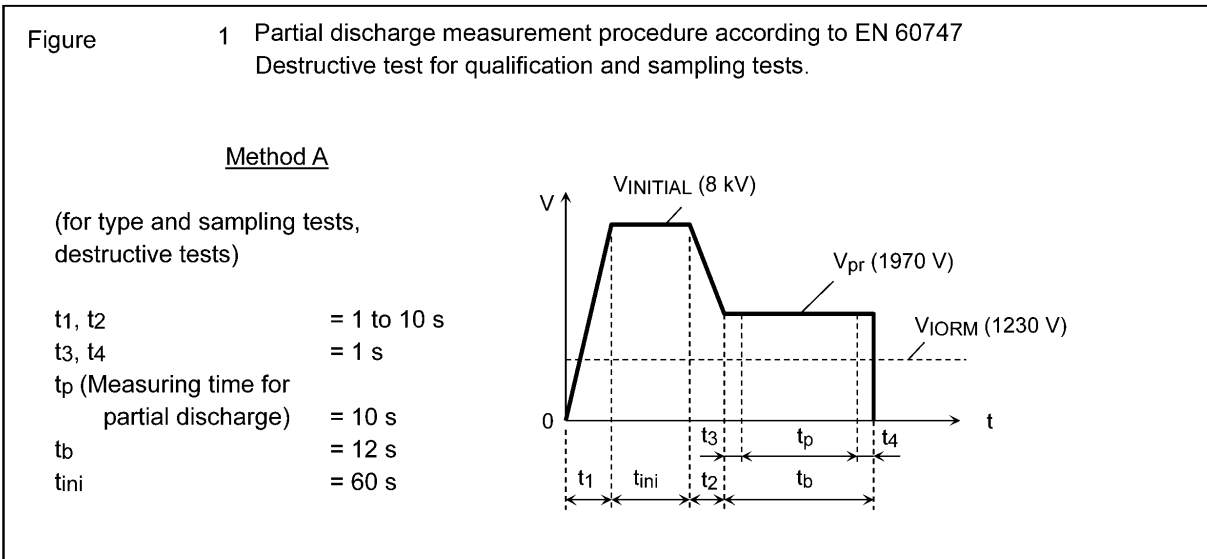


Fig. 16.5 Measurement Procedure

## 17. Ordering Information

When placing an order, please specify the part number, tape type and quantity as shown in the following example.

Example) TLP2261(TP4,E 1500 pcs

Part number: TLP2261

Tape type: TP4

[[G]]/RoHS COMPATIBLE: E (**Note 1**)

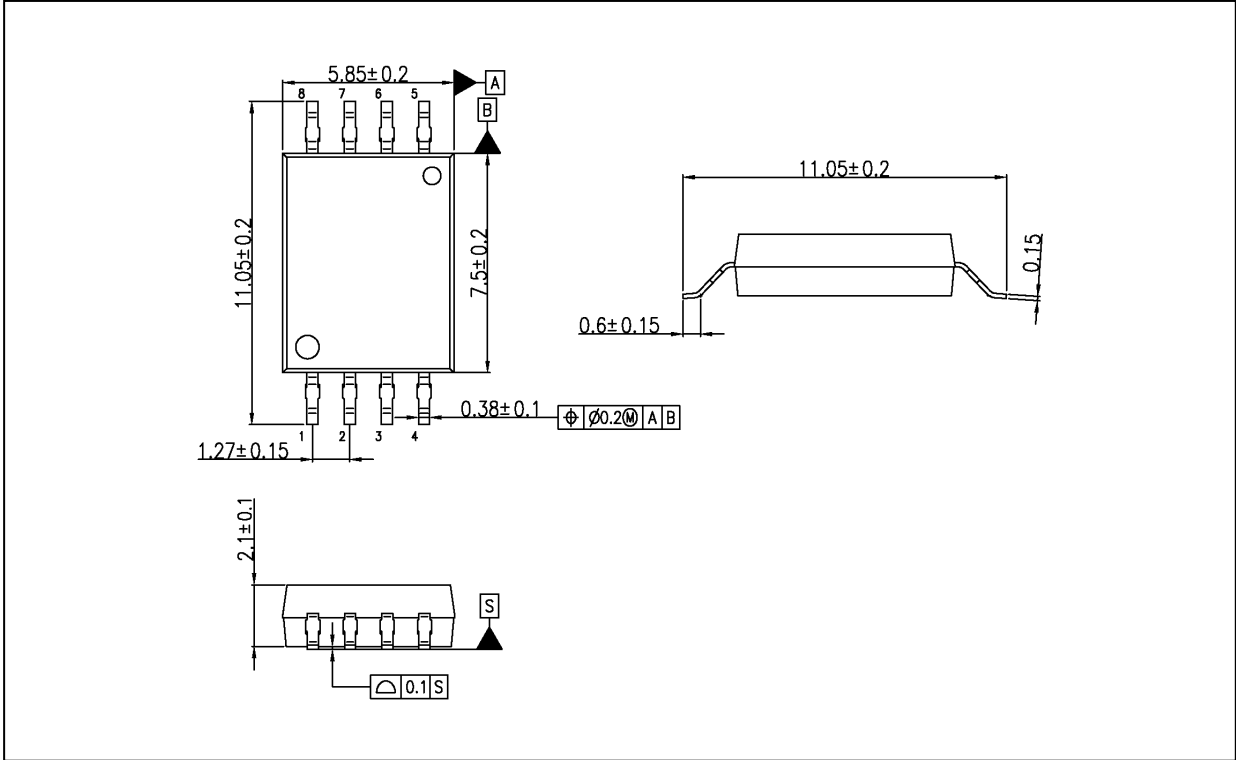
Quantity (must be a multiple of 1500): 1500 pcs

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Package Dimensions

Unit: mm



Weight: 0.203 g (typ.)

Package Name(s)
TOSHIBA: 11-6B1A

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