



## **Datasheet Correction: Vector Engine**

We would like to inform customers the corrections of the following datasheets. If you have any questions or require any further information, please contact your local sales office.

## 1. Products

TMPM370FYFG、TMPM370FYDFG TMPM372FWUG TMPM373FWDUG TMPM374FWUG TMPM376FDFG、TMPM376FDDFG

## 2. Correction

These products are not support this function.

## VEMCTLFx (Status flags Register) (x:0,1)

	31	30	29	28	27	26	25	24
bit symbol	-	-	-	-	-	-	-	-
After reset	0	0	0	0	0	0	0	0
	23	22	21	20	19	18	17	16
bit symbol	-	-	-	-	-	-	-	-
After reset	0	0	0	0	0	0	0	0
	15	14	13	12	44	40		
	2	14	13	12	11	10	9	8
bit symbol	-	-	-	-	-	-	-	-
bit symbol After reset								
	-	-	-	-	-	-	-	-
	- 0	- 0	- 0	- 0	- 0	- 0	-	- 0

Error

Bit	Bit Symbol	Туре	Function			
31-8	-	R	Read as 0.			
7-6	-	R/W	Always write "0".			
5	PLSLFM	R/W	Previous value of <plslf></plslf>			
	PLOUF	DW.	Minimum apparty or parce mean			
			Minimum disparity of pulse width VEMINPLS1 <minpls> case="0"</minpls>			
			Minimum dispar- ity of pulse width  VEMINPLS1 <minpls> case ="1"</minpls>			
3	-	R/W	Always write "0".			
2	LVTF	RW	Supply voltage lower			
			VEVDC1<\/DC> 0x0100 (1/128) case="0"			
			VEVDC1 <vdc> 0x0100 (1/128) case="1"</vdc>			
1	LAVFM	R/W	Previous <lavf> valu</lavf>			
0	LAVF	R/W	Low-speed flag 0: High-speed 1: Low-speed VEOMEGA1 <cn vefpwmchg1<fpwmchg=""> case="10"</cn>			
			VEOMEGA1 <omega> &lt; VEFPWMCHG1<fpwmchg> case="1"</fpwmchg></omega>			
5	-	R	Read as 0.			

Correction