TOSHIBA Photocoupler IRED & Photo-Transistor

TLP284-4

Programmable Controllers AC/DC-Input Module Hybrid ICs

TOSHIBA

TLP284-4 consists of photo transistor, optically coupled to two infrared emitting diode connected inverse parallel, and can operate directly by AC input current.

Since TLP284-4 is guaranteed wide operating temperature (Ta=-55 to 110 °C) and high isolation voltage (3750Vrms), it's suitable for high-density surface, mounting applications such as programmable controllers and hybrid ICs.

- Collector-emitter voltage : 80 V (min)
- Current transfer ratio : 50% (min)

Rank GB : 100% (min)

: 3750 Vrms (min)

- Isolation voltage
- Guaranteed performance over: -55 to 110 °C
- UL-recognized : UL 1577, File No.E67349
- cUL-recognized

- : CSA Component Acceptance Service No.5A File No.E67349

: EN 60747-5-5 (Note 1)

VDE-approved

Note 1: When a VDE approved type is needed, please designate the Option(V4).

Construction Mechanical Rating

5.0 mm (min)
5,0 mm (min)
0.4 mm (min)



Weight: 0.19 g (typ.)

Pin Configuration (top view)



1,3,5,7 : Anode-Cathode 2,4,6,8 : Cathode Anode 9,11,13,15 : Emitter 10,12,14,16 : Collector

Start of commercial production 2009-02

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Current Transfer Ratio

	Classification	Current Trans (IC	sfer Ratio (%) /I _F)	
TYPE	(Note1)	IF = 5 mA, V _{CE} = 5 V, Ta = 25°C		Marking of Classification
		Min	Max	\langle
TLP284-4	Blank	50	600	Blank , GB
ILF 204-4	Rank GB	100	600	GB

Note1: ex. Rank GB: TLP284-4 (GB)

Note: Application type name for certification test, please use standard product type name, i.e. TLP284-4 (GB): TLP284-4

Absolute Maximum Ratings (Ta = 25°C)

			\sim	\sim	
	Characteristic	Symbol	Rating	Unit	\rangle
	Forward current	I _{F(RMS)}	±50	mA	$\overline{\mathcal{O}}$
	Forward current derating (Ta ≥ 50°C)	ΔI _F /°C	-0.67	mA/°C	
	Pulse forward current (Note 1)	IFR	±1		
LED	Diode power dissipation (1 circuit)	RD	70	mW	
	Diode power dissipation derating $(Ta \ge 50^{\circ}C)$ (1 circuit)	APD/°C	-0.93	mW/°C	
	Junction temperature	Тј	125	°C	
	Collector-emitter voltage	VCEO	80	V	
	Emitter-collector voltage	VECO	7	V	
٦	Collector current	lc	50	mA	
Detector	Collector power dissipation (1 circuit)	Pc	100	mW	
	Collector power dissipation derating ($Ta \ge 25^{\circ}C$) (1 circuit)	ΔPc/°C	-1.0	mW/°C	
	Junction temperature		125	°C	
Оре	erating temperature range	Topr	-55 to 110	°C	
Stor	rage temperature range	T _{stg}	-55 to 125	°C	
Lea	d soldering temperature (10 s)	T _{sol}	260	°C	
Tota	al package power dissipation (1 circuit)	Рт	170	mW	
Tota	al package power dissipation derating (Ta ≥ 25°C) (1 circuit)	ΔP _T /°C	-1.7	mW/°C	
Isola	ation voltage (Note 2)	BVs	3750	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width \leq 100 μ s, frequency 100 Hz

Note 2: AC, 60 s, R.H.≤60 %

Device considered a two terminal device: LED side pins shorted together and DETECTOR side pins shorted together.

Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
LED	Forward voltage	VF	$I_F = \pm 10 \text{ mA}$	1.0	1.15	1.3	V
Ш	Capacitance	Ст	V = 0 V, f = 1 MHz	_	60	—	pF
	Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = 0.5 mA	80	Ι	-	V
	Emitter-collector breakdown voltage	V(BR)ECO	IE = 0.1 mA		\mathcal{F}	_	V
Detector	Collector dark current	lana	$V_{CE} = 48 V,$ Ambient light below (100 & (Note 1))	775)	0.01 (2)	0.1 (10)	μA
	(Note 2)	ICEO	V _{CE} = 48 V, Ta = 85 °C Ambient light below (100 & x) (Note 1)		2 (4)	50 (50)	μA
	Capacitance (collector to emitter)	CCE	V = 0 V, f = 1 MHz	2_	10	Z	pF

Note.1: Irradiation to marking side using standard light bulb.

Note 2: Because of the construction, leak current might be increased by ambient light. Please use photocoupler with less ambient light.

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	10/1-	1E = ±5 mA, VCE = 5 V	50		600	%
	IC/IF	Rank GB	100		600	70
Saturated CTR		IF = ±1 mA, VCE = 0.4 V	—	60	-	%
Saturated CTR	IC/IF(sat)	Rank GB	30			70
	\mathcal{C}	1 _C = 2.4 mA, I _F = ±8 mA	—		0.4	
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{C} = 0.2 \text{ mA}, I_{F} = \pm 1 \text{ mA}$	—	0.2	_	V
	77	Rank GB	—		0.4	
Off-state collector current	IC(off)	VF = ± 0.7 V, VCE = 48 V	—	_	10	μA
CTR symmetry	IC(ratio)	IC (IF = -5 mA)/ IC (IF = 5 mA) (Note 1)	0.33	_	3	_

Note 1:



I_{C1} o V_{CE} I_{F1}

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	V _S = 0 V, f = 1 MHz	—	0.8	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H.≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 60 s	3750	_	_	V _{rms}

Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition Min Typ. Max Ur	nit
Rise time	tr	- 2 -	
Fall time	tf	V _{CC} = 10 V, I _C = 2 mA	•
Turn-on time	t _{on}	$R_L = 100 \Omega$ μ	5
Turn-off time	toff	- 3 -	
Turn-on time	ton		
Storage time	ts	RL = 1.9 kΩ (Fig.1) 25 μ V _{CC} = 5 V, I _E = ±16 mA μ ²⁵ - μ	S
Turn-off time	toff	40 -	

Fig. 1: Switching time test circuit





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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Soldering and Storage

1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as

much as possible by observing the following conditions.

1) Using solder reflow

·Temperature profile example of lead (Pb) solder



·Temperature profile example of using lead (Pb)-free solder



2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

- Please preheat it at 150°C between 60 and 120 seconds.
- Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Embossed-Tape Packing (TP) for Mini-Flat Coupler

1. Applicable Package

Package Name	Product Type
SOP16	Mini-Flat Coupler

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)



Note 1: Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

3. Tape Dimensions

3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 2.



Figure 2 Device Orientation

- 3.2 Tape Packing Quantity: 2500 devices per reel
- 3.3 Empty Device Recesses are as Shown in Table 1.

Table1 Empty Device Recesses

/		Standard	Remarks
	Occurrences of 2 or more successive empty device recesses	0 device	Within any given 40-mm section of tape, not including leader and trailer
	Single empty device recesses	6 device (max.) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 3 and table 2.



3.6 Reel

Material: Plastic

Dimensions: The reel dimensions are as shown in Figure 4 and Table 3.





4. Packing

Packed in a shipping carton.

Figure 4 Reel Forms

5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.



Note 1: Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

EN 60747-5-5 Option (V4) Specification

Types : TLP284-4 (Note 1)

Type designations for "option: (V4)", which are tested under EN 60747 requirements.

Ex.: TLP284-4 (V4GB-TP,F

V4 : EN 60747 option GB : CTR rank type TP : Standard tape & reel type F : [[G]]/RoHS COMPATIBLE (Note 2)

Note 1: Use TOSHIBA standard type number for safety standard application. Ex.: TLP284-4 (V4GB-TP,F \rightarrow TLP284-4

Note 2: Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

EN 60747 Isolation Characteristics

Description	Symbol	Rating	Unit
Application classification)	
for rated mains voltage ≤ 150Vrms for rated mains voltage ≤ 300Vrms		I-IV I-III	_
Climatic classification)	55 / 110 / 21	_
Pollution degree		2	_
Maximum operating insulation voltage	VIORM	707	Vpk
Input to output test voltage, Method A Vpr=1.6 × VIORM, type and sample test tp=10s, partial discharge<5pC	Vpr	1131	Vpk
Input to output test voltage, Method B Vpr=1.875 × VIORM, 100% production test tp=1s, partial discharge<5pC	Vpr	1325	Vpk
Highest permissible overvoltage (transient overvoltage, tpr=60s)	Vtr	6000	Vpk
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve)			
current (input current IF, Psi=0) power (output or total power dissipation) temperature	lsi Psi Tsi	250 400 150	mA mW °C
Insulation resistance VIO=500V, Ta=T _{si}	Rsi	≥10 ⁹	Ω

Insulation Related Specifications

Minimum clearanceCI5.0mmMinimum insulation thicknessti0.4mmComparative tracking indexCTI175	Minimum creepage distance	Cr	5.0mm
	Minimum clearance	CI	5.0mm
Comparative tracking index CTI 175	Minimum insulation thickness	ti	0.4mm
	Comparative tracking index	CTI	175

Note: If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value. If this is not permissible, the user shall take suitable measures.

Note: This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuit.







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