

MOSFETs Silicon N-Channel MOS

# SSM3H137TU

## 1. Applications

· Relay Drivers

#### 2. Features

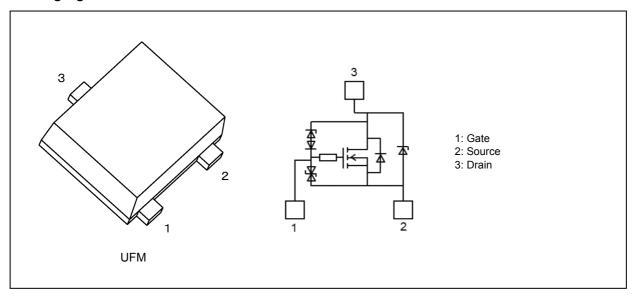
- (1) AEC-Q101 (Rev. D) qualified. (Note 1)
- (2) 4.0-V gate drive voltage.
- (3) Low drain-source on-resistance
  - :  $R_{DS(ON)}$  = 295 m $\Omega$  (max) (@V<sub>GS</sub> = 4.0 V,  $I_D$  = 0.5 A)

 $R_{\rm DS(ON)}$  = 280 m $\Omega$  (max) (@V<sub>GS</sub> = 4.5 V, I<sub>D</sub> = 1.0 A)

 $R_{\rm DS(ON)}$  = 240 m $\Omega$  (max) (@ $V_{\rm GS}$  = 10 V,  $I_{\rm D}$  = 1.0 A)

Note 1: For detail information, Please contact to our sales.

#### 3. Packaging and Internal Circuit





# 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			$V_{DSS}$	34	V
Gate-source voltage			$V_{GSS}$	±20	
Drain current (DC)		(Note 1)	I <sub>D</sub>	2	Α
Drain current (pulsed)		(Note 1), (Note 2)	I <sub>DP</sub>	6	
Power dissipation		(Note 3)	$P_D$	800	mW
Power dissipation	(t = 1 s)	(Note 3)		1000	
Channel temperature			T <sub>ch</sub>	150	°C
Single-pulse avalanche energy		(Note 4)	E <sub>AS</sub>	3.5	mJ
Avalanche current			I <sub>AR</sub>	2.0	Α
Storage temperature			T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Ensure that the channel temperature does not exceed 150 °C.
- Note 2: Pulse width (PW)  $\leq$  10  $\mu$ s, duty  $\leq$  1%
- Note 3: Device mounted on an FR4 board. (25.4 mm × 25.4 mm × 1.6 mm ,Cu pad: 645 mm<sup>2</sup>)
- Note 4:  $V_{DD}$  = 25 V, Starting  $T_{ch}$  = 25 °C, L = 0.5 mH

Note: The MOSFETs in this device are sensitive to electrostatic discharge. When handling this device, the worktables, operators, soldering irons and other objects should be protected against anti-static discharge.

Note: The channel-to-ambient thermal resistance, R<sub>th(ch-a)</sub>, and the drain power dissipation, P<sub>D</sub>, vary according to the board material, board area, board thickness and pad area. When using this device, be sure to take heat dissipation fully into account.



#### 5. Electrical Characteristics

# 5.1. Static Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain-source breakdown voltage		V <sub>(BR)DSS</sub>	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	34	_	37	V
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 30.4 V, V <sub>GS</sub> = 0V		_	10	μА
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$		_	±10	
Gate threshold voltage	(Note 1)	$V_{th}$	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	0.7	_	1.7	V
Drain-source on-resistance	(Note 2)	R <sub>DS(ON)</sub>	$V_{GS} = 4.0 \text{ V}, I_D = 0.5 \text{ A}$	_	230	295	mΩ
			V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.0 A	_	220	280	
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.0 A	_	200	240	
Forward transfer admittance	(Note 2)	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 A	_	2.2	_	S

Note 1: Let  $V_{th}$  be the voltage applied between gate and source that causes the drain current ( $I_D$ ) to below (1 mA for this device). Then, for normal switching operation,  $V_{GS(ON)}$  must be higher than  $V_{th}$ , and  $V_{GS(OFF)}$  must be lower than  $V_{th}$ . This relationship can be expressed as:  $V_{GS(OFF)} < V_{th} < V_{GS(ON)}$ .

Take this into consideration when using the device.

Note 2: Pulse measurement.

## 5.2. Dynamic Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$	_	119	_	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1 MHz	_	8	_	
Output capacitance	Coss		_	40	_	
Switching time (turn-on time)	t <sub>on</sub>	$V_{DD} = 20 \text{ V}, I_D = 0.5 \text{ A},$ $V_{GS} = 0 \text{ to } 4.5 \text{ V}, R_{GS} = 10 \Omega$	_	320	_	ns
Switching time (turn-off time)	t <sub>off</sub>	Duty $\leq$ 1%, $V_{IN}$ : $t_r$ , $t_f$ < 5 ns, Common source, See Chapter 5.3.	_	800		

#### 5.3. Switching Time Test Circuit

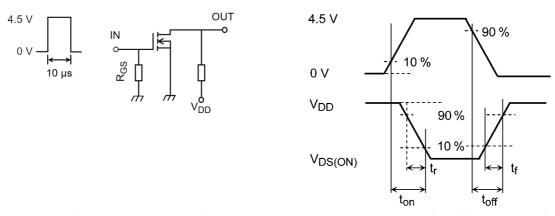


Fig. 5.3.1 Switching Time Test Circuit

Fig. 5.3.2 Input Waveform/Output Waveform

## 5.4. Gate Charge Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} = 20 \text{ V}, I_D = 1.0 \text{ A},$	_	3.0	_	nC
Gate-source charge 1	Q <sub>gs1</sub>	V <sub>GS</sub> = 10 V	_	0.8		
Gate-drain charge	$Q_{gd}$			0.4		

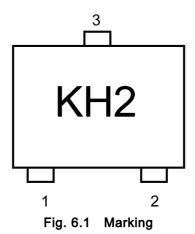


# 5.5. Source-Drain Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	(Note 1)	$V_{DSF}$	$I_{DR} = 2.0 \text{ A}, V_{GS} = 0 \text{ V}$	_	0.82	1.2	V

Note 1: Pulse measurement.

## 6. Marking





### 7. Characteristics Curves (Note)

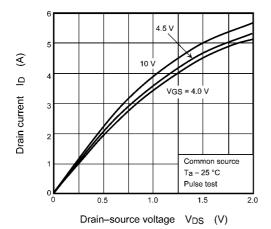
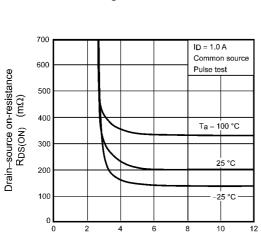


Fig. 7.1 I<sub>D</sub> - V<sub>DS</sub>



Gate-source voltage V<sub>GS</sub> (V) Fig. 7.3 R<sub>DS(ON)</sub> - V<sub>GS</sub>

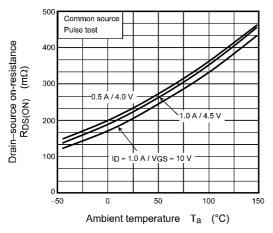


Fig. 7.5 R<sub>DS(ON)</sub> - T<sub>a</sub>

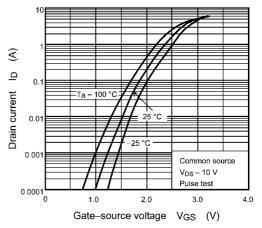


Fig. 7.2 I<sub>D</sub> - V<sub>GS</sub>

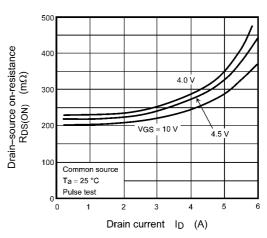


Fig. 7.4 R<sub>DS(ON)</sub> - I<sub>D</sub>

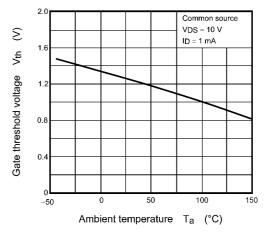


Fig. 7.6 V<sub>th</sub> - T<sub>a</sub>

Rev.3.0



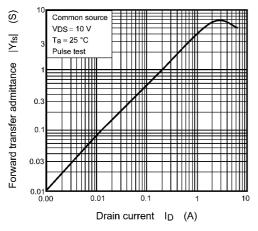
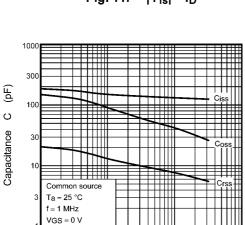


Fig. 7.7 |Y<sub>fs</sub>| - I<sub>D</sub>



Drain-source voltage V<sub>DS</sub> (V) Fig. 7.9 C - V<sub>DS</sub>

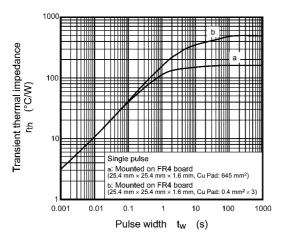


Fig. 7.11  $r_{th}$  -  $t_w$  (MOSFET)

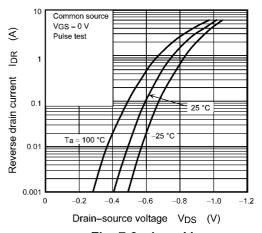


Fig. 7.8 IDR - VDS

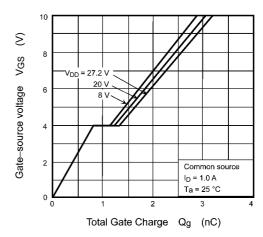


Fig. 7.10 Dynamic Input Characteristics

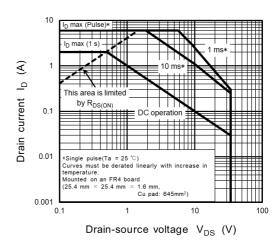
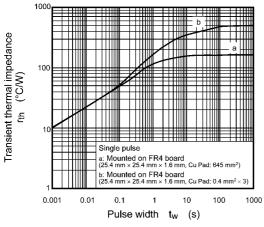


Fig. 7.12 Safe Operating Area





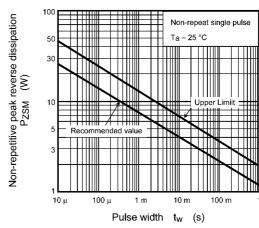


Fig. 7.13  $r_{th}$  -  $t_w$  (Zener)

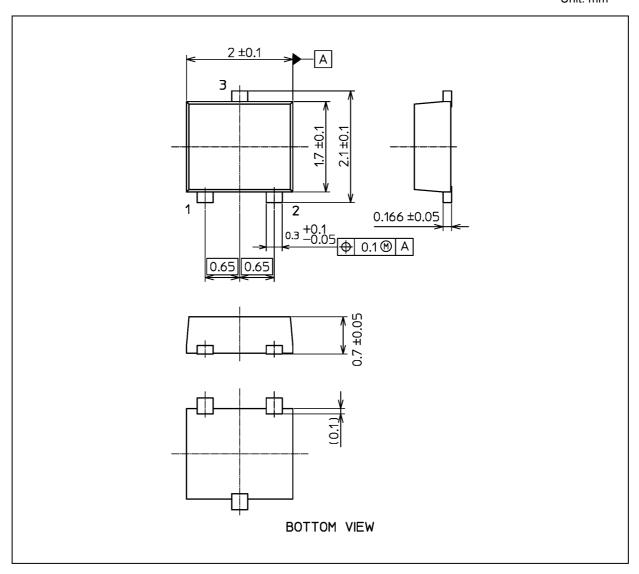
Fig. 7.14 P<sub>ZSM</sub> - t

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 6.6 mg (typ.)

	Package Name(s)	
Nickname: UFM		



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