

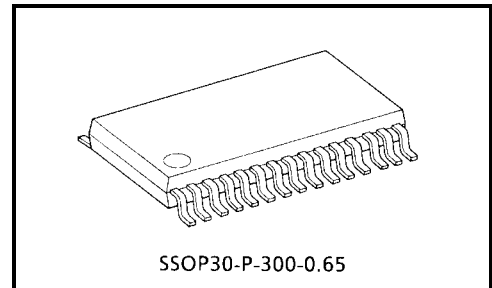
# TB6584AFNG

## 3-Phase Full-Wave Sine-Wave PWM Brushless Motor Controller

The TB6584AFNG is designed for motor fan applications for three-phase brushless DC motors.

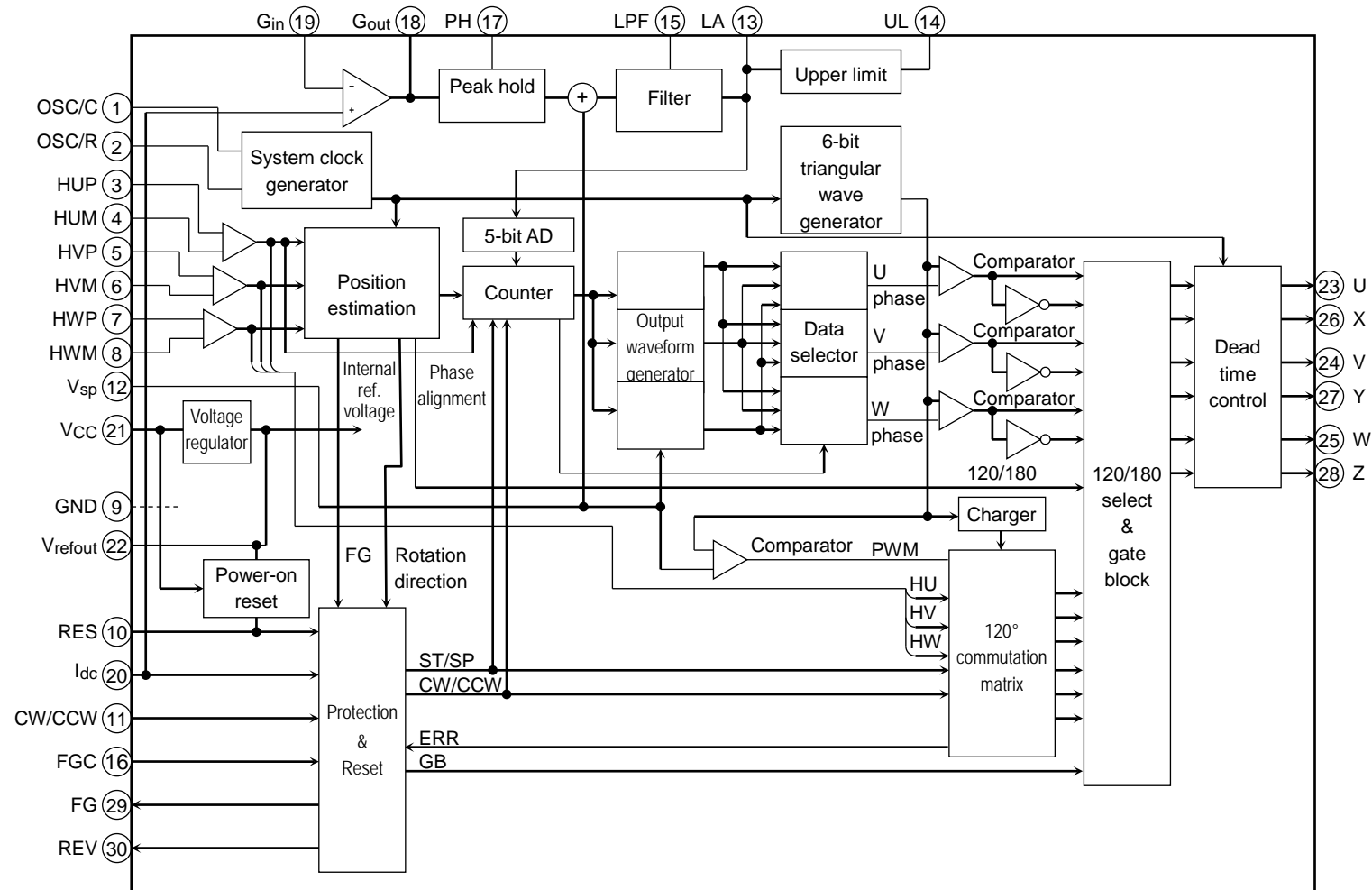
### Features

- Sine-wave PWM control
- Triangular-wave generator  
(with a carrier frequency of  $f_{osc}/252$  Hz)
- Lead angle control ( $0^\circ$  to  $58^\circ$  in 32 separate steps)  
External setting or automatic internal control
- Current-limiting input pin
- Internal voltage regulator circuit ( $V_{refout} = 5$  V (typ.), 30 mA (max))
- Operating supply voltage range:  $V_{CC} = 6$  V to 16.5 V

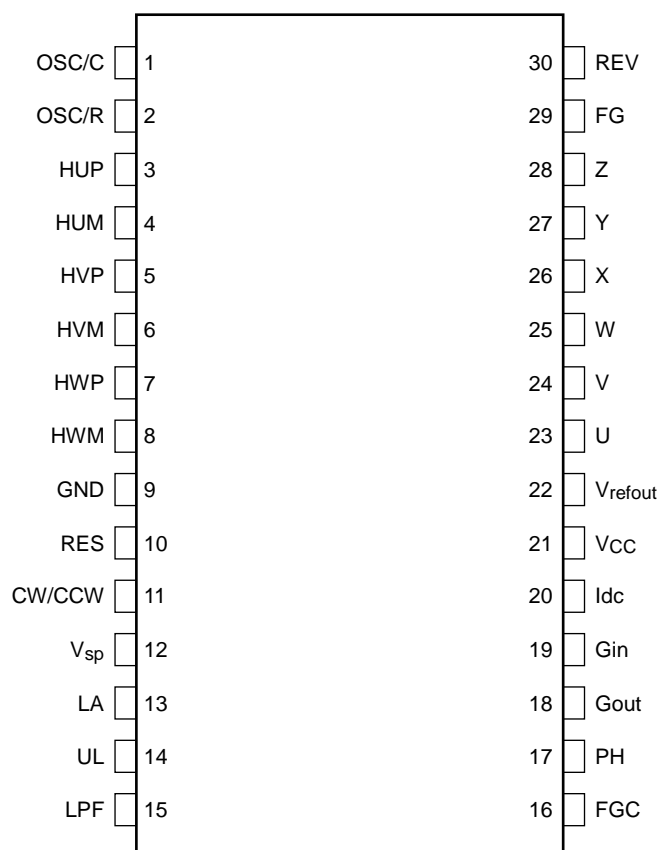


Weight: 0.17 g (typ.)

## Block Diagram



In the above block diagram, part of the functional blocks or constants may be omitted or simplified for explanatory purposes.

**Pin Configuration**

## Pin Description

Pin No.	Symbol	Function	Description	
1	OSC/C	Oscillator capacitor	CR oscillation	
2	OSC/R	Oscillator resistor		
3	HUP	Position signal input, U	Gate block protection is activated when UVW = 111 or 000. These inputs have internal digital filters ( $\approx 500$ ns)	
4	HUM			
5	HVP	Position signal input, V		
6	HVM			
7	HWP	Position signal input, W		
8	HWM			
9	GND	Ground		—
10	RES	Reset input		L: Runs the motor. H: Stops the motor. (The commutation output signals are forced Low.) The RES input has an internal pull down resistor.
11	CW/CCW	Clockwise/counterclockwise rotation	L: Clockwise rotation H: Counterclockwise rotation The CW/CCW input has an internal pull-up resistor.	
12	V <sub>sp</sub>	Voltage command input	The V <sub>sp</sub> input has an internal pull down resistor.	
13	LA	Lead angle (LA) control input	The LA input allows the lead angle to be adjusted between 0° and 58 in 32 separate steps.	
14	UL	Upper limit for LA	The UL input determines the upper limit for the lead angle (UL = 0 to 5.0 V).	
15	LPF	RC low pass filter capacitor	A capacitor for the RC low pass filter is connected to this pin. (A 100 k $\Omega$ resistor is contained on-chip.)	
16	FGC	FG output signal select input	H or OPEN: FG = 3 ppr L: FG = 1 ppr The FGC input has an internal pull-up resistor.	
17	PH	Peak hold	A peak-hold capacitor and a discharge resistor are connected to this pin.	
18	Gout	Gain setting	The Gin and Gout pins are used to amplify the I <sub>dc</sub> level so that the lead angle will be optimal.	
19	Gin			
20	Idc	Current limit control input	The DC-link current is applied to the I <sub>dc</sub> input. The reference voltage is 0.5 V. The I <sub>dc</sub> input has an internal RC filter (with a time constant of 1 $\mu$ s) and a digital filter (with a time constant of 1 $\mu$ s).	
21	V <sub>CC</sub>	Power supply	V <sub>CC</sub> = 6 to 16.5 V	
22	V <sub>refout</sub>	Reference voltage output	5 V (typ.), 30 mA (max) A capacitor for oscillation prevention is connected to the V <sub>refout</sub> output.	
23	U	Commutation signal output U, (U high-side)	High-active	
24	V	Commutation signal output V, (V high-side)		
25	W	Commutation signal output W, (W high-side)		
26	X	Commutation signal output X, (U low-side)		
27	Y	Commutation signal output Y, (V low-side)		
28	Z	Commutation signal output Z, (W low-side)		
29	FG	FG signal output	FGC = H or OPEN: FG = 3 ppr output FGC = L: FG = 1 ppr output *ppr: One pulse per an electrical angle	
30	REV	Reverse rotation detection signal	The REV output is used to detect an occurrence of reverse rotation.	

## Input/Output Equivalent Circuits

Equivalent circuit diagrams may be partially omitted or simplified for explanatory purposes.

Pin	Symbol	Input/Output Signal	Internal Circuit
Position signal input, U Position signal input, V Position signal input, W	HUP HUM HVP HVM HWP HWM	Analog Hysteresis: $\pm 7.5$ mV (typ.)	
Clockwise/counterclockwise rotation  L: CW H: CCW	CW/CCW	Digital L: 0.8 V (max) H: $V_{refout} - 1$ V (min)	
Reset input  L: Runs the motor. H: Stops the motor. (Reset)	RES	Digital L: 0.8 V (max) H: $V_{refout} - 1$ V (min)	
FG signal select input H or OPEN: FG = 3 ppr L: FG = 1 ppr	FGC	Digital L: 0.8 V (max) H: $V_{refout} - 1$ V (min)	
Voltage command signal  1.0 V < Vsp ≤ 2.1 V Refresh operation (The X, Y and Z pins have a conduction duty cycle of 8%.)	Vsp	Analog Vsp voltage range: 0 to 10 V When 5.7 V ≤ Vsp ≤ 7.3 V, the PWM duty cycle is fixed at 92% (typ.). When 8.2 V ≤ Vsp ≤ 10 V, the TB6584AFNG is put in test mode.	
Lead angle control input  0 V: 0° 5 V: 58° (5-bit AD)	LA	To fix the lead angle externally, UL and Vrefout should be connected together. The lead angle is linearly determined according to the voltage applied to the LA input. LA voltage range: 0 to 5.0 V ( $V_{refout}$ ) If $LA > V_{refout}$ , the commutation occurs with the maximum lead angle of 58°. When configured for auto lead angle control, the LA input should be left OPEN. At this time, the LA input can be used to check the lead angle in real time.	

Pin	Symbol	Input/Output Signal	Internal Circuit
Gain setting (Lead angle control circuitry)	Gin Gout	Non-inverting amplifier 25 dB max Gout: Output voltage L: GND H: VCC - 1.7 V	
Peak hold (Lead angle control circuitry)	PH	A peak-hold capacitor and a discharge resistor are connected to the PH pin. Recommended R/C values: 100 kΩ/0.1 μF	
Low pass filter (Lead angle control circuitry)	LPF	A capacitor for the RC low pass filter is connected to this pin. A 100 kΩ (typ.) resistor is contained on-chip. Recommended C value: 0.1 μF	
Upper limit for LA	UL	If the voltage applied to the LA input exceeds the upper limit set by this input, it is clipped to limit the lead angle. UL = 0 to 5.0 V	
Current limit control input	Idc	Analog filter time constant: 1 μs (typ.) Digital filter time constant: 1 μs (typ.) Gate block protection is activated when the Idc voltage exceeds 0.5 V. (It is deactivated after a carrier cycle.) If Idc is left unconnected, all the commutation outputs are disabled.	
Reference voltage output	Vrefout	5 ± 0.5 V (30 mA max)	

Pin	Symbol	Input/Output Signal	Internal Circuit
Reverse rotation detection signal	REV	Digital Push-pull output ( $\pm 1$ mA (max))	
FG signal output	FG	Digital Push-pull output ( $\pm 1$ mA (max))  FGC = H or OPEN 3 ppr output (3 pulses per an electrical angle)  FGC = L 1 ppr output (One pulse per an electrical angle)	
Commutation signal output, U Commutation signal output, V Commutation signal output, W Commutation signal output, X Commutation signal output, Y Commutation signal output, Z	U V W X Y Z	Digital Push-pull outputs ( $\pm 2$ mA (max)) L: 0.78 V (max) H: $V_{refout} - 0.78$ V (min)	

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	18	V
Input voltage	V <sub>IN (1)</sub>	- 0.3 to V <sub>CC</sub> (Note 1)	V
	V <sub>IN (2)</sub>	- 0.3 to V <sub>refout</sub> + 0.3 (Note 2)	
Commutation output current	I <sub>OUT</sub>	2	mA
V <sub>refout</sub> output current	I <sub>refout</sub>	30 (Note 3)	mA
Power dissipation	P <sub>D</sub>	1.1 (Note 4)	W
Operating temperature	T <sub>opr</sub>	- 30 to 115 (Note 5)	°C

Note 1: V<sub>IN (1)</sub> pins: V<sub>sp</sub>, LA, and UL

Note 2: V<sub>IN (2)</sub> pins: HUP, HVP, HWP, HUM, HVM, HWM CW/CCW, RES, I<sub>dc</sub>, FGC, and Gin

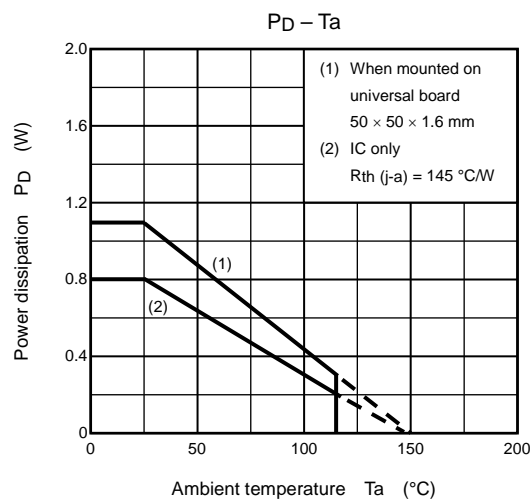
Note 3: Since the V<sub>refout</sub> pin delivers a maximum output current of 30 mA, care should be exercised to the output impedance.

Note 4: When mounted on a universal board (50 × 50 × 1.6 mm, Cu 40%)

Note 5: The operating temperature range is determined by the P<sub>D</sub> - T<sub>a</sub> characteristics.

## Operating Ranges (Ta = 25°C)

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply voltage	V <sub>CC</sub>	6	15	16.5	V
Oscillation frequency	f <sub>osc</sub>	3	4.5	6	MHz



**Electrical Characteristics (Ta = 25°C, Vcc = 15 V)**

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit	
Supply current		ICC	Vrefout = OPEN	—	5	8	mA	
Input current		IIN (1)-1	VIN = 5 V LA	—	25	50	μA	
		IIN (1)-2	VIN = 5 V Vsp	—	35	70		
		IIN (2)-1	VIN = 5 V RES	—	50	100		
		IIN (2)-2	VIN = 0 V CW/CCW, FGC	-100	-50	—		
Input voltage		VIN	High	CW/CCW, RES, FGC	Vrefout -1	—	Vrefout	V
			Low		—	—	0.8	
		Vsp	T	Sine-wave commutation ON Conduction duty = 92% (typ.)	8.2	—	10	
			H	PWM duty = 92%	5.1	5.4	5.7	
			M	Refresh → Motor startup	1.8	2.1	2.4	
L	Commutation off → Refresh		0.7	1.0	1.3			
Hall effect inputs	Input sensitivity	VS	Differential inputs	100	—	—	mVpp	
	Common-mode input voltage	VW		1.5	—	3.5	V	
	Input hysteresis	VH (1)	(Note)	±5.5	±7.5	±9.5	mV	
Input delay time		TDT	Hall inputs (fosc = 4.5 MHz)	—	1.0	—	μs	
		TDC	Idc (fosc = 4.5 MHz)	—	2.5	—		
Output voltage		VOUT (H)-1	IOUT = 2 mA U, V, W, X, Y, Z	Vrefout - 0.78	Vrefout - 0.3	—	V	
		VOUT (L)-1	IOUT = -2 mA U, V, W, X, Y, Z	—	0.3	0.78		
		VREV (H)	IOUT = 1 mA REV	Vrefout - 1.0	Vrefout - 0.2	—		
		VREV (L)	IOUT = -1 mA REV	—	0.2	1.0		
		VFG (H)	IOUT = 1 mA FG	Vrefout - 1.0	Vrefout - 0.2	—		
		VFG (L)	IOUT = -1 mA FG	—	0.2	1.0		
		Vrefout	IOUT = 30 mA Vrefout	4.5	5.0	5.5		
Output leakage current		IL (H)	VOUT = 0 V U, V, W, X, Y, Z	—	0	10	μA	
		IL (L)	VOUT = Vrefout U, V, W, X, Y, Z	—	0	10		
Output off time (Low-High)		TOFF	(fosc = 4.5 MHz), IOUT = ± 2 mA	1.7	2.0	2.3	μs	
Current sensing		VDC	Idc	0.46	0.5	0.54	V	
LA gain setting amp		AMPOUT	Gin = 100 kΩ, Gout = 10 kΩ, Idc = 0.2 V, IOUT = 1 mA	2.0	2.2	2.4	V	
		AMPOFS	Gin = 100 kΩ, Gout = 10 kΩ, Idc = 0.2 V	—	5	—	mV	
LA limit setting error		ΔU	UL = 2.0 V	-20	—	20	mV	
LA peak hold output voltage		PHOUT	Gin = 100 kΩ, Gout = 10 kΩ, Idc = 0.2 V, IOUT = 5 mA	2.0	2.2	2.4	V	
Lead angle correction		T <sub>LA</sub> (0)	LA = 0 V or OPEN, Hall inputs = 100 Hz	—	0	—	°	
		T <sub>LA</sub> (2.5)	LA = 2.5 V, Hall inputs = 100 Hz	26	30	33		
		T <sub>LA</sub> (5)	LA = 5 V, Hall inputs = 100 Hz	52	57	60		
VCC monitor		VCC (H)	Output turn-on threshold	4.2	4.5	4.8	V	
		VCC (L)	Output turn-off threshold	3.7	4.0	4.3		
		VH	Input hysteresis width	—	0.5	—		
PWM oscillation frequency (carrier frequency)		FC (20)	OSC/C = 330 pF, OSC/R = 9.1 kΩ	18	20	22	kHz	
		FC (18)	OSC/C = 330 pF, OSC/R = 10 kΩ	16.2	18	19.8		
Maximum conduction duty cycle		TON (max)	OSC/C = 330 pF, OSC/R = 10 kΩ VSP = 5.7 V	89	92	95	%	

Note: Not tested in production

## Functional Description

### 1. Basic Operation

During startup, the motor is driven by square-wave commutation signals that are generated according to the position signals. When the position signals indicate a rotational speed ( $f$ ) of 5 Hz, the TB6584AFNG estimates the rotor positions from the position signals and modulate them. The TB6584AFNG then generates sine-wave by comparing the modulated signals against a triangular waveform.

From startup to 5 Hz: square-wave drive (120° commutation);  $f = f_{osc} / (2^{12} \times 32 \times 6)$

5 Hz to: Sine-wave PWM drive (180° commutation);  $f$  will be approximately 5.7 Hz when  $f_{osc} = 4.5$  MHz

### 2. Voltage Command ( $V_{sp}$ ) Signal and Bootstrap Voltage Regulation

- (1) When  $V_{sp} \leq 1.0$  V:

The commutation signal outputs are disabled (i.e., gate protection is activated).

- (2) When  $1.0$  V  $< V_{sp} \leq 2.1$  V:

The low-side commutation signal outputs are turned on at a regular (PWM carrier) frequency. (The conduction duty cycle is approx. 8%.)

- (3) When  $2.1$  V  $< V_{sp} \leq 7.3$  V:

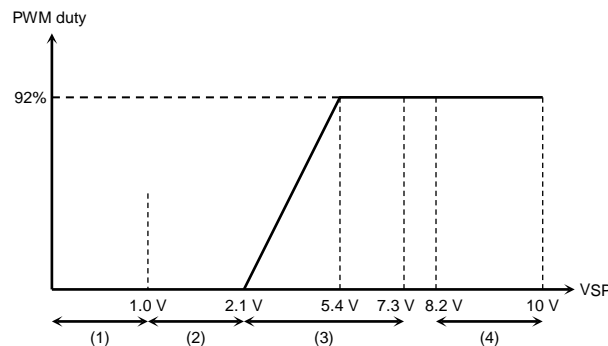
During sine-wave PWM drive, the commutation signals directly appear externally. During square-wave drive, the low-side commutation signal outputs are forced on at a regular (PWM carrier) frequency. (The conduction duty is approx. 8%.)

- (4) When  $8.2$  V  $\leq V_{sp} \leq 10$  V (test mode):

The TB6584AFNG drives in sine-wave drive mode with lead angle of zero. However, it drives in square-wave mode in detecting reverse rotation.

When  $V_{sp}$  reaches 7.9 V (typ.), lead angle switches to zero.

The PWM duty is calculated as  $PWM\_carrier\_frequency \times 92\%$  (typ.) and kept the constant value ( $5.4$  V  $\leq V_{sp}$  (typ.)).

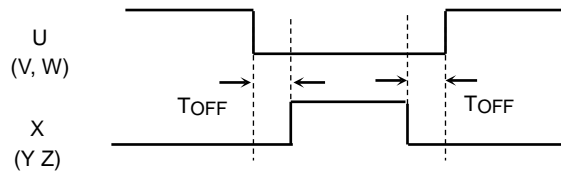


**3. Dead Time Insertion (cross conduction protection)**

To prevent a short-circuit between external low-side and high-side power elements during sine-wave PWM drive, a dead time is digitally inserted between the turn-on of one side and the turn-off of the other side. (The dead time is also implemented at the full duty cycle during square-wave drive.)

$$T_{OFF} = 9/f_{osc}$$

$T_{OFF} \approx 2.0 \mu s$  when  $f_{osc} = 4.5 \text{ MHz}$ , where  $f_{osc}$  is the reference clock frequency (i.e., CR oscillator frequency).



**4. Lead Angle Control**

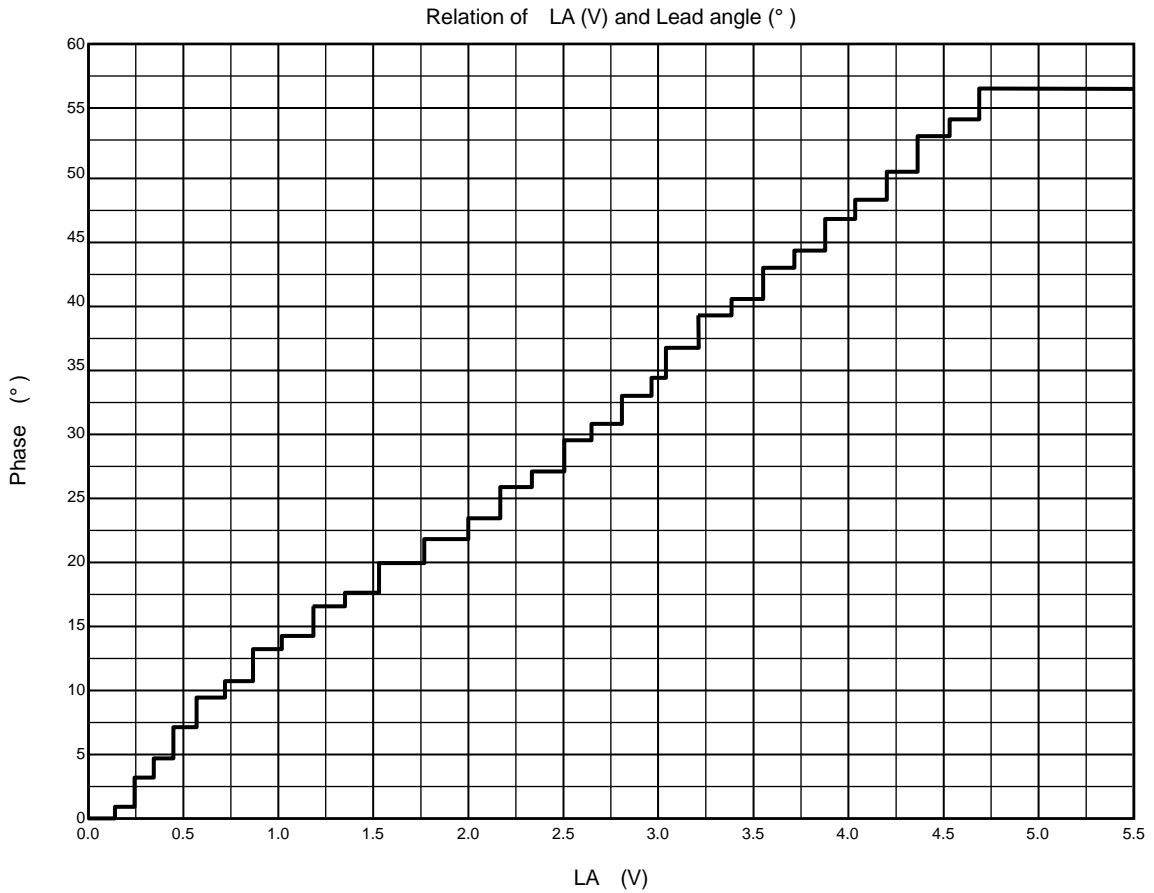
The lead angle can be adjusted between 0° and 58° in 32 separate steps according to the induced voltage level on the LA input, which works with 0 to 5 V.

$$0 \text{ V} = 0^\circ$$

5 V = 58° (A lead angle of 58° is assumed when the LA voltage exceeds 5 V.)

Sample evaluation

Step	LA (V)	Lead angle (°)	Step	LA (V)	Lead angle (°)	Step	LA (V)	Lead angle (°)
0	0.00	0.00	11	1.72	19.92	22	3.44	40.58
1	0.16	0.94	12	1.88	21.79	23	3.59	43.01
2	0.31	3.18	13	2.03	23.47	24	3.75	44.32
3	0.47	4.68	14	2.19	25.90	25	3.91	46.75
4	0.63	7.11	15	2.34	27.12	26	4.06	48.25
5	0.78	9.44	16	2.50	29.55	27	4.22	50.49
6	0.94	10.75	17	2.66	30.86	28	4.38	52.74
7	1.09	13.18	18	2.81	33.01	29	4.53	54.05
8	1.25	14.21	19	2.97	34.41	30	4.69	56.48
9	1.41	16.55	20	3.13	36.75	31	4.84	56.48
10	1.56	17.58	21	3.28	39.27	32	5.00	56.48



**5. PWM Carrier Frequency**

The triangular waveform generator provides a carrier frequency of  $f_{osc}/252$  necessary for PWM generation. (The triangular wave is also used to force the switch-on of low-side transistors during square-wave drive.)

Carrier frequency =  $f_{osc}/252$  (Hz),  
 where  $f_{osc}$  = reference clock (CR oscillator) frequency

**6. Reverse Rotation Signal**

This feature provides the rotational direction of the motor every 360 electrical degrees. A Low on the REV pin indicates 180° commutation mode (with Hall effect inputs of  $\geq 5$  Hz).

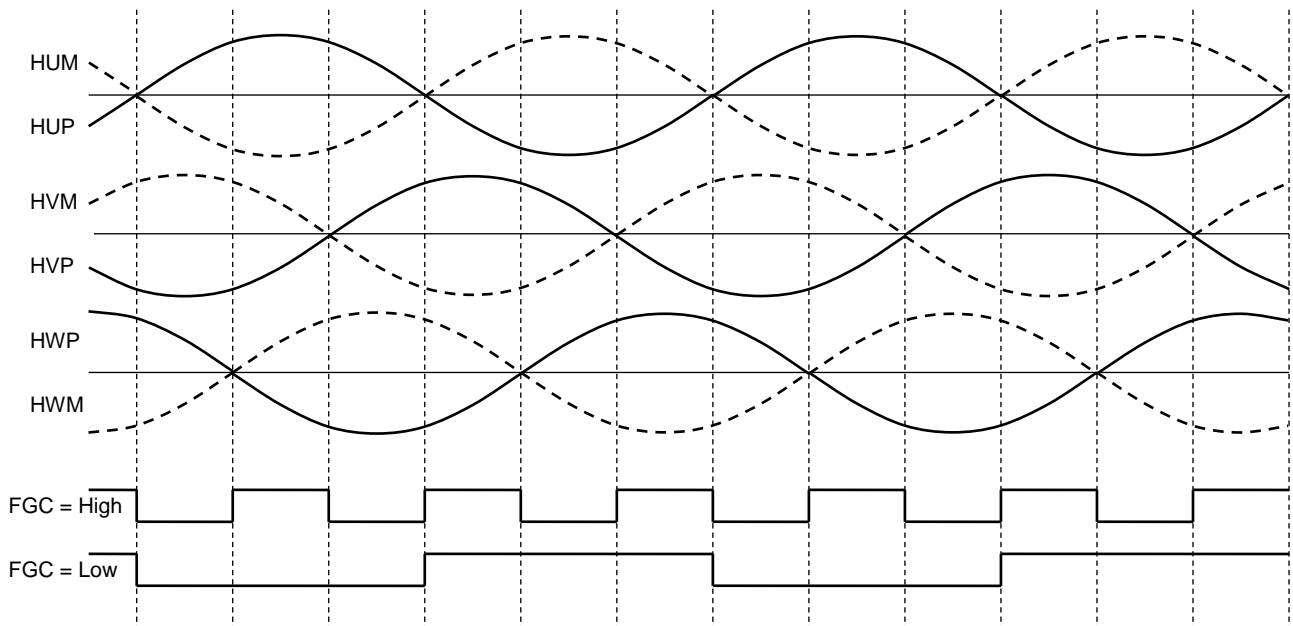
CW/CCW Pin	Actual Motor Rotation Direction	REV Pin
Low (CW)	CW (forward)	Low
	CCW (reverse)	High
High (CCW)	CW (forward)	High
	CCW (reverse)	Low

**7. Rotating Pulse Output**

The TB6584AFNG outputs rotating pulse based on hall signal. FGC terminal can switch one pulse per electrical angle or 3 pulses per electrical angle. One pulse per electrical angle is generated from hall signal of U phase. 3 pulses per electrical angle are generated by combining each rising and falling edge of U, V, and W phases.

FGC	FG
High or Open	3 pulses per electrical angle
Low	1 pulse per electrical angle

**Timing Chart of FG Signal**



**8. Protection-Related Input Pins**

(1) Overcurrent protection (Idc pin)

If the voltage of the DC-link current exceeds the internal reference voltage, the commutation signals are forced Low. Overcurrent protection is disabled after every carrier period.  
Reference voltage = 0.5 V (typ.)

(2) Gate block protection (RES pin)

When the RES input is High, the commutation outputs are disabled. When the RES input is then set Low or OPEN, the commutation outputs are re-enabled.  
Any irregular conditions of the motor should be detected by external hardware; such indications should be presented to the RES input.

RES Pin	Commutation Output Signals (U, V, W, X, Y, Z)
High	Low
Low or OPEN	The motor can be driven.

(When RES = High, charging of the bootstrap capacitor stops. In case the operation re-starts by deactivating reset, the bootstrap capacitor is not charged.)

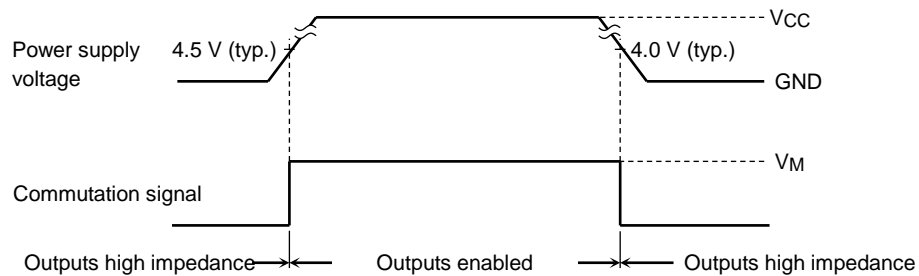
(3) Internal protection

- Abnormal position signal protection

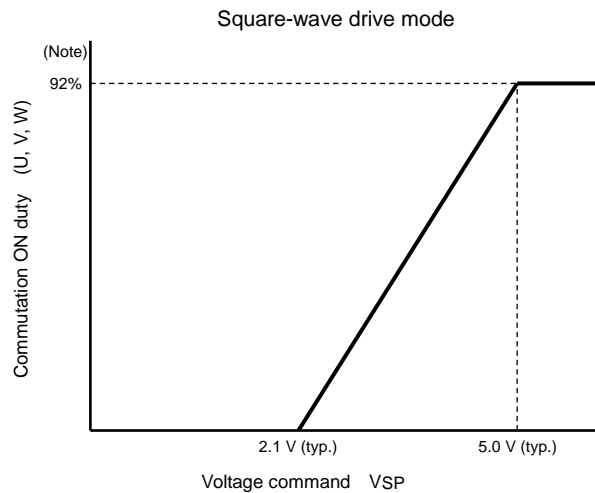
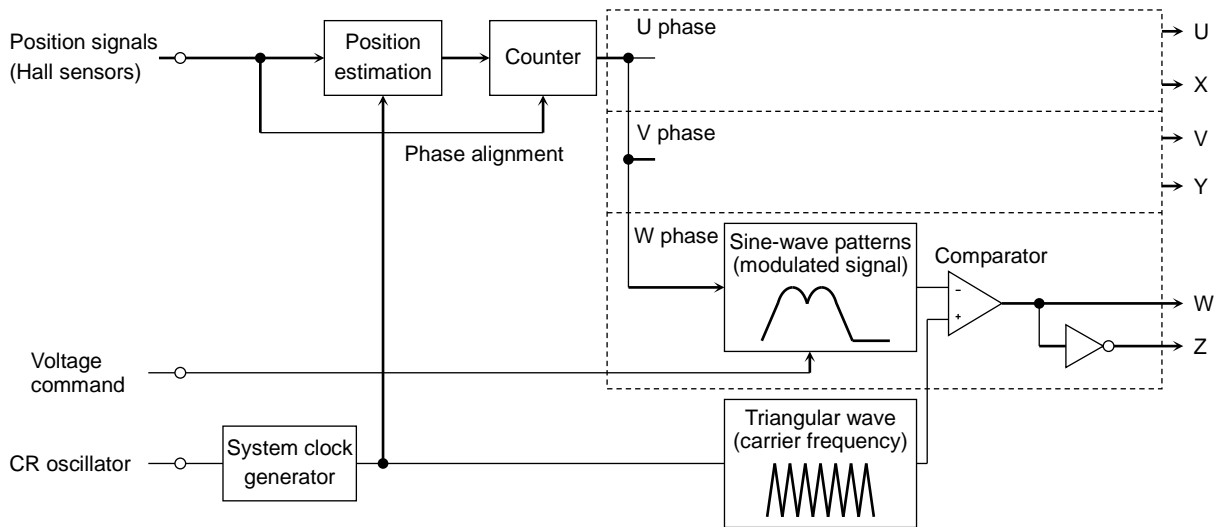
When the position signal inputs (UVW) are all Highs or all Lows, the commutation outputs are forced off (i.e., set Low). When these inputs are then set to any other combination, the commutation outputs are re-enabled. (The all-High and all-Low conditions are internal hall amplifier outputs.)

- Undervoltage lockout (VCC monitor)

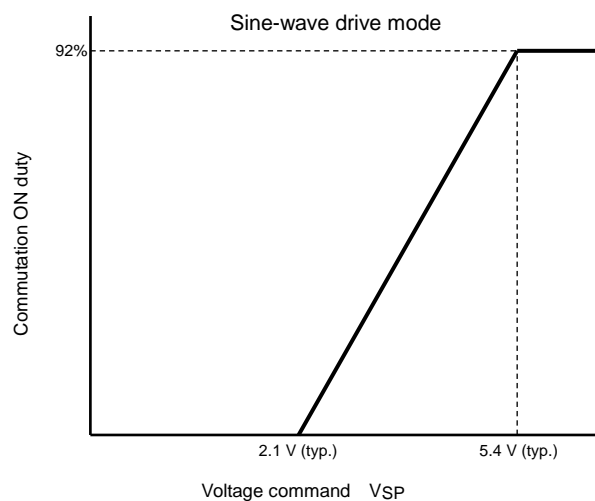
While the power supply voltage is outside the rated range during power-on or power-off, the commutation outputs are set to the high-impedance state to prevent external power elements from damage due to short-circuits.



## Operation Flow



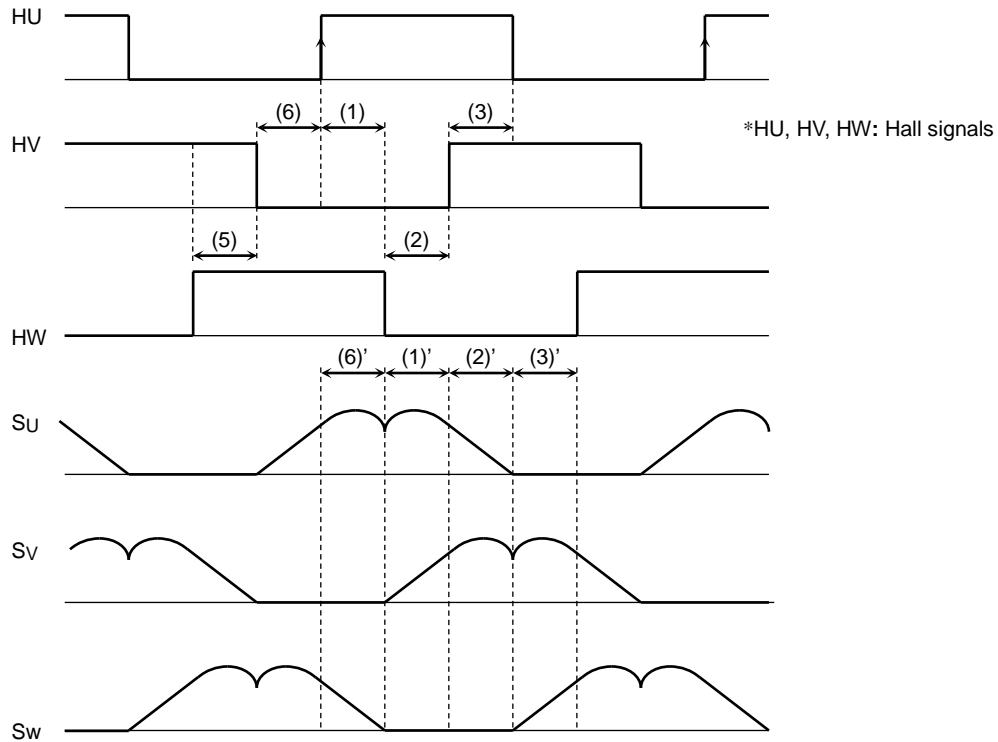
Note: The conduction period is reduced by the dead time.  $(\text{carrier period} \times 92\% - T_d \times 2)$



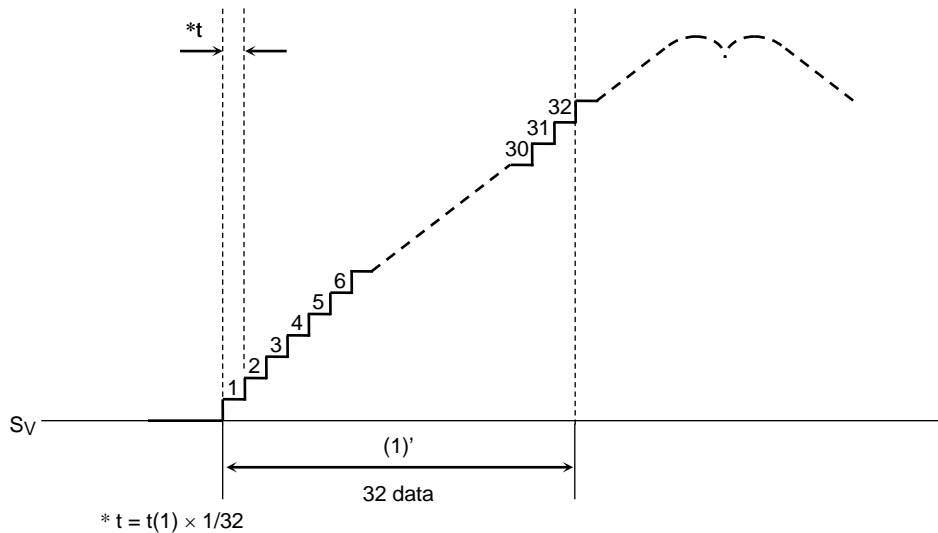
The position signals from Hall sensors are modulated, and the modulated signals are then compared against a triangular waveform to generate a sinusoidal PWM waveform.

The counter measures the period from a given rising (falling) edge of three hall signals to its next falling (rising) edge (60 electrical degrees). This period is then used as 60° phase data for the next modulation.

A total of 32 ticks comprise 60 electrical degrees; the length of a tick equals 1/32nds the time period of the immediately preceding 60° phase.



In the above diagram, the modulated waveforms have an interval ((1)') equal to the interval of 1/32 between a rising edge of HU to a falling edge of HW ((1)). And the modulated waveforms have an interval ((2)') equal to the interval of 1/32 between a falling edge of HW to a rising edge of HV ((2)). If there is not an HU rising edge before 32 ticks ends, (2)' becomes equal to (1)' until the next rising edge of HU.



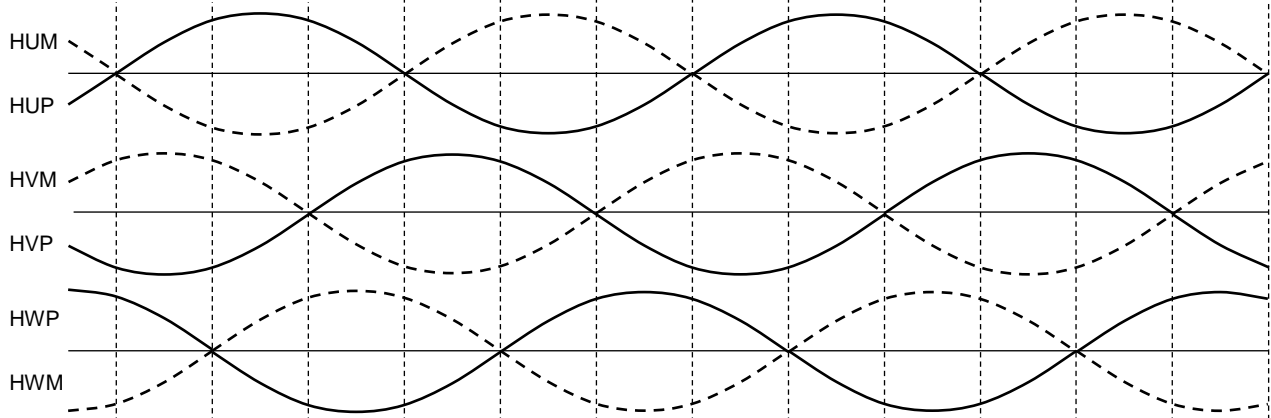
Phase of data and modulated waveform is adjusted for every zero cross of position detecting signal.

Modulation is reset on each rising and falling edge of position detecting signal, which occurs every 60 electrical degrees. While the hall signal is out of its position and the motor is accelerating or decelerating, the modulated waveform becomes discontinuous upon each reset.

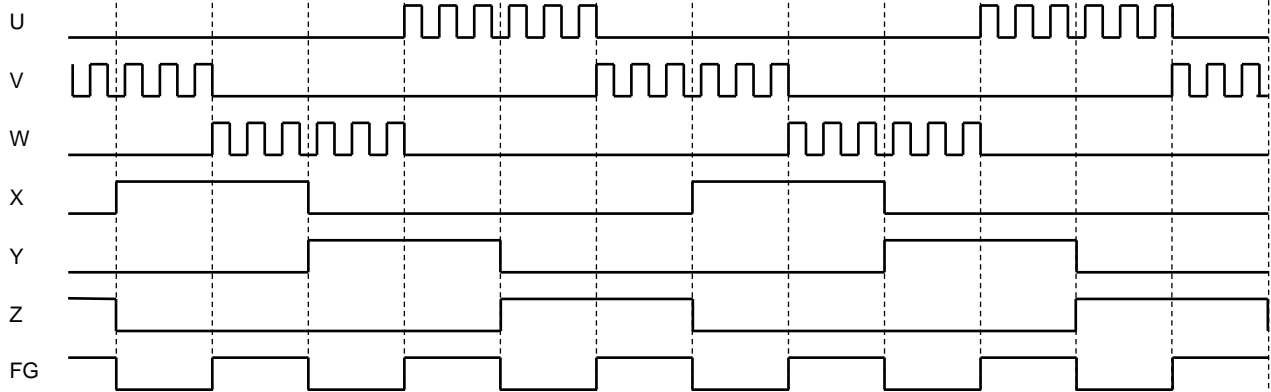
Note: In the above diagram, hall signals are shown as square waveforms for the sake of simplicity.

## Forward Rotation Timing Chart (CW/CCW = Low, LA = GND, FGC = High)

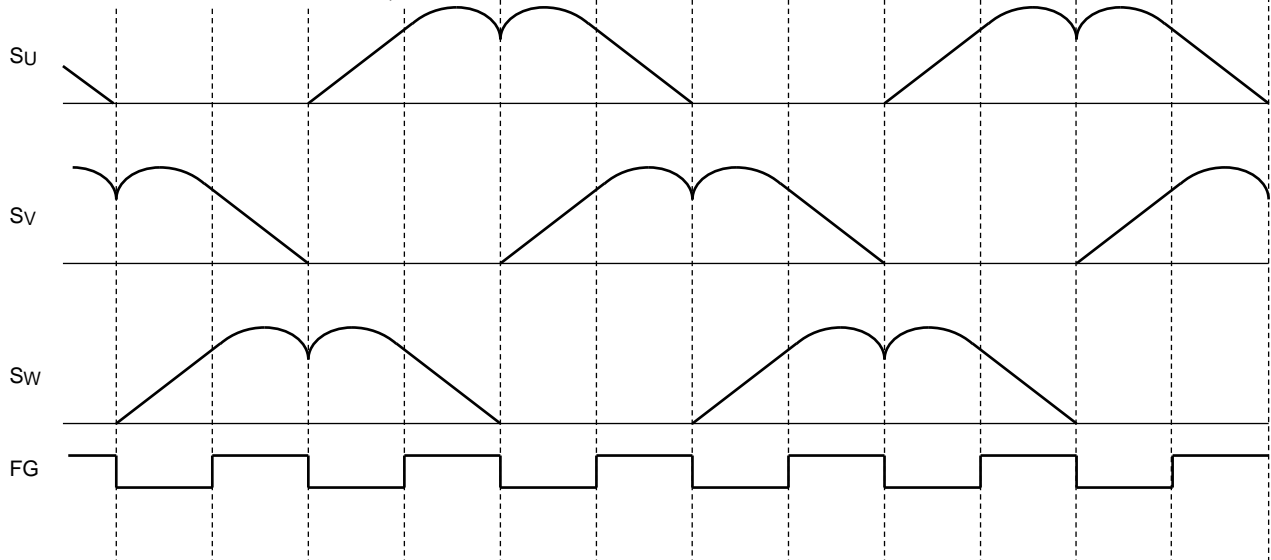
(Non-inverted hall signal inputs)



0 < Hall signals < 5.7 Hz  
(120° commutation)



5.7 Hz < Hall signals  
(180° commutation: Modulated waveforms)

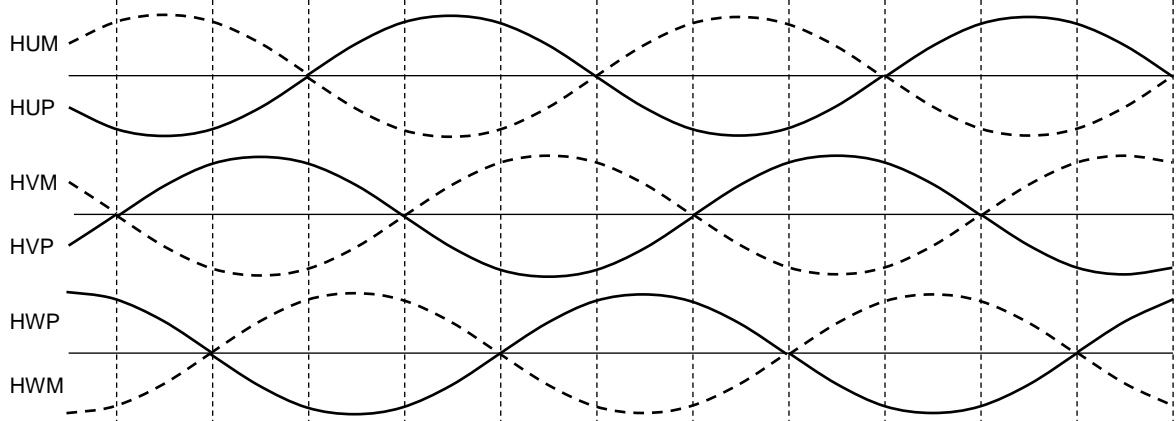


\*: When the Hall input frequency is equal to or greater than approximately 5.7 Hz (@  $f_{osc} = 4.5$  MHz), lead angle control is activated according the LA input.

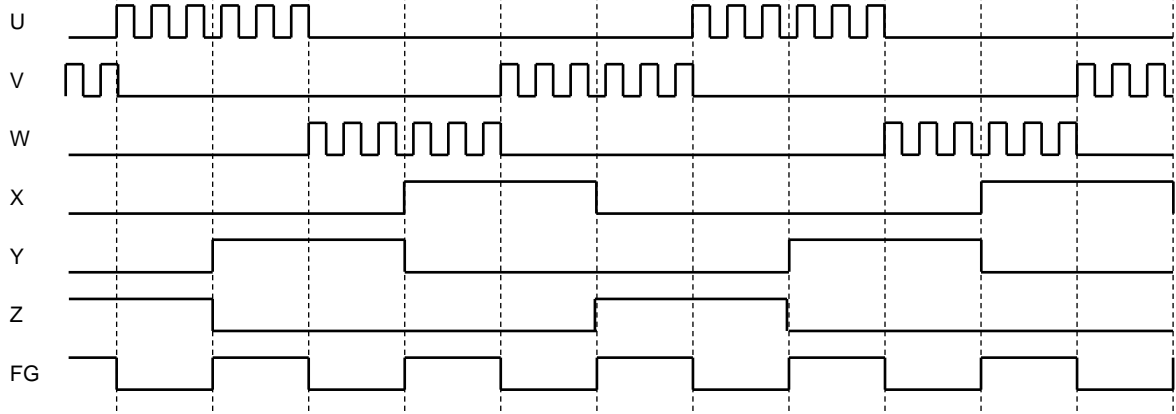
The above timing chart is simplified to illustrate the function and behavior of the device.

**Forward Rotation Timing Chart (CW/CCW = Low, LA = GND, FGC=High)**

(Inverted hall signal inputs)



Reverse rotation sensing  
(120° commutation)

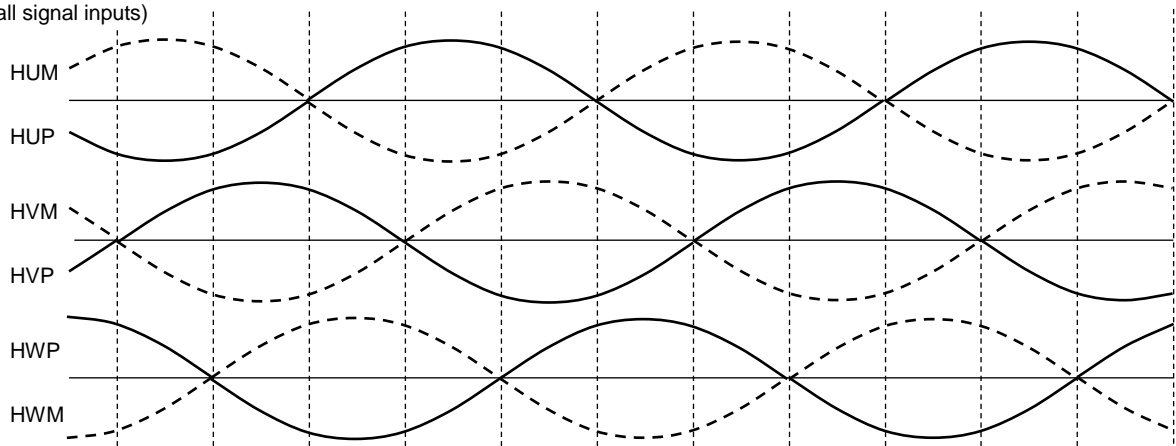


\*: When CW/CCW = Low, inverted Hall signals put the TB6584AFNG in 120 commutation mode with a lead angle of 0° (reverse rotation).

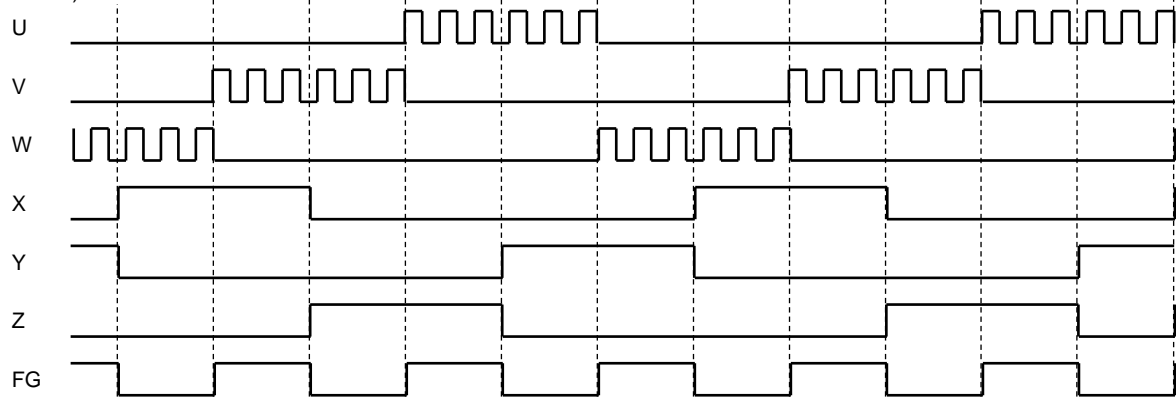
The above timing chart is simplified to illustrate the function and behavior of the device.

## Reverse Rotation Timing Chart (CW/CCW = High, LA = GND, FGC=High)

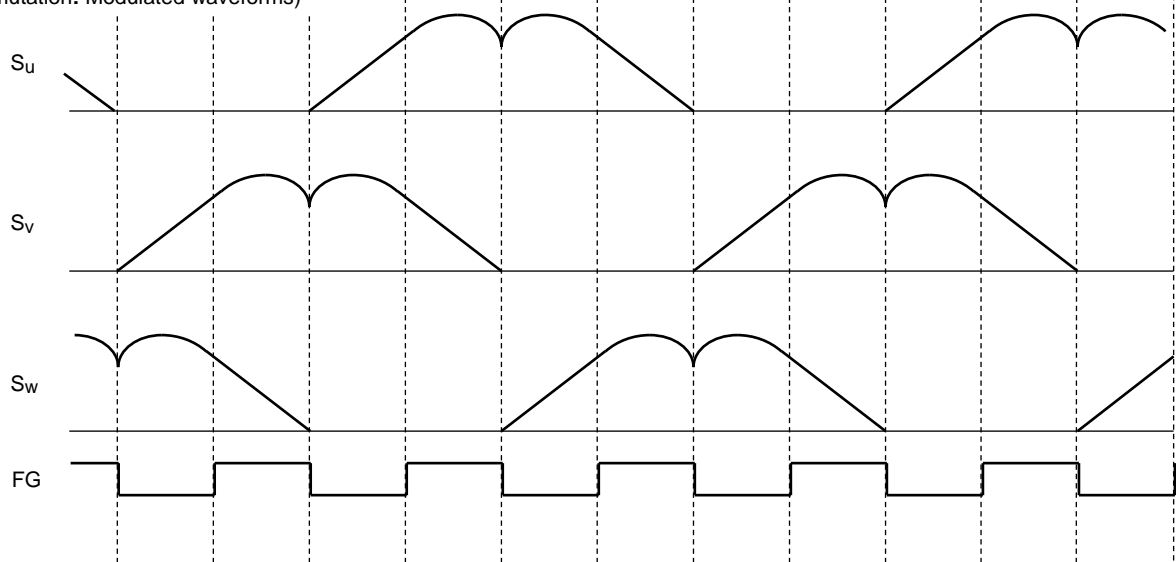
(Inverted hall signal inputs)



0 < Hall signals < 5.7 Hz  
(120° commutation)



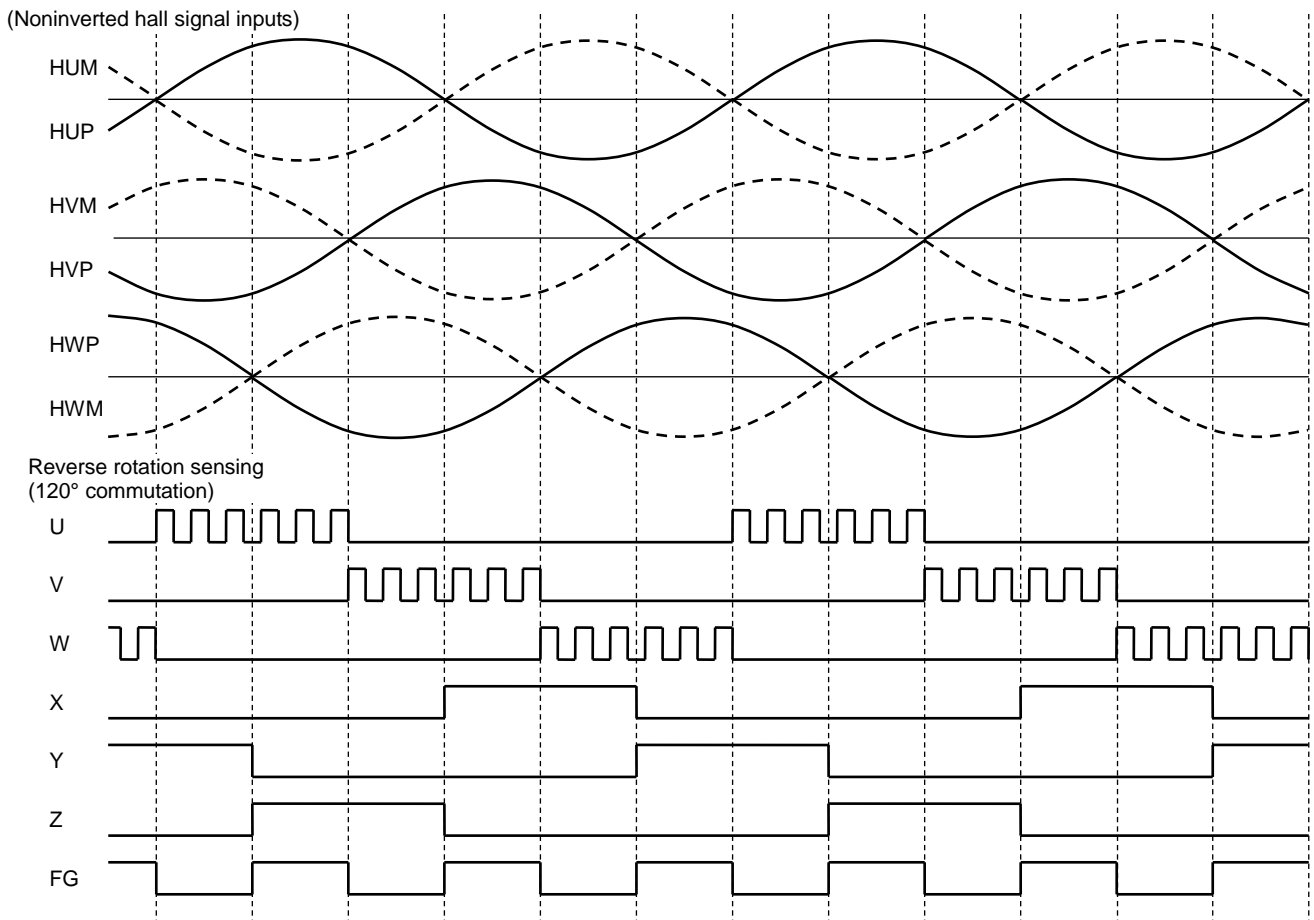
5.7 Hz < Hall signals  
(180° commutation: Modulated waveforms)



\*: When the Hall input frequency is equal to or greater than approximately 5.7 Hz (@  $f_{osc} = 4.5$  MHz), lead angle control is activated according the LA input.

The above timing chart is simplified to illustrate the function and behavior of the device.

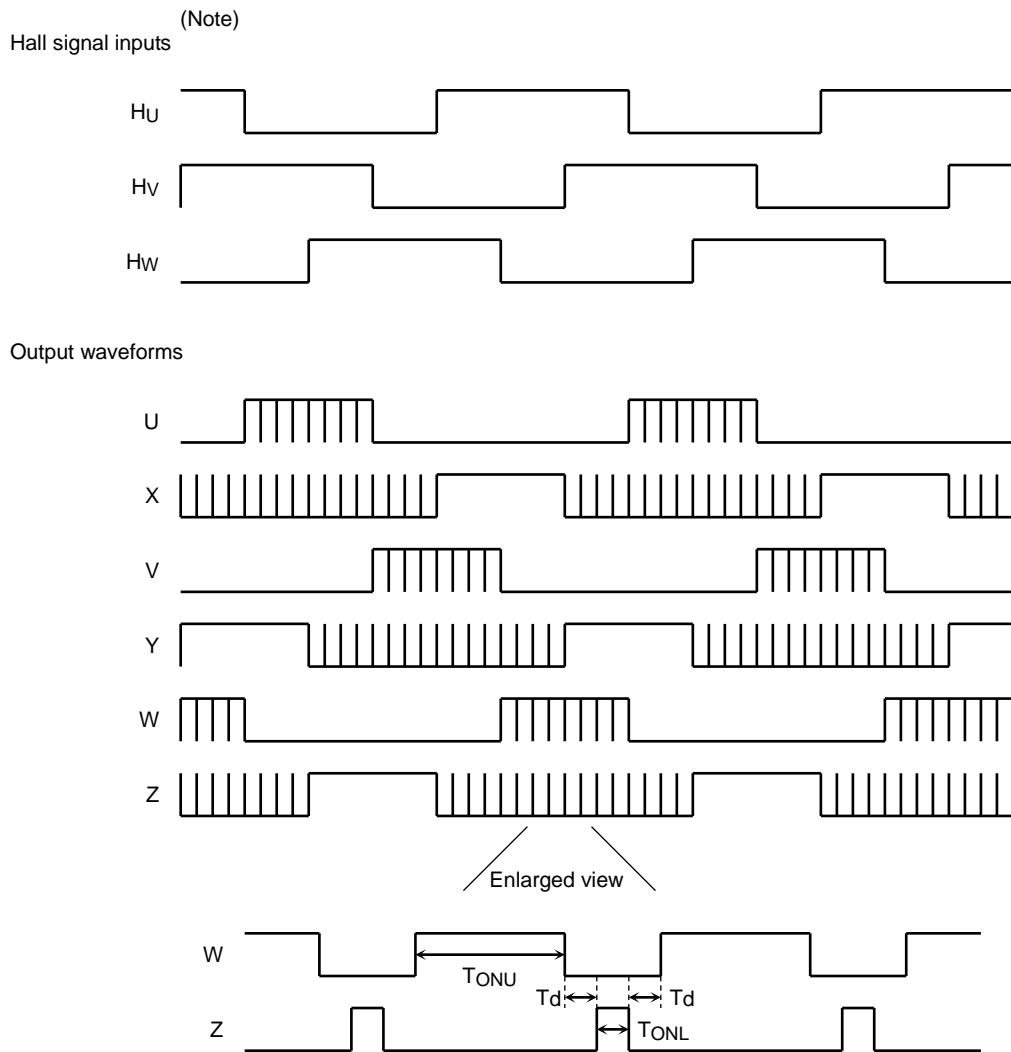
**Reverse Rotation Timing Chart (CW/CCW = High, LA = GND, FGC=High)**



\*: When CW/CCW = High, noninverted Hall signals put the TB6584AFNG in 120° commutation mode with a lead angle of 0° (reverse rotation).

The above timing chart is simplified to illustrate the function and behavior of the device.

## Square-Wave Drive Waveform (CW/CCW = Low)



Note: Square waveforms are used in the above diagram for the sake of simplicity.

To obtain an adequate bootstrap voltage, the low-side outputs (X, Y and Z) are always turned on for eight percent of the carrier period ( $T_{ONL}$ ) even during the off time of the low side in 120° commutation mode. As shown in the enlarged view, the high-side outputs (U, V and W) are turned off for a dead time period while the low-side outputs are on. ( $T_d$  varies with the  $V_{sp}$  input.)

$$\text{Carrier period} = 252/f_{osc} \text{ (s)}$$

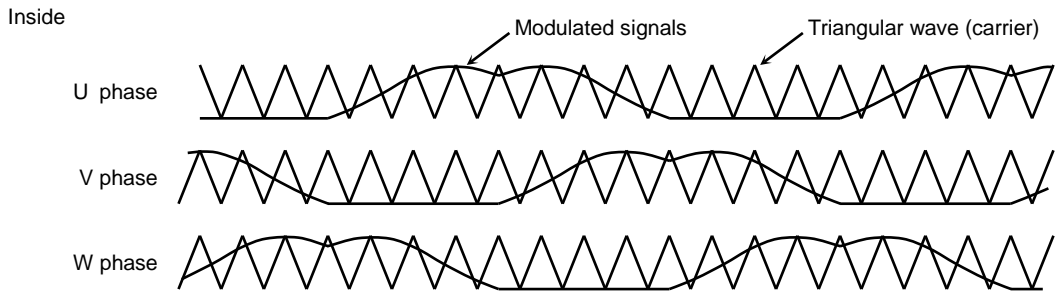
$$\text{Dead time: } T_d = 9/f_{osc} \text{ (s) (} V_{sp} \geq 5.0 \text{ V,)}$$

$$T_{ONL} = \text{Carrier period} \times 8\% \text{ (s) (Constant regardless of the } V_{sp} \text{ input)}$$

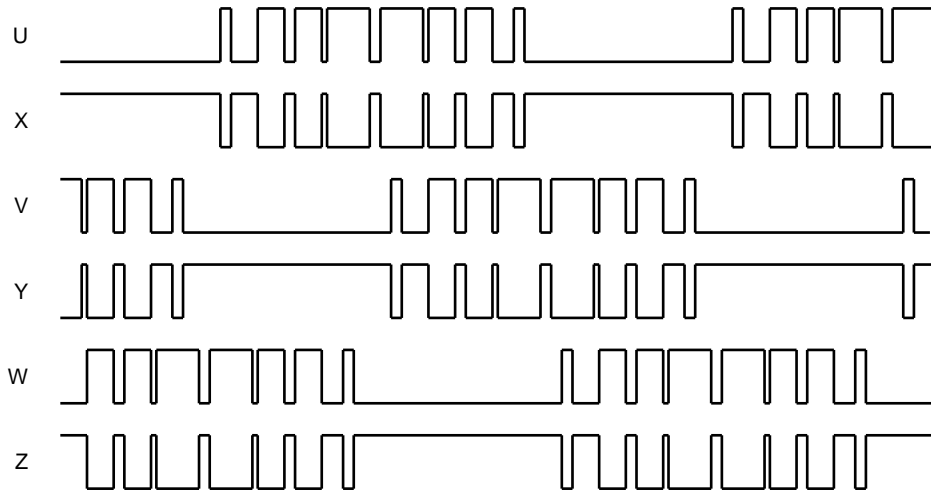
In square-wave drive mode, the changing of the motor speed is enabled, depending on the  $V_{sp}$  voltage; the motor speed is determined by the duty cycle of  $T_{ONU}$ . (See the square-wave drive mode diagram on page 15.)

Note: At startup, the motor is driven by a square wave when the Hall signal frequency is approximately 5.7 Hz or lower (@  $f_{osc} = 4.5 \text{ MHz}$ ) and when the motor is rotating in the direction reverse to the settings of the TB6584AFNG (REV = High).

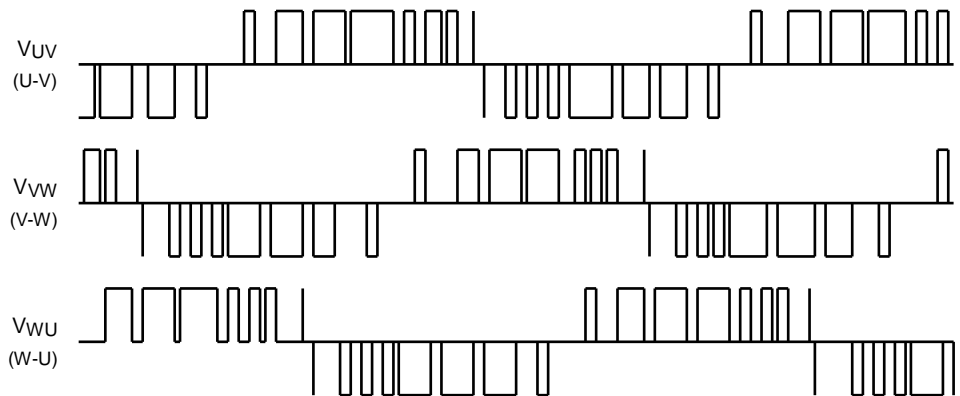
## Sine-Wave Drive Waveform (CW/CCW = Low)



Output waveforms



Phase voltage differences

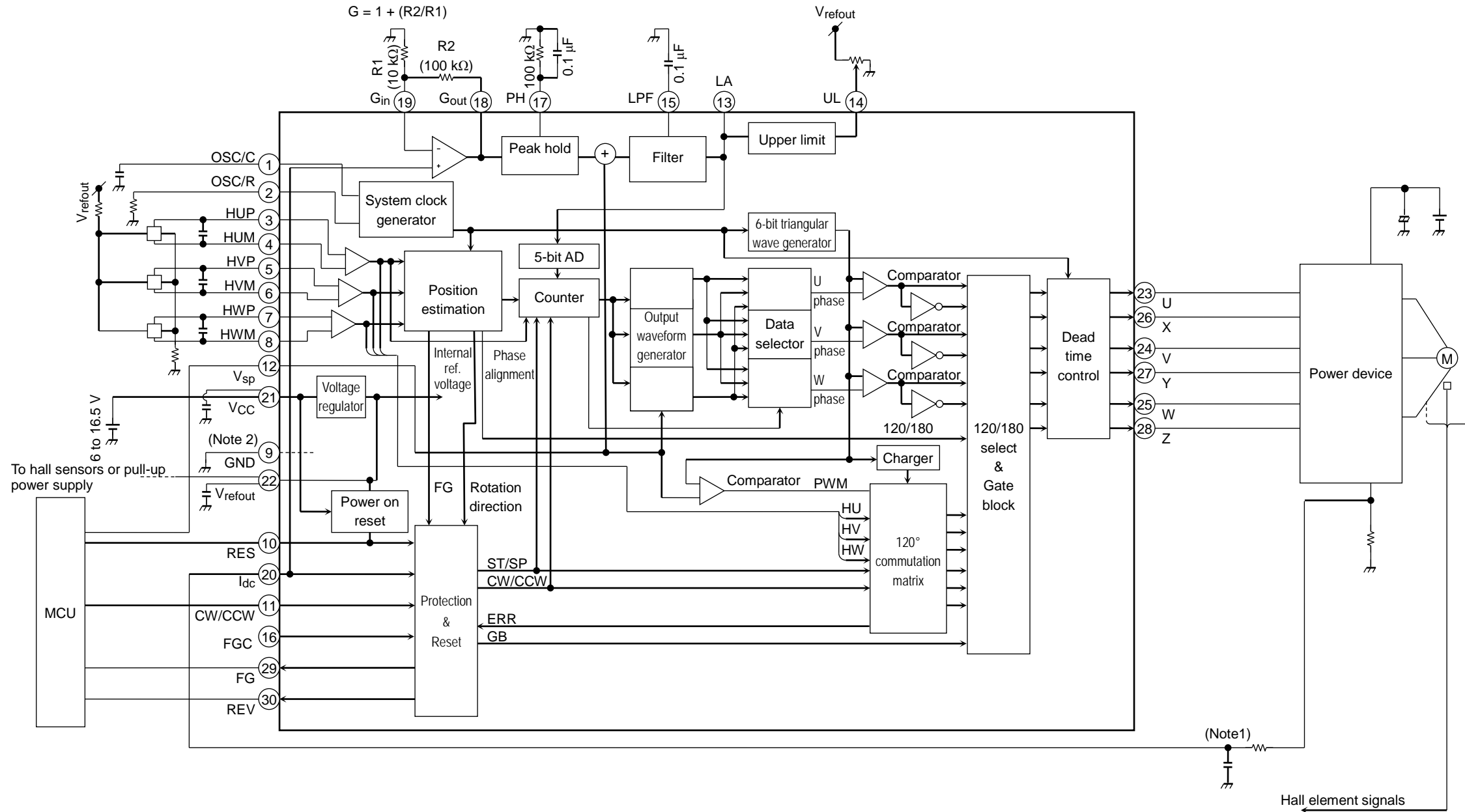


In sine-wave drive mode, the amplitude of the modulated signals varies with the  $V_{sp}$  voltage, and the motor speed changes with the conduction duty cycle of the output waveforms. (See the sine-wave drive mode diagram on page 15.)

$$\text{Triangular wave frequency} = \text{carrier frequency} = f_{osc}/252 \text{ (Hz)}$$

**Note:** At startup, the motor is driven by a sine wave when the Hall signal frequency is approximately 5.7 Hz or higher (@  $f_{osc} = 4.5 \text{ MHz}$ ) and when the motor is rotating in the same direction as settings of the TB6584AFNG (REV = Low).

Application Circuit Example



Note 1: Connect to ground as necessary to prevent IC malfunction due to noise.

Note 2: Connect GND to signal ground on the application circuit.

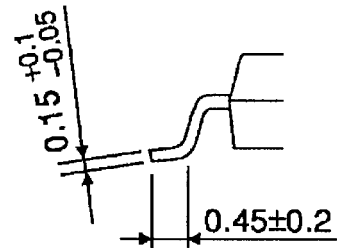
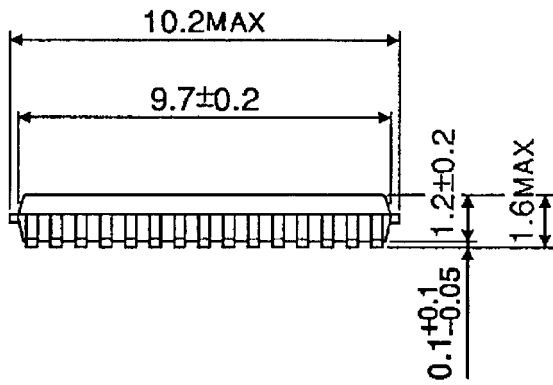
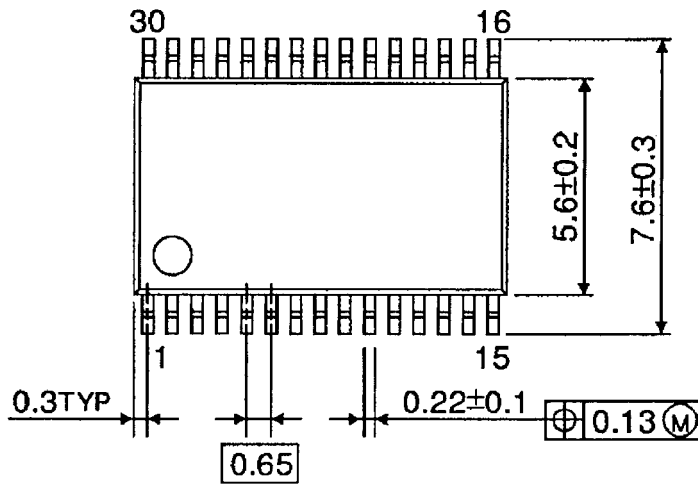
Note 3: Utmost care is required in the design of the output, Vcc, and GND lines since the IC may shatter or explode due to short-circuits between outputs, short to Vcc or short to ground. The IC may also shatter or explode when it is installed in a wrong orientation.

Note 4: Make sure that the TB6584AFNG might output small pulse of 100 ns because it does not limit the width of minimum pulse in outputting.

**Package Dimensions**

SSOP30-P-300-0.65

Unit : mm



Weight: 0.17 g (typ.)

**Notes on Contents****1. Block Diagrams**

Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purposes.

**2. Equivalent Circuits**

The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.

**3. Timing Charts**

Timing charts may be simplified for explanatory purposes.

**4. Absolute Maximum ratings**

The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings. Exceeding the rating(s) may cause device breakdown, damage, deterioration or ignition, and may result injury by explosion or combustion.

Applications using the device should be designed so that no maximum rating will ever be exceeded under any operating conditions.

It must be ensured that the device is used within the specified operating range.

**5. Application Circuits**

The application circuits shown in this document are provided for reference purposes only. Thorough evaluation is required, especially at the mass production design stage.

Toshiba does not grant any license to any industrial property rights by providing these examples of application circuits.

**6. Test Circuits**

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

## IC Usage Considerations

### Notes on handling of ICs

- (1) The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.  
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
- (2) Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- (3) If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition.  
Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- (4) Do not insert devices in the wrong orientation or incorrectly.  
Make sure that the positive and negative terminals of power supplies are connected properly. Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.  
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.

### Points to Remember on Handling of ICs

- (1) Over current protection circuit  
Over current protection circuits (referred to as current limiter circuits) do not necessarily protect ICs under all circumstances. If the Over current protection circuits operate against the over current, clear the over current status immediately.  
Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the over current protection circuit to not operate properly or IC breakdown before operation. In addition, depending on the method of use and usage conditions, if over current continues to flow for a long time after operation, the IC may generate heat resulting in breakdown.
- (2) Heat radiation design  
In using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (T<sub>J</sub>) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into considerate the effect of IC heat radiation with peripheral components.
- (3) Back-EMF  
When a motor rotates in the reverse direction, stops or slows down abruptly, a current flow back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond absolute maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

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