

TOSHIBA Diode Silicon Epitaxial Planar Type

# 1SS387

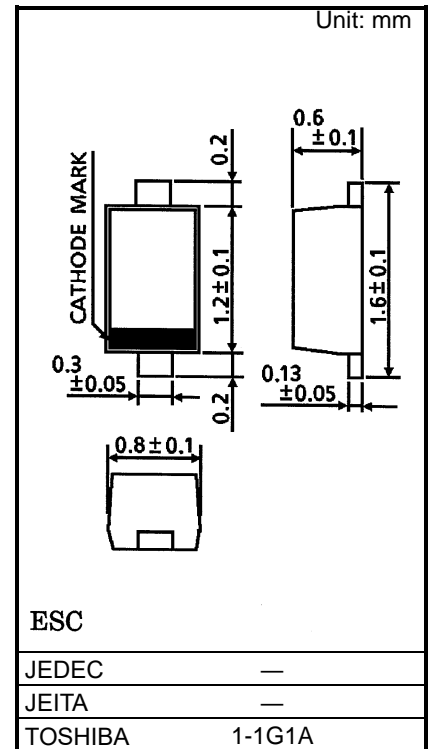
## Ultra High Speed Switching Applications

- AEC-Q101 Qualified (Note1)
- Compact 2-pin package – ideal for high-density mounting
- Low forward voltage :  $V_F(3) = 0.98 \text{ V (typ.)}$
- Fast reverse recovery time:  $t_{rr} = 1.6 \text{ ns (typ.)}$
- Small total capacitance :  $C_T = 0.5 \text{ pF (typ.)}$

Note1: For detail information, please contact our sales.

## Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	$V_{RM}$	85	V
Reverse voltage	$V_R$	80	V
Maximum (peak) forward current	$I_{FM}$	200	mA
Average forward current	$I_O$	100	mA
Surge current (10ms)	$I_{FSM}$	1	A
Power dissipation	$P_D$ (Note 2, 4)	200	mW
	$P_D$ (Note 3, 4)	150	
Junction temperature	$T_j$ (Note 2)	150	°C
	$T_j$ (Note 3)	125	
Storage temperature	$T_{stg}$ (Note 2)	-55 to 150	°C
	$T_{stg}$ (Note 3)	-55 to 125	



Weight: 1.4mg (typ)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: For devices with the ordering part number ending in L3F(T).

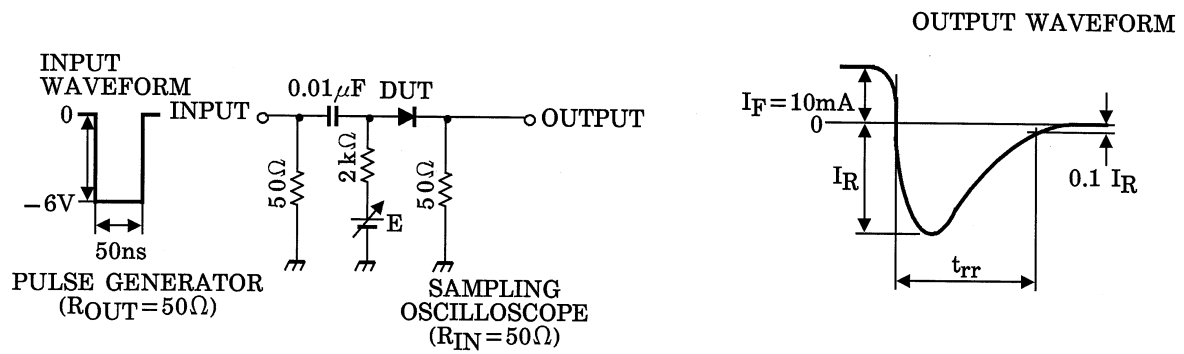
Note 3: For devices with the ordering part number in other than L3F(T).

Note 4: Mounted on a glass epoxy circuit board of 20 mm × 20 mm, pad dimension of 4 mm × 4mm.

Start of commercial production  
1994-11

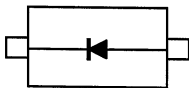
## Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Forward voltage	V <sub>F</sub> (1)	I <sub>F</sub> = 1 mA	—	0.62	—	V
	V <sub>F</sub> (2)	I <sub>F</sub> = 10 mA	—	0.75	—	
	V <sub>F</sub> (3)	I <sub>F</sub> = 100 mA	—	0.98	1.20	
Reverse current	I <sub>R</sub> (1)	V <sub>R</sub> = 30 V	—	—	0.1	μA
	I <sub>R</sub> (2)	V <sub>R</sub> = 80 V	—	—	0.5	
Total capacitance	C <sub>T</sub>	V <sub>R</sub> = 0 V, f = 1 MHz	—	0.5	3.0	pF
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 10 mA, Fig.1	—	1.6	4.0	ns

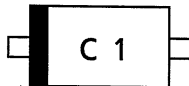


**Fig.1 Reverse Recovery Time (t<sub>rr</sub>) Test Circuit**

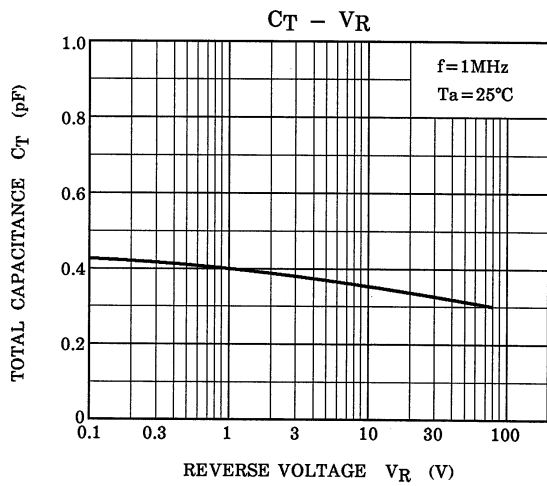
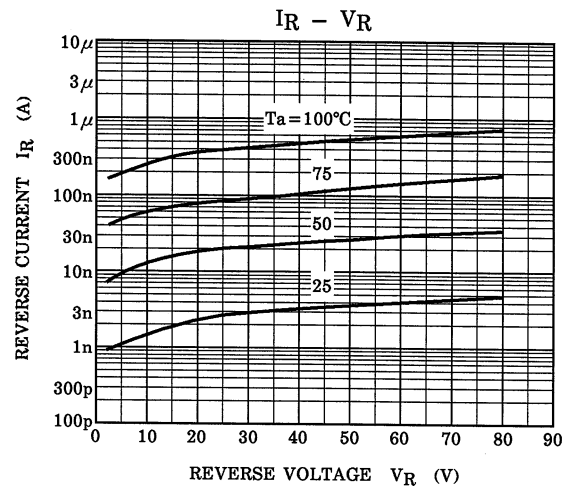
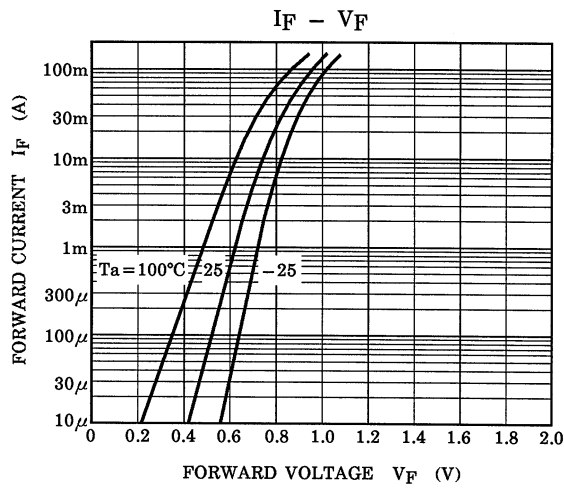
### Equivalent circuit (Top View)



### Marking



## Characteristics Curves



The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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