

Field Effect Transistors Silicon N-Channel MOS

RFM03U3P

1. Applications

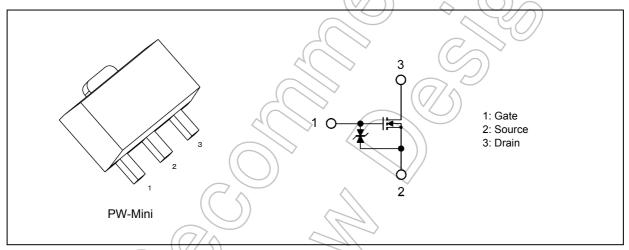
· VHF/UHF-Band Power Amplifiers

Note: This product is intended for radio-frequency power amplifiers of telecommunications equipment. This product is neither intended nor warranted for any other use. Do not use this product except for radio-frequency power amplifiers of telecommunications equipment.

2. Features

- (1) Output power: $P_0 = 3.0 \text{ W (typ.)}$
- (2) High gain: $G_{PS} = 14.8 \text{ dB (typ.)}$
- (3) Drain efficiency: $\eta_D = 60 \%$ (typ.)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Drain-source voltage	V_{DSS}		16	V
Gate-source voltage	V _{GSS}		3	V
Drain current	ID	/	2.5	Α
Power dissipation	(P _D	(Note 1)	7	W
Channel temperature	T _{ch}		150	°C
Storage temperature	T _{stg}		-45 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: T_c = 25 °C (When mounted on a 0.4 mm (t) glass-epoxy PCB with heatsink)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

Note: Care should be taken not to drop this device because it is sensitive to dropping impact stress.

Start of commercial production



5. Electrical Characteristics (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Drain-source leakage current	I _{DSS}		V _{DS} = 10 V, V _{GS} = 0 V	_	_	10	μА
Gate-source leakage current	I _{GSS}		V _{GS} = 3 V	_	_	5	μА
Gate threshold voltage	V _{th}		$V_{DS} = 3.6 \text{ V}, I_{D} = 0.1 \text{ mA}$	0.1	0.6	1.1	>
Output power	Po		V_{DS} = 3.6 V, I_{idle} = 500 mA (V_{GS} =	2.3	3.0	_	W
Drain efficiency	η_{D}		adjust), f = 470 MHz, P _I = 0.1 W, Z _G = Z _L = 50	50	60	_	%
Power gain	G _{PS}		Ω	13.6	14.8	_	dB
Maximum load mismatch without damage	_	(Note 1)	V_{DS} = 3.6 V, P_{O} = 3 W (P_{I} = adjust), I_{idle} = 500 mA (V_{GS} = adjust), f = 470 MHz, VSWR LOAD 20:1 all phase	<u>Z</u>	_	_	

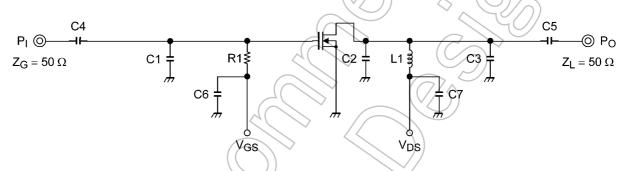
Note: These performance characteristics were measured using Toshiba-specified tools.

Note 1: Not damaged

6. Output Power Test Fixture

(Test condition: f = 470 MHz, $V_{DS} = 3.6 \text{ V}$, $I_{idle} = 500 \text{ mA}$, $P_I = 0.1 \text{ W}$

C7: 30 µF

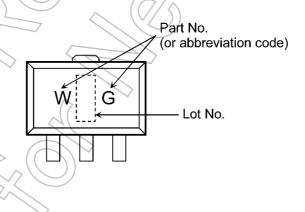


C3: 20 pF C6: 30 μF

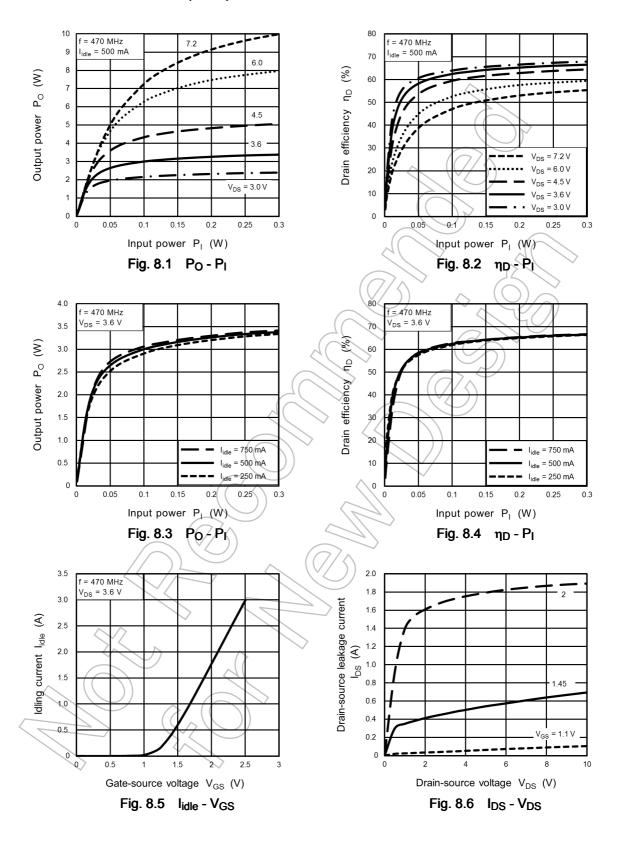
L1: φ0.5 mm enamel wire, 5.0 ID, 8.5 T

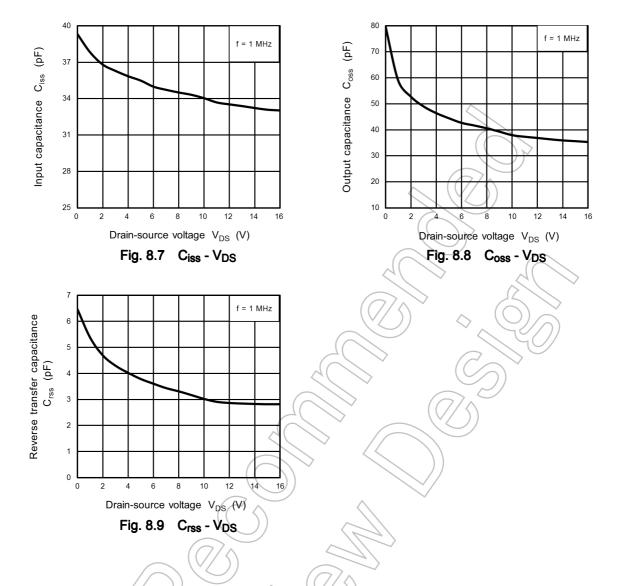
R1: 470 Ω

7. Marking



8. Characteristics Curves (Note)



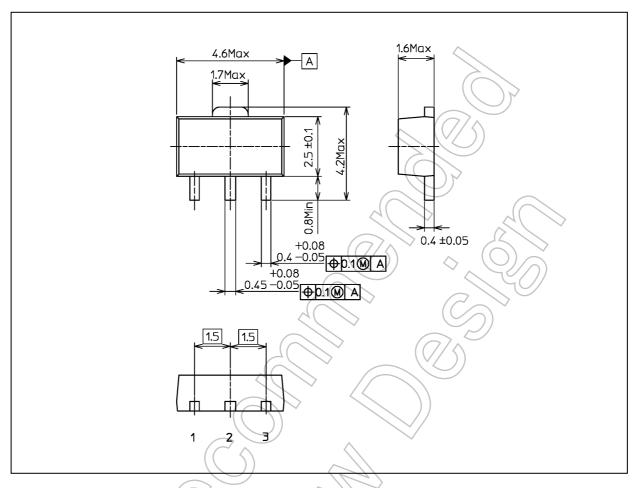


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

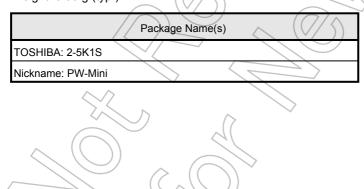


Package Dimensions

Unit: mm



Weight: 0.05 g (typ.)



Rev.1.0



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