TOSHIBA Diode Silicon Epitaxial Planar Type

JDV2S14E

Useful for VCO/TCXO

• Small Package

• High Capacitance Ratio: $C_{1V} / C_{2.5V} = 2.15$ (typ.)

• Low Series Resistance : $r_s = 0.4 \Omega$ (typ.)

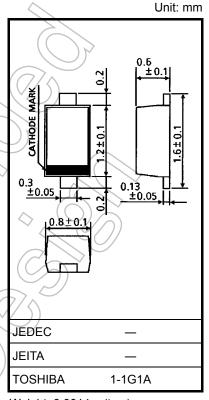
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Reverse voltage	V _R	10	(N)
Junction temperature	Tj	125	(°C)
Storage temperature range	T _{stg}	-55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling

Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.0014 g (typ.)

Electrical Characteristics (Ta = 25°C)

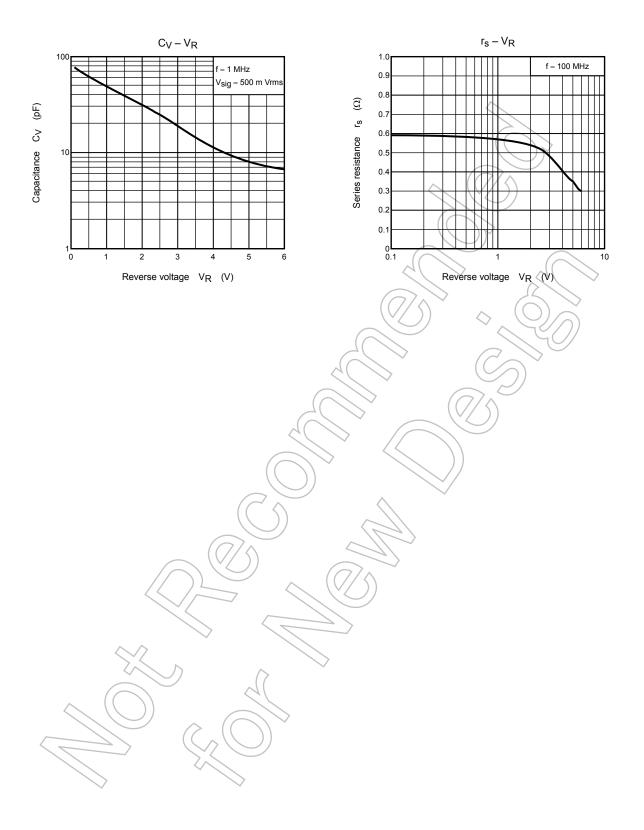
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse voltage	VR	$I_R = 1 \mu A$	10	_	_	V
Reverse current	IR	V _R = 10 V	_	_	3	nA
	€ _{0.5} ∨	V _R = 0.5 V, f = 1 MHz	56.3		64.7	
Capacitance	C _{1V}	V _R = 1 V, f = 1 MHz	44		49.5	pF
	C _{2.5V}	V _R = 2.5 V, f = 1 MHz	19		26.5	
	C _{4V}	V _R = 4 V, f = 1 MHz	9.2		12	
Capacitance ratio	C _{0.5V} / C _{1V}	_	1.25		1.35	
Capacitatice failo	C _{1V} / C _{2.5V}	_	1.99	2.15	2.3	
Series resistance	r _s	V _R = 4 V, f = 100 MHz	_	0.4	0.8	Ω

Note: Signal level when capacitance is measured. $V_{sig} = 500 \text{ mV}_{rms}$

Marking



Start of commercial production 2000-04



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