

Figure 1. Circuit Configuration (top view)

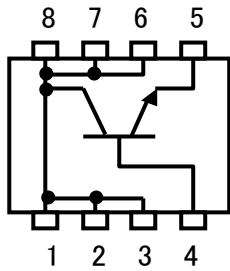
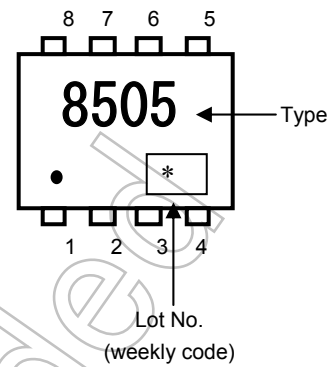
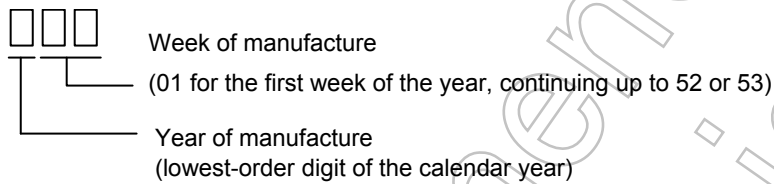


Figure 2. Marking (Note 4)



Note 4: ● on the lower left of the marking indicates Pin 1.

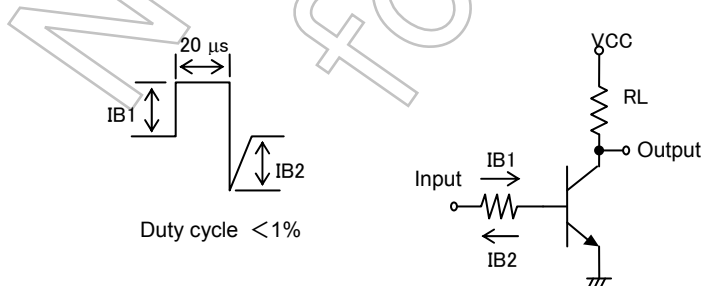
* Weekly code (three digits):

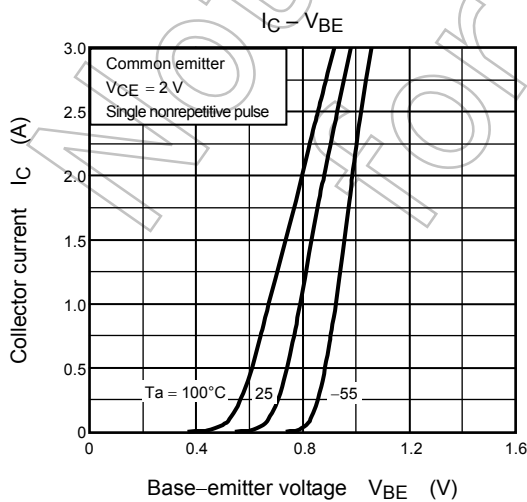
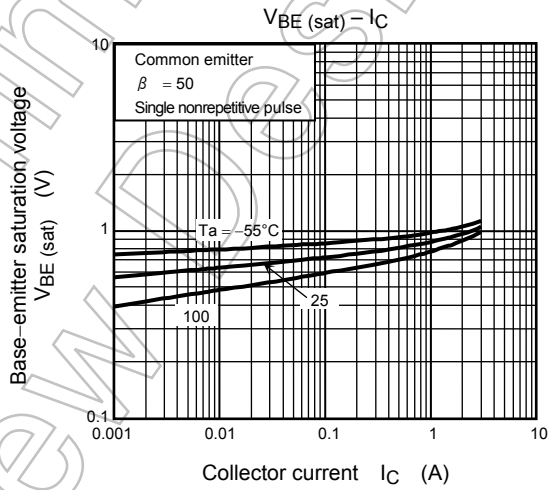
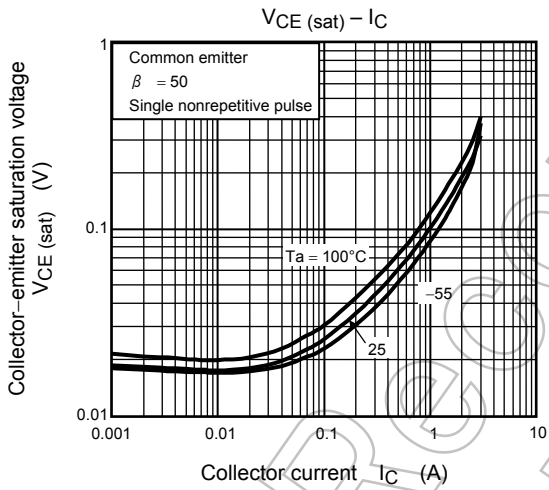
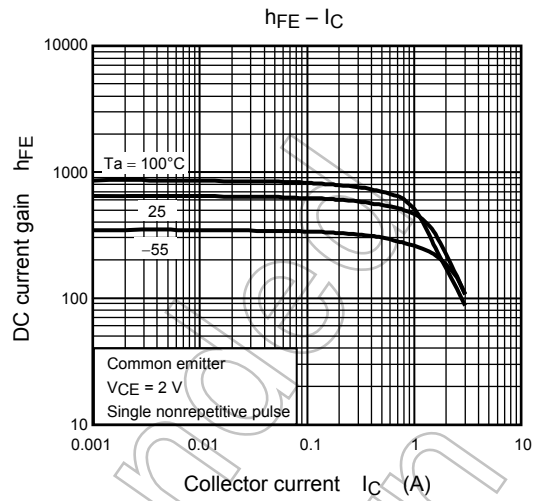
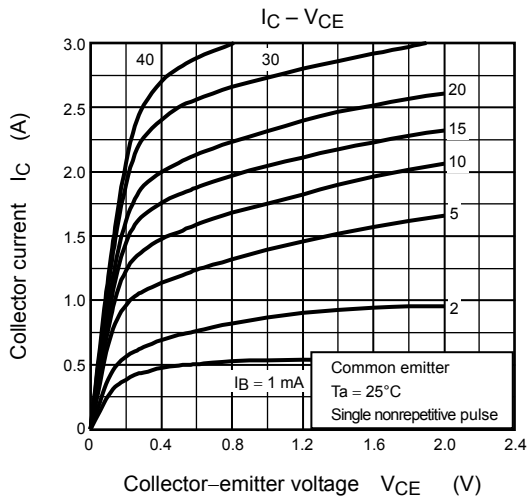


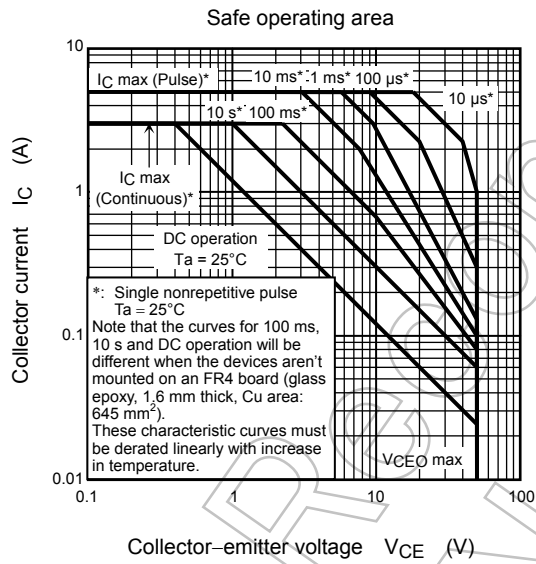
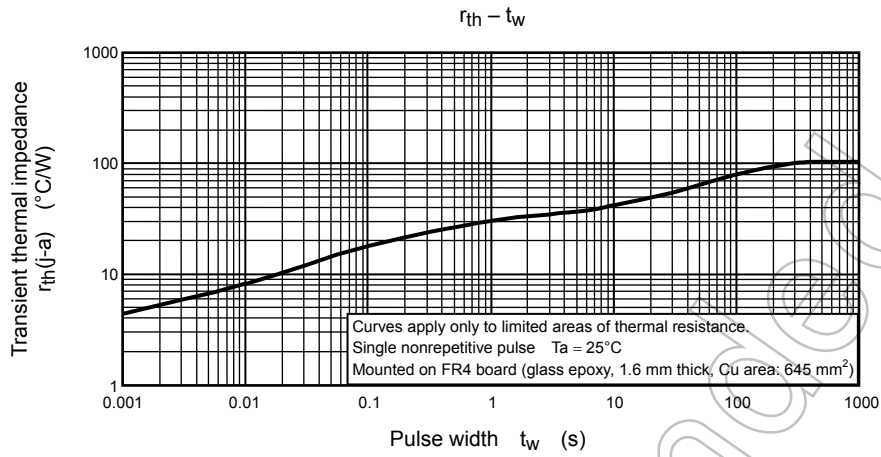
Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	I_{CBO}	$V_{CB} = 100\text{ V}, I_E = 0$	—	—	100	nA
Emitter cut-off current	I_{EBO}	$V_{EB} = 7\text{ V}, I_C = 0$	—	—	100	nA
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = 1\text{ mA}, I_B = 0$	100	—	—	V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 10\text{ mA}, I_B = 0$	50	—	—	V
DC current gain	$h_{FE}(1)$	$V_{CE} = 2\text{ V}, I_C = 0.3\text{ A}$	400	—	1000	
	$h_{FE}(2)$	$V_{CE} = 2\text{ V}, I_C = 1.0\text{ A}$	200	—	—	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 1\text{ A}, I_B = 20\text{ mA}$	—	—	0.14	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C = 1\text{ A}, I_B = 20\text{ mA}$	—	—	1.1	V
Switching time	Rise time	t_r	—	40	—	ns
	Storage time	t_{stg}	—	500	—	
	Fall time	t_f	—	120	—	

Figure 3. Switching Time Test Circuit & Timing Chart







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