TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

GT5G133

Strobe Flash Applications

• Enhancement-mode

• Low gate drive voltage: $V_{GE} = 2.5 \text{ V (min)} (@I_{C} = 130 \text{ A})$

• Peak collector current: $I_C = 130 \text{ A (max)}$

• Compact and Thin (TSON-8) package

Absolute Maximum Ratings (Ta = 25°C)

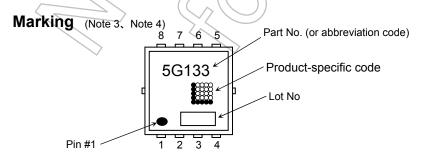
Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	400	M	
Gate-emitter voltage	DC	V _{GES}	± 4		
	Pulse	V_{GES}	± 5	$(\langle / / \rangle)$	
Collector current	Pulse (Note 1)	I _{CP}	130	*	
Collector power dissipation(t = 10 s)	(Note 2a)	P _C (1)	0.83	W	
	(Note 2b)	P _C (2)	0.69	> w	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

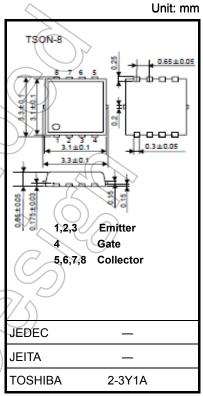
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal resistance , junction to ambient (t = 10 s) (Note2a)	R _{th (j-a) (1)}	150	°C/W
Thermal resistance , junction to ambient (t = 10 s) (Note2b)	R _{th} (j-a) (2)	180	°C/W

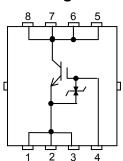


Note: For (Note 1), (Note 2a), (Note 2b), (Note 3) and (Note 4).



Weight: 0.02 g (Typ.)

Circuit Configuration

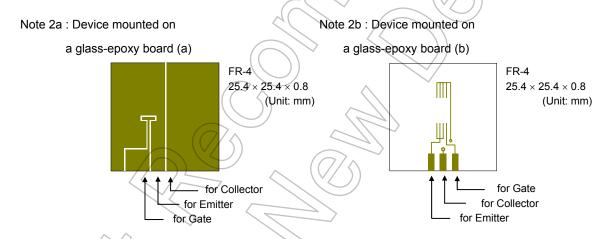


Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Gate leakage cur	rent	I _{GES}	$V_{GE} = \pm 4 \text{ V}, V_{CE} = 0$	_	_	± 10	μА
Collector cut-off of	current	I _{CES}	V _{CE} = 400 V, V _{GE} = 0	_	_	10	μА
Gate-emitter cut-	off voltage	V _{GE} (OFF)	I _C = 1 mA, V _{CE} = 5 V	0.55	_	1.05	٧
Collector-emitter	saturation voltage	V _{CE} (sat)	I _C = 130 A, V _{GE} = 2.5 V		3.0	_	٧
Input capacitance	9	C _{ies}	V _{CE} = 10 V, V _{GE} = 0, f = 1 MHz	(F	2480	_	pF
Switching time	Rise time	t _r	$\begin{array}{c c} 3 \ V \\ 0 \\ \hline \\ 0 \\ \hline \\ V_{IN}: \ t_r \leq 100 \ ns \\ t_f \leq 100 \ ns \\ \hline \\ Duty \ cycle \leq 1\% \\ \end{array} \\ \begin{array}{c c} 62 \ \Omega \\ \hline \\ 0 \\ \hline \\ 300V \\ \hline \end{array}$) 	2.0		- µs
	Turn-on time	t _{on})_	2.1		
	Fall time	t _f			1.5		
	Turn-off time	t _{off}		- /	2.1		

Note

Note 1: Please use devices on condition that the junction temperature is below 150°C Repetitive rating: pulse width limited by maximum junction temperature.



Note 3: ● on lower right of the marking indicates Pin 1.

* Weekly code: (Three digits)

Week of manufacture (01 for first week of year, continues up to 52 or 53)

Year of manufacture (One low-order digits of calendar year)

Note 4: A line under a Lot No. identifies the indication of product Labels. [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Caution on handling

This device is MOS gate type. Therefore, please care of a protection from ESD in your handling.

Caution in design

●You should be design dV/dt value is under 400V/µs below figure.1.

The slope of V_{CE} from 30v to 90v (attached figure.1)

$$dv/dt = (90V-30V) / (\triangle t)$$

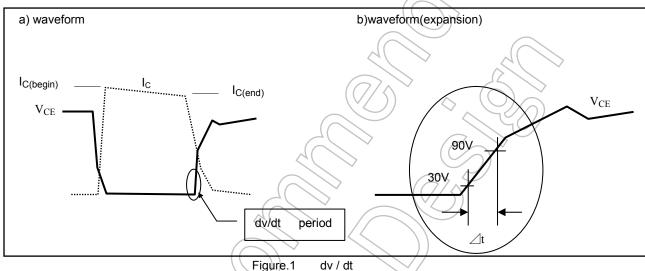
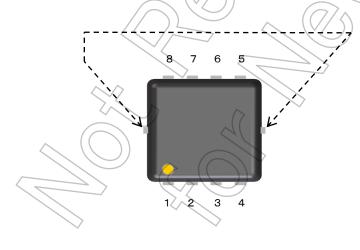


Figure.1

Outside pin

Outside pin is same collector voltage.

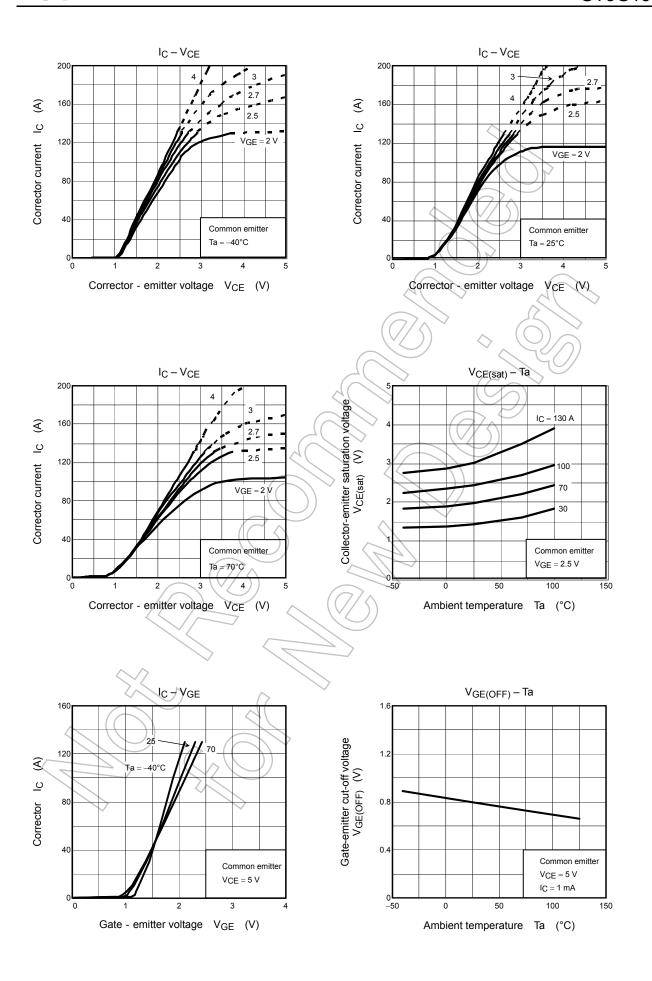


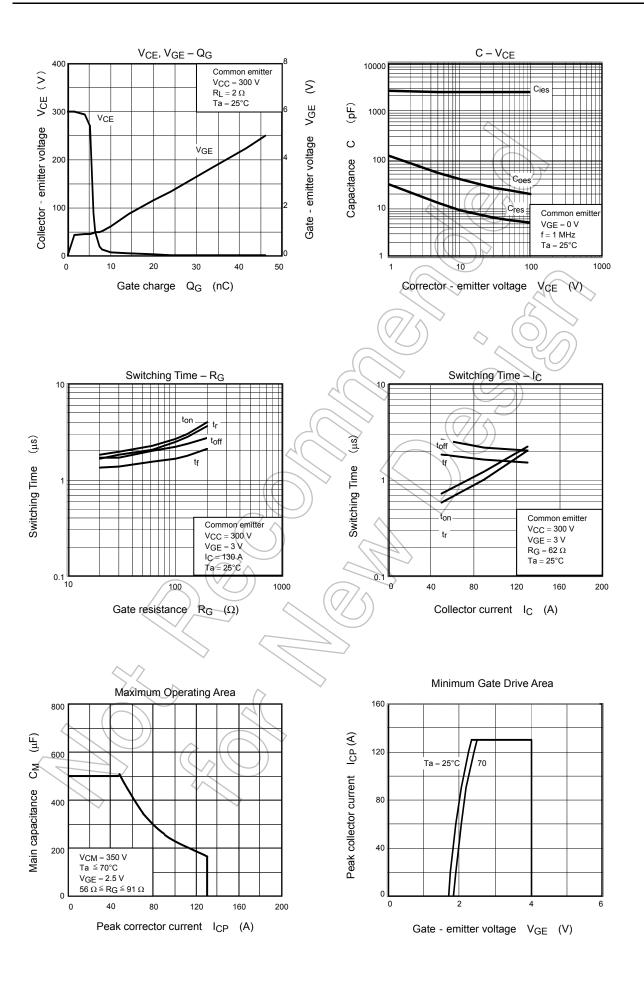
These outside pin are same collector voltage.

PIN ASSIGNMENT

: Emitter 1, 2, 3 : Gate 5, 6, 7, 8 : Collector

3 2010-01-15





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6