

CMOS Digital Integrated Circuits Silicon Monolithic

TC74VCX138FT

1. Functional Description

Low-Voltage 3-to-8 Line Decoder with 3.6-V Tolerant Inputs and Outputs

2. General

The TC74VCX138FT is a high performance CMOS 3-to-8 decoder which is guaranteed to operate from 1.2 V to 3.6 V. Designed for use in 1.5 V, 1.8 V, 2.5 V or 3.3 V systems, it achieves high speed operation while maintaining the CMOS low power dissipation.

It is also designed with over voltage tolerant inputs and outputs up to 3.6 V.

When the device is enabled, 3 binary select inputs (A, B and C) determine which one of the outputs $(\overline{Y}0 \text{ to } \overline{Y}7)$ will go low.

When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go high.

G1, $\overline{G}2A$ and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

All inputs are equipped with protection circuits against static discharge.

3. Features

- (1) Wide operating temperature range: $T_{opr} = -40$ to 125 °C (Note 1)
- (2) Low-voltage operation: $V_{CC} = 1.2$ to 3.6 V
- (3) High-speed operation: $t_{pd} = 3.5 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$

 $t_{pd} = 4.1 \text{ ns (max)} (V_{CC} = 2.3 \text{ to } 2.7 \text{ V})$

 $t_{pd} = 8.2 \text{ ns (max)} (V_{CC} = 1.65 \text{ to } 1.95 \text{ V})$

 $t_{pd} = 16.4 \text{ ns (max) (V}_{CC} = 1.4 \text{ to } 1.6 \text{ V})$

 $t_{pd} = 41.0 \text{ ns (max) (V}_{CC} = 1.2 \text{ V)}$

(4) Output current: $I_{OH}/I_{OL} = \pm 24$ mA (min) ($V_{CC} = 3.0$ V)

 $I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$

 $I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.65 \text{ V)}$

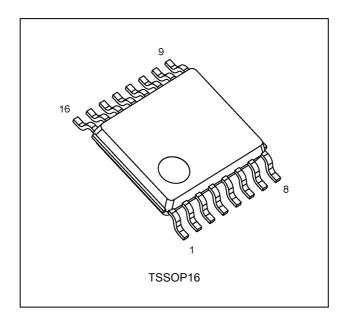
 $I_{OH}/I_{OL} = \pm 2$ mA (min) ($V_{CC} = 1.4$ V)

(5) 3.6 V tolerant function and power-down protection provided on all inputs and outputs.

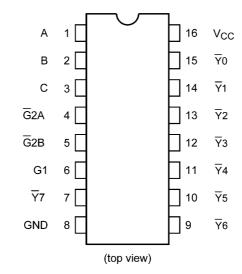
Note 1: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.



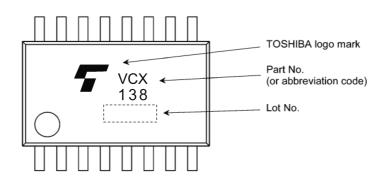
4. Packaging



5. Pin Assignment



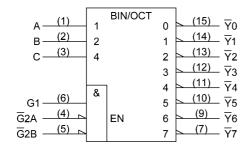
6. Marking

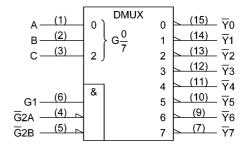


Rev.3.0



7. IEC Logic Symbol





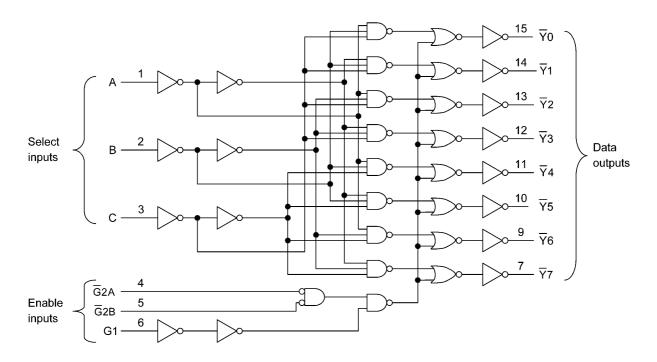
8. Truth Table

		Inp	uts				Outputs								
	Enable			Select		_ Y0	<u>7</u> 1	√ Y2	<u>7</u> 3			<u>7</u> 6	7 7	Selected Output	
G1	G ₂ A	G ₂ B	C	В	Α	10	1 1	12	13	14	13	10	1 /	·	
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None	
Х	Н	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None	
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None	
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	<u></u>	
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	₹1	
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	<u>₹</u> 2	
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	<u></u>	
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Y 4	
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	<u>₹</u> 5	
Н	L	L	Ι	Н	L	Н	Н	Н	Н	Н	Н	L	Н	<u>₹</u> 6	
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Y 7	

X: Don't care



9. System Diagram



10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 4.6	V
Input voltage	V _{IN}		-0.5 to 4.6	V
Output voltage	V _{OUT}	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-50	mA
Output diode current	I _{OK}	(Note 3)	±50	mA
Output current	I _{OUT}		±50	mA
Power dissipation	P _D	(Note 4)	180	mW
V _{CC} /ground current	I _{CC} /I _{GND}		±100	mA
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High (H) or Low (L) state. I_{OUT} absolute maximum rating must be observed.

Note 3: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Note 4: 180 mW in the range of T_a = -40 to 85 °C. From T_a = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.



11. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		1.2 to 3.6	V
Input voltage	V _{IN}		-0.3 to 3.6	V
Output voltage	V _{OUT}	(Note 1)	0 to 3.6	V
		(Note 2)	0 to V _{CC}	
Output current	I _{OH} ,I _{OL}	(Note 3)	±24	mA
		(Note 4)	±18	
		(Note 5)	±6	
		(Note 6)	±2	
Operating temperature	T _{opr}	(Note 7)	-40 to 125	°C
Input rise and fall times	dt/dv	(Note 8)	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 1: Output in OFF state.

Note 2: High (H) or Low (L) state.

Note 3: V_{CC} = 3.0 to 3.6 V

Note 4: V_{CC} = 2.3 to 2.7 V

Note 5: V_{CC} = 1.65 to 1.95 V

Note 6: V_{CC} = 1.4 to 1.6 V

Note 7: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 8: V_{IN} = 0.8 to 2.0 V , V_{CC} = 3.0 V



12. Electrical Characteristics

12.1. DC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		1.2 to 1.4	$V_{CC} \times 0.8$	_	V
				1.4 to 1.65	V _{CC} × 0.65	_	
				1.65 to 2.3	$V_{CC} \times 0.65$	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V _{IL}	_		1.2 to 1.4	_	V _{CC} × 0.05	V
				1.4 to 1.65	_	$V_{CC} \times 0.05$	
				1.65 to 2.3	_	$V_{CC} \times 0.2$	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.2	V _{CC} - 0.1	_	V
				1.4 to 1.65	V _{CC} - 0.2	_	
				1.65 to 3.6	V _{CC} - 0.2	_	
			I _{OH} = -2 mA	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.25	_	
				2.3	2.0	_	
			I _{OH} = -12 mA	2.3	1.8	_	
				2.7	2.2	_	
			I _{OH} = -18 mA	2.3	1.7	_	
				3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	1.2	_	0.05	V
				1.4 to 1.65	_	0.05	
				1.65 to 3.6	_	0.2	
			I _{OL} = 2 mA	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	_	0.3	
			I _{OL} = 12 mA	2.3	_	0.4	
				2.7	_	0.4	
			I _{OL} = 18 mA	2.3	_	0.6	
				3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		1.2 to 3.6	_	±5.0	μΑ
Power-OFF leakage current	I _{OFF}	$V_{IN}/V_{OUT} = 0$ to 3.6 V		0	_	10.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		1.2 to 3.6	_	20.0	μΑ
		$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		1.2 to 3.6	_	±20.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per 1 input)		2.7 to 3.6	_	750	μА



12.2. DC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	on	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		1.2 to 1.4	$V_{CC} \times 0.8$	_	V
				1.4 to 1.65	$V_{CC} \times 0.65$	_	
				1.65 to 2.3	$V_{CC} \times 0.65$	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V _{IL}	_		1.2 to 1.4	_	V _{CC} × 0.05	V
				1.4 to 1.65	_	$V_{CC} \times 0.05$	
				1.65 to 2.3	_	$V_{CC} \times 0.2$	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.2	V _{CC} - 0.1	_	V
				1.4 to 1.65	V _{CC} - 0.2	_	
				1.65 to 3.6	V _{CC} - 0.2	_	
			I _{OH} = -2 mA	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.25	_	
				2.3	2.0	_	
			I _{OH} = -12 mA	2.3	1.8	_	
				2.7	2.2	_	
			I _{OH} = -18 mA	2.3	1.6	_	
				3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	1.2	_	0.05	V
				1.4 to 1.65	_	0.05	
				1.65 to 3.6		0.2	
			I _{OL} = 2 mA	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	_	0.3	
			I _{OL} = 12 mA	2.3	_	0.4	
				2.7	_	0.4	
			I _{OL} = 18 mA	2.3	_	0.8	
				3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		1.2 to 3.6		±20.0	μΑ
Power-OFF leakage current	I _{OFF}	$V_{IN}/V_{OUT} = 0$ to 3.6 V		0	_	40.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		1.2 to 3.6	_	80.0	μΑ
	I _{CC}	$V_{CC} \leq V_{IN} \leq 3.6 \ V$		1.2 to 3.6	_	±80.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per 1 input)		2.7 to 3.6	_	1.5	mA

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.



12.3. AC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	41.0	ns
$(A,B,C - \overline{Y})$			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	16.4	
				1.8 ± 0.15	1.5	8.2	
				2.5 ± 0.2	8.0	4.1	
				3.3 ± 0.3	0.6	3.5	
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	41.0	ns
(G1 - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	16.4	
				1.8 ± 0.15	1.5	8.2	
				2.5 ± 0.2	0.8	4.1	
				3.3 ± 0.3	0.6	3.5	
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	41.0	ns
(G2 - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	16.4	
				1.8 ± 0.15	1.5	8.2	
				2.5 ± 0.2	0.8	4.1	
				3.3 ± 0.3	0.6	3.5	

12.4. AC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	53.0	ns
$(A,B,C - \overline{Y})$			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	20.9	
				1.8 ± 0.15	1.5	9.7	
				2.5 ± 0.2	0.8	4.9	
				3.3 ± 0.3	0.6	4.2	
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	53.0	ns
(G1 - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	20.9	
				1.8 ± 0.15	1.5	9.7	
				2.5 ± 0.2	0.8	4.9	
				3.3 ± 0.3	0.6	4.2	
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	1.2	3.0	53.0	ns
(G2 - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	20.9	
				1.8 ± 0.15	1.5	9.7	
				2.5 ± 0.2	0.8	4.9	
				3.3 ± 0.3	0.6	4.2	

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.



12.5. Dynamic Switching Characteristics (Note) (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	0.25	V
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	0.6	
		V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	
Quiet output minimum dynamic V _{OL}	V _{OLV}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	-0.25	\ \
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	-0.6	
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	-0.8	
Quiet output minimum dynamic V _{OH}	V _{OHV}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	1.5	\ \
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	1.9	
		V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	2.2	

Note: Parameter guaranteed by design.

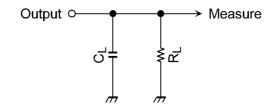
12.6. Capacitive Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		_	1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C _{PD}	(Note 1)	f _{IN} = 10 MHz	1.8, 2.5, 3.3	40	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC}(opr) = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}$



12.7. AC Test Circuit



12.8. AC Waveform

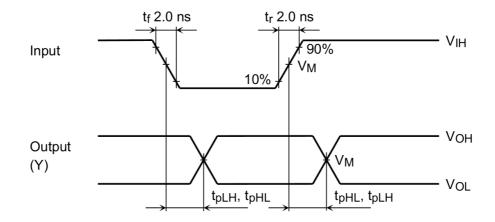


Fig. 12.8.1 t_{PLH}, t_{PHL}

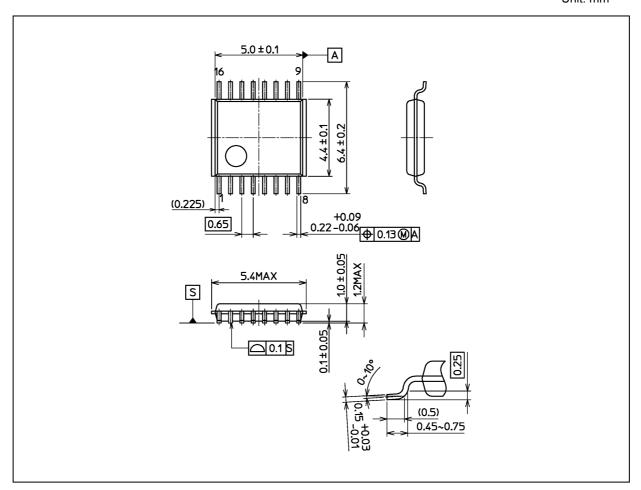
Table 12.8.1 AC Waveform Symbols

	Symbol	V_{CC} = 3.3 \pm 0.3 V	V_{CC} = 2.5 ± 0.2 V V_{CC} = 1.8 ± 0.15 V	$V_{CC} = 1.5 \pm 0.1 \text{ V}$ $V_{CC} = 1.2 \text{ V}$
Input	V _{IH}	2.7 V	V _{CC}	V _{CC}
	V _M	1.5 V	V _{CC} /2	V _{CC} /2
	t _r , t _f	2.0 ns	2.0 ns	2.0 ns
Output	V_{M}	1.5 V	V _{CC} /2	V _{CC} /2
Load	C _L	30 pF	30 pF	15 pF
	R _L	500 Ω	500 Ω	2 kΩ



Package Dimensions

Unit: mm



Weight: 0.06 g (typ.)

	Package Name(s)
Nickname: TSSOP16	



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