TOSHIBA CMOS LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TC75W51FU, TC75W51FK**

# **DUAL OPERATIONAL AMPLIFIER**

TC75W51 is a CMOS operational amplifier with low supply voltage, low supply current.

#### **FEATURES**

• Low supply voltage :  $V_{DD} = \pm 0.75 \sim \pm 3.5V$  or  $1.5 \sim 7V$ 

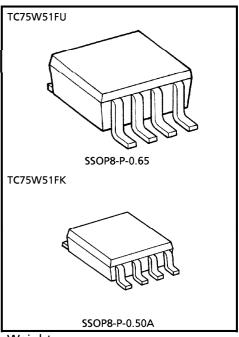
• Low supply current :  $I_{DD}$  ( $V_{DD} = 3V$ ) =  $120 \mu A$  (Typ.)

The internally phase compensated operational amplifier.

Small package

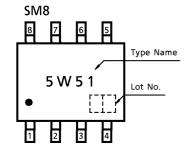
#### MAXIMUM RATINGS (Ta = 25°C)

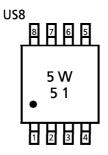
CHARACTERISTIC	SYMBOL	RATING	N	
Supply Voltage	V <sub>DD</sub> , V <sub>SS</sub>	7	V	
Differential Input Voltage	DVIN	± 7	V	
Input Voltage	V <sub>IN</sub>	$V_{DD} \sim V_{SS}$	V	
Power Dissipation	D-	250 (SM8)	mW	
	PD	200 (US8)		
Operating Temperature	T <sub>opr</sub>	<i>-</i> 40∼85	°C	
Storage Temperature	T <sub>stg</sub>	<b>-</b> 55∼125	°C	



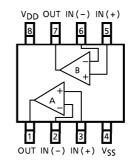
Weight SSOP8-P-0.65 : 0.021g (Typ.) SSOP8-P-0.50A : 0.01g (Typ.)

#### MARKING (TOP VIEW)





# PIN CONNECTION (TOP VIEW)



## **ELECTRICAL CHARACTERISTICS**

DC CHARACTERISTICS ( $V_{DD} = 3.0V$ ,  $V_{SS} = GND$ , Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V <sub>IO</sub>	1	$R_S = 1k\Omega$ , $R_F = 100k\Omega$	_	2	10	mV
Input Offset Current	lo	_	_	_	1	_	pА
Input Bias Current	lį	_	_	_	1	_	pΑ
Common Mode Input Voltage	CMVIN	2	$R_S = 1k\Omega$ , $R_F = 100k\Omega$	0	_	2.5	V
Voltage Gain (Open Loop)	GV	_	_	60	70	_	dB
Maximum Output Voltage VOH VOL	Voн	3	$R_L \ge 100 k\Omega$	2.9	_	_	V
	VOL	4	$R_L \ge 100 k\Omega$	_	_	0.1	٧
Common Mode Input Signal Rejection Ratio	CMRR	2	V <sub>IN</sub> = 0.0~2.5V	55	65	_	dB
Supply Voltage Rejection Ratio	SVRR	1	$V_{DD} = 1.5 \sim 7.0 V$	60	70	_	dB
Supply Current	lDD	5	_	_	120	400	$\mu$ A

# DC CHARACTERISTICS ( $V_{DD} = 1.5V$ , $V_{SS} = GND$ , $T_{a} = 25$ °C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V <sub>IO</sub>	1	$R_S = 10k\Omega$ , $R_F = 100k\Omega$	_	2	10	mV
Input Offset Current	lο	_	_	_	1	_	pА
Input Bias Current	Ц	_	_	_	1	_	pА
Common Mode Input Voltage	CMVIN	2	$R_S = 10k\Omega$ , $R_F = 100k\Omega$	0	_	1.0	٧
Voltage Gain (Open Loop)	$G_V$	_	_	60	70	_	dB
Maximum Output Voltage	Voн	3	$R_L \ge 100 k\Omega$	1.4	_	_	٧
	VOL	4	$R_L \ge 100 k\Omega$	_	_	0.1	٧
Supply Current	I <sub>DD</sub>	5	_	_	100	300	$\mu$ A

(Note) This device should be operated less than  $70\mu A$  source current.

# AC CHARACTERISTICS ( $V_{DD} = 3.0V$ , $V_{SS} = GND$ , $T_{a} = 25$ °C)

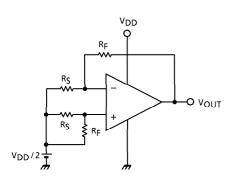
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	_	$A_V = 0dB$		0.5	_	<b>V</b> / μ <b>s</b>
Unity Gain Cross Frequency	f <sub>T</sub>	_	$A_V = 40dB$		0.6	_	MHz

# AC CHARACTERISTICS ( $V_{DD} = 1.5V$ , $V_{SS} = GND$ , $T_{a} = 25^{\circ}C$ )

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	_	$A_V = 0dB$	_	0.3		<b>V</b> / μ <b>s</b>
Unity Gain Cross Frequency	f <sub>T</sub>	_	$A_V = 40dB$	_	0.5		MHz

#### **TEST CIRCUIT**

## 1. SVRR, V<sub>IO</sub>

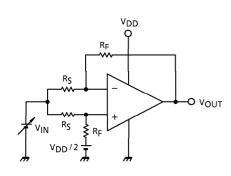


SVRR

$$\begin{split} &V_{DD} = 1.5 \text{V} : V_{DD} = V_{DD}1, \ V_{OUT} = V_{OUT}1 \\ &V_{DD} = 7.0 \text{V} : V_{DD} = V_{DD}2, \ V_{OUT} = V_{OUT}2 \\ &SVRR = 20 \ell \text{og} \left( \left| \frac{V_{OUT}1 - V_{OUT}2}{V_{DD}1 - V_{DD}2} \right| \times \frac{R_S}{R_F + R_S} \right) \end{split}$$

• 
$$V_{IO}$$
  
 $V_{IO} = \left(V_{OUT} - \frac{V_{DD}}{2}\right) \times \frac{R_S}{R_F + R_S}$ 

### 2. CMRR, CMVIN

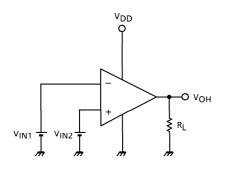


CMRR

$$\begin{split} &V_{IN} = 0.0V \ : \ V_{IN} = V_{IN}1, \ V_{OUT} = V_{OUT}1 \\ &V_{IN} = 2.5V \ : \ V_{IN} = V_{IN}2, \ V_{OUT} = V_{OUT}2 \\ &CMRR = 20 \ell og \left( \left| \frac{V_{OUT}1 - V_{OUT}2}{V_{IN}1 - V_{IN}2} \right| \times \frac{R_S}{R_F + R_S} \right) \end{split}$$

CMV<sub>IN</sub>

## 3. VOH

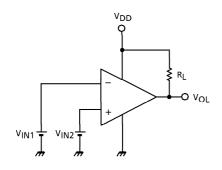


VOF

$$V_{IN1} = \frac{V_{DD}}{2} - 0.05V$$

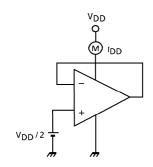
$$V_{IN2} = \frac{V_{DD}}{2} + 0.05V$$

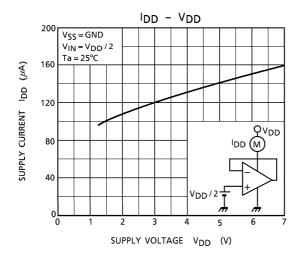
4. V<sub>OL</sub>

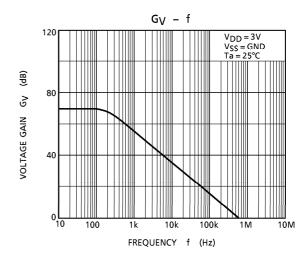


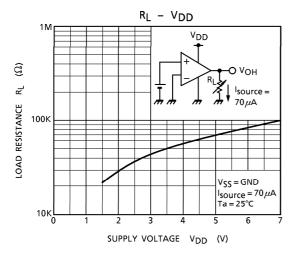
 $V_{OL}$   $V_{IN1} = \frac{V_{DD}}{2} + 0.05V$   $V_{IN2} = \frac{V_{DD}}{2} - 0.05V$ 

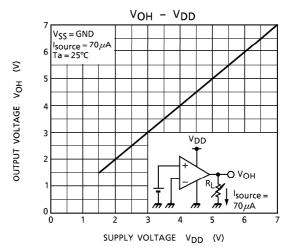
5. I<sub>DD</sub>

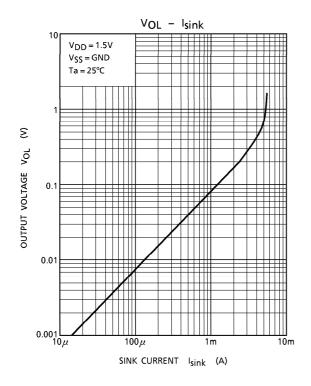


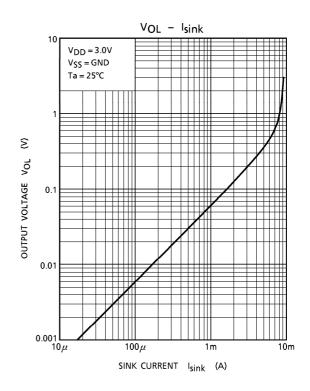


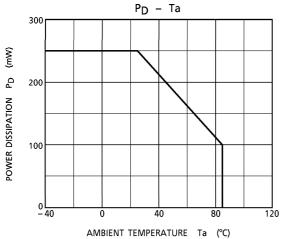






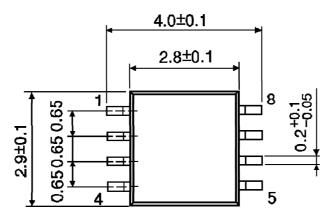


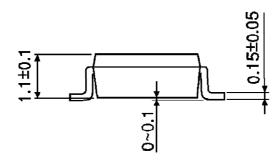




## OUTLINE DRAWING SSOP8-P-0.65

Unit: mm



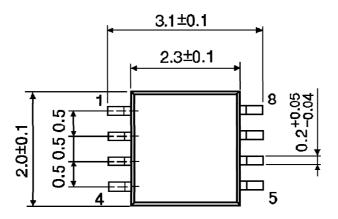


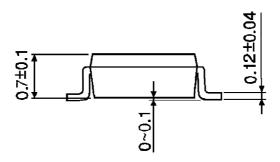
Weight: 0.021g (Typ.)

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## OUTLINE DRAWING SSOP8-P-0.50A

Unit: mm





Weight: 0.01g (Typ.)

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