TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7PA04FU

Inverter with 3.6 V Tolerant Input and Output

#### **Features**

Low voltage operation :  $V_{CC}$  = 1.8 to 3.6 V

High speed operation :  $t_{pd}$  = 2.8 ns (max) ( $V_{CC}$  = 3.0 to 3.6 V)

:  $t_{pd}$  = 3.7 ns (max) (V<sub>CC</sub> = 2.3 to 2.7 V)

 $: t_{pd} = 7.4 \text{ ns (max) (V}_{CC} = 1.8 \text{ V)}$ 

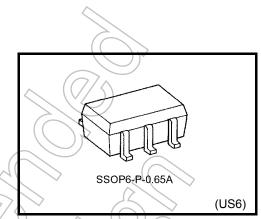
High Output current  $: I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$ 

 $: I_{OH}/I_{OL} = \pm 18 \text{ mA (min)} (V_{CC} = 2.3 \text{ V})$ 

 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min)} (V_{CC} = 1.8 \text{ V})$ 

3.6-V Tolerant input.

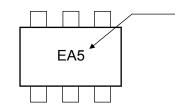
Power down protection is provided on output.



Weight: 0.0068 g (typ.)

#### Marking

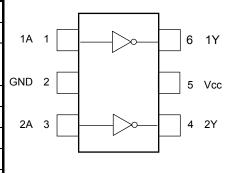




#### Absolute Maximum Ratings (Ta = 25°C)

	.//		
Characteristics	Symbol	Rating	Unit
Power supply voltage	Vec	-0.5 to 4.6	V
DC input voltage	$\supset$ $V_{IN}$	-0.5 to 4.6	V
DC output voltage	V <sub>OUT</sub>	-0.5 to 4.6 (Note 1)	V
De output voltage	V001	-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	V
Input diode current	I <sub>IK</sub>	-50	mA
Output diode current	lok	-50 (Note 3)	mA
DC output current	IOUT	±50	mA
Power dissipation	PD	200	mW
DC V <sub>CC</sub> /ground current	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	±100	mA
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C
	1 1	·	

#### Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or low state. I<sub>OUT</sub> absolute maximum rating be observed.

Note 3: Vout < GND

Start of commercial production

2001-10



### **IEC Logic Symbol**

#### **Truth Table**



А	Y
L	Н
Н	L

#### **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	1.8 to 3.6	$(\langle \langle \rangle \rangle)$
Tower supply voitage	v CC	1.2 to 3.6 (Note 4)	
Input voltage	V <sub>IN</sub>	-0.3 to 3.6	V <sub>1</sub>
Output voltage	V <sub>OUT</sub>	0 to 3.6 (Note 5)	V
Culput voltage	VOU1	0 to V <sub>CC</sub> (Note 6)	V
		±24 (Note 7)	
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±18 (Note 8)	⟨mA
		±6 (Note 9)	
Operating temperature range	T <sub>opr</sub>	-40 to 85	(°C)
Input rise and fall time	dt/dv	0 to 10 (Note 10)	ns/V

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or low state

Note 7:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 9:  $V_{CC} = 1.8 \text{ V}$ 

Note 10:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

#### **Electrical Characteristics**

#### DC Characteristics (Ta = -40 to 85°C, 2.7 V < V<sub>CC</sub> $\leq$ 3.6 V)

Charac	cteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit	
Innut voltage	High level	V <sub>IH</sub> —		_	2.7 to 3.6	2.0	_	V	
Input voltage	Low level	V <sub>IL</sub>	V <sub>IL</sub> —		2.7 to 3.6	_	0.8	V	
High level Output voltage			I <sub>OH</sub> = -100 μA	2.7 to 3.6	V <sub>CC</sub> - 0.2	_			
	High level	V <sub>OH</sub>	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -12 mA	2.7	2.2			
				I <sub>OH</sub> = -18 mA	3.0	2.4	_		
				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_		
			V V	$I_{OL} = 100 \mu A$	2.7 to 3.6		0.2		
	Low level	V <sub>OL</sub>		V <sub>IN</sub> = V <sub>IH</sub>	\/ = \/	I <sub>OL</sub> = 12 mA	2.7	*	0.4
	LOW level	VOL	VIN — VIH	$I_{OL} = 18 \text{ mA}$	I <sub>OL</sub> = 18 mA 3.0		0.4		
				$I_{OL} = 24 \text{ mA}$	3.0		0.55		
Input leakage curre	ent	I <sub>IN</sub>	$V_{IN} = 0 \text{ to } 3.6$	V	2.7 to 3.6	4	±5.0	μΑ	
Power off leakage	current	I <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0	to 3.6 V	0	>)	10.0	μΑ	
Quiescent supply current		loo	$V_{IN} = V_{CC}$ or $C$	$V_{IN} = V_{CC}$ or GND		_	20.0		
		Icc	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OUT</sub> ) ≤ 3.6 V		2.7 to 3.6	_	±20.0	μА	
Increase in I <sub>CC</sub> pe	r input	Δl <sub>CC</sub>	V <sub>IH</sub> = V <sub>CC</sub> - 0.	6 V	2.7 to 3.6	_	750		

## DC Characteristics (Ta = -40 to 85°C, 2.3 V ≤ V<sub>CC</sub> ≤ 2.7 V)

Charac	Characteristics Symbol Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit		
Input voltage	High level	VH		===	2.3 to 2.7	1.6	_	V
input voitage	Low level	// SVIL			2.3 to 2.7	_	0.7	V
				I <sub>OH</sub> = -100 μA	2.3 to 2.7	V <sub>CC</sub> - 0.2		
High level		VoH	$V_{IN} = V_{IL}$	$I_{OH} = -6 \text{ mA}$	2.3	2.0	_	
		1		I <sub>OH</sub> = -12 mA	2.3	1.8	_	
Output voltage	$\sqrt{\gamma}$			I <sub>OH</sub> = -18 mA	2.3	1.7	_	V
2		$\wedge$		I <sub>OL</sub> = 100 μA	2.3 to 2.7	_	0.2	
	Low level	Vol	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 12 mA	2.3	_	0.4	
	))			I <sub>OL</sub> = 18 mA	2.3	_	0.6	
Input leakage curre	nt	( IIN)	V <sub>IN</sub> = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μА
Power off leakage	current	OFF	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3	3.6 V	0	_	10.0	μА
Quiggoont guiraly a	urront		V <sub>IN</sub> = V <sub>CC</sub> or GNE	)	2.3 to 2.7	_	20.0	
Quiescent supply c	urrent	Icc	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OUT</sub>	·) ≤ 3.6 V	2.3 to 2.7	_	±20.0	μА

3

### DC Characteristics (Ta = -40 to $85^{\circ}$ C, $1.8 \text{ V} \le \text{V}_{\text{CC}} < 2.3 \text{ V})$

Characteristics		Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
Input voltage	High level	V <sub>IH</sub>		_	1.8 to 2.3	V <sub>CC</sub> ×0.7		V
Input voltage	Low level	V <sub>IL</sub>	V <sub>IL</sub> —		1.8 to 2.3	_	V <sub>CC</sub> ×0.2	V
	High level	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	1.8	VCC 0.2		
Output voltage				I <sub>OH</sub> = -6 mA	7/1,8	1.4		V
	Laurianal	V <sub>OL</sub>	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 100 μA	1.8	_	0.2	
	Low level			I <sub>OL</sub> = 6 mA	1.8	_	0.3	
Input leakage curre	ent	I <sub>IN</sub>	$V_{IN} = 0 \text{ to } 3.6 \text{ V}$		1.8	)	±5.0	μА
Power off leakage current		l <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 V		0	(A)	10.0	μΑ
Quiescent supply current		loo	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.8	)	20.0	Δ
Quiescent supply o	unent	Icc	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OU</sub>	r)≤3.6 V	1.8	) <del> </del>	±20.0	μА

## AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition	Vcc (V)	Min	Max	Unit
	+		1.8	1.0	7.4	
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	$2.5\pm0.2$	0.8	3.7	ns
	<sup>t</sup> pHL		$3.3 \pm 0.3$	0.6	2.8	

For  $C_L = 50$  pF, add approximately 300 ps to the AC maximum specification.



### Dynamic Switching Characteristics (Ta = 25°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

項	B		記号	測定条	件		標準	単位
タ	П		配 与	<b>烈 足 未</b>	IT	V <sub>CC</sub> (V)	标牛	丰位
				V <sub>IH</sub> = 1.8V, V <sub>IL</sub> = 0V	(Note 11)	1.8	0.25	
Quiet output maximum dynamic		$V_{OL}$	VOLP	V <sub>IH</sub> = 2.5V, V <sub>IL</sub> = 0V	(Note 11)	2.5	0.6	V
				V <sub>IH</sub> = 3.3V, V <sub>IL</sub> = 0V	(Note 11)	3.3	8.0	
				V <sub>IH</sub> = 1.8V, V <sub>IL</sub> = 0V	(Note 11)	1.8	-0.25	
Quiet output minimum dynamic		$V_{OL}$	VOLV	V <sub>IH</sub> = 2.5V, V <sub>IL</sub> = 0V	(Note 11)	2.5	-0.6	V
ĺ				V <sub>IH</sub> = 3.3V, V <sub>IL</sub> = 0V	(Note 11)	3.3	-0.8	
				V <sub>IH</sub> = 1.8V, V <sub>IL</sub> = 0V	(Note 11)	1.8	1.5	
Quiet output minimum dynamic		Vон	VOHV	V <sub>IH</sub> = 2.5V, V <sub>IL</sub> = 0V	(Note 11)	2.5	1.9	V
				V <sub>IH</sub> = 3.3V, V <sub>IL</sub> = 0V	(Note 11)	3.3	2.2	

Note 11: Parameter guaranteed by design

#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Vcc(V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>		1,8, 2.5, 3.3	5	pF
Power dissipation capacitance	C <sub>PD</sub>	f <sub>IN</sub> = 10 MHz	(Note 12) 1.8, 2.5, 3.3	18	pF

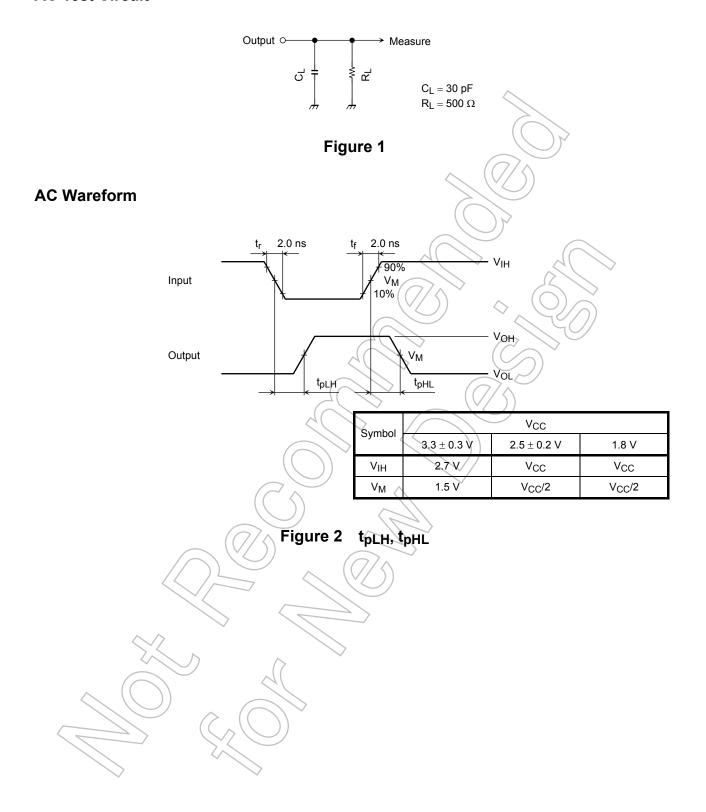
Note 12: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

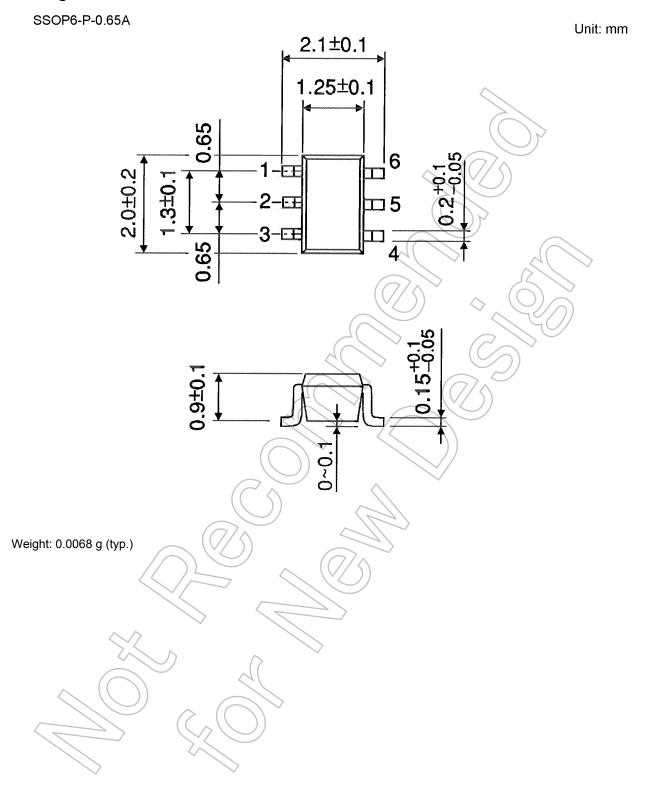
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 



#### **AC Test Circuit**



### **Package Dimensions**



#### **RESTRICTIONS ON PRODUCT USE**

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE
  EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH
  MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT
  ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without
  limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for
  automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions,
  safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE
  PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your
  TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any
  applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE
  FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY
  WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR
  LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND
  LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO
  SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS
  FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without
  limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile
  technology products (mass destruction weapons). Product and related software and technology may be controlled under the
  applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the
  U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited
  except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
  Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES
  OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

8