

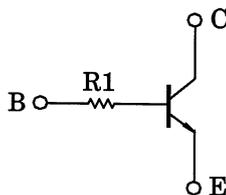
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process) (Bias Resistor built-in Transistor)

RN1710, RN1711

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Including two devices in USV (ultra super mini type with 5 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary to RN2710 and RN2711

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

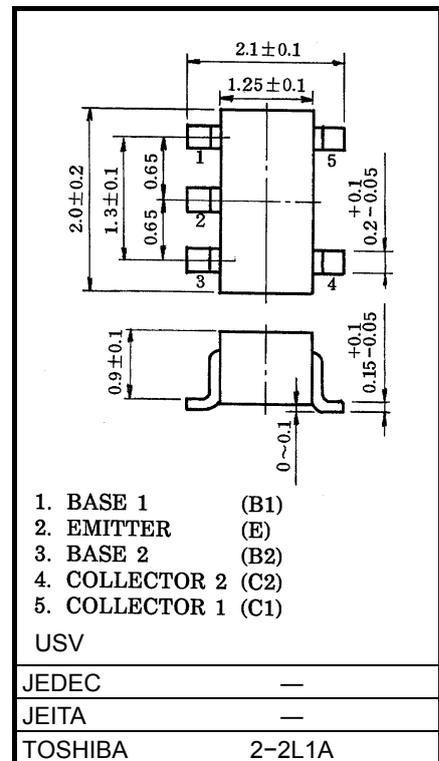
Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CB0}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	v
Collector current	I _C	100	mA
Collector power dissipation	P _C *	200	mW
Junction temperature	T _j	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*: Total rating

Unit: mm



1. BASE 1 (B1)
2. EMITTER (E)
3. BASE 2 (B2)
4. COLLECTOR 2 (C2)
5. COLLECTOR 1 (C1)

USV

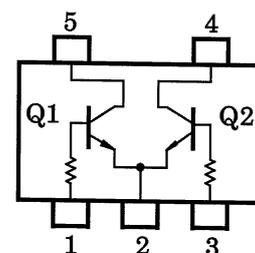
JEDEC —

JEITA —

TOSHIBA 2-2L1A

Weight: 6.2 mg (typ.)

Equivalent Circuit (Top View)

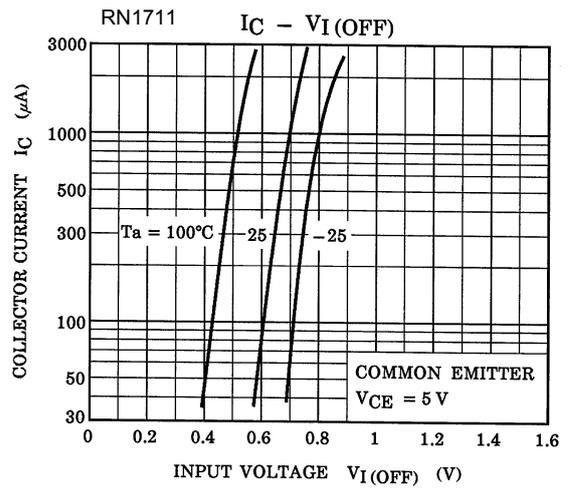
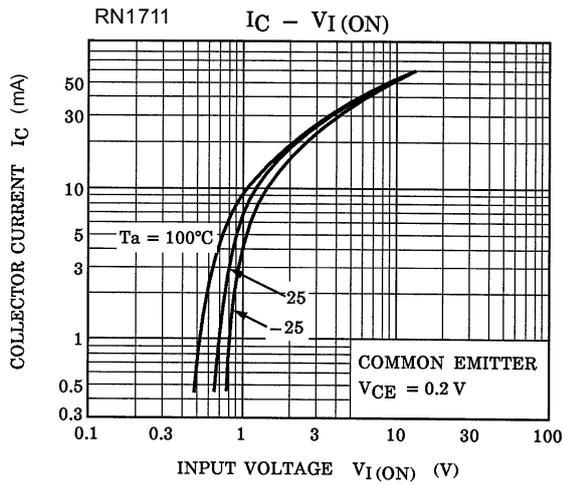
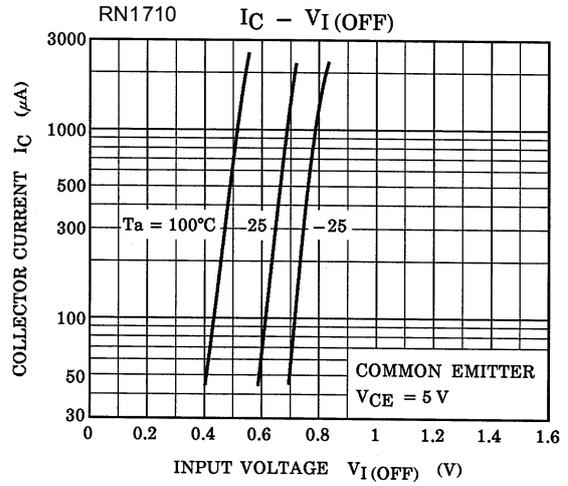
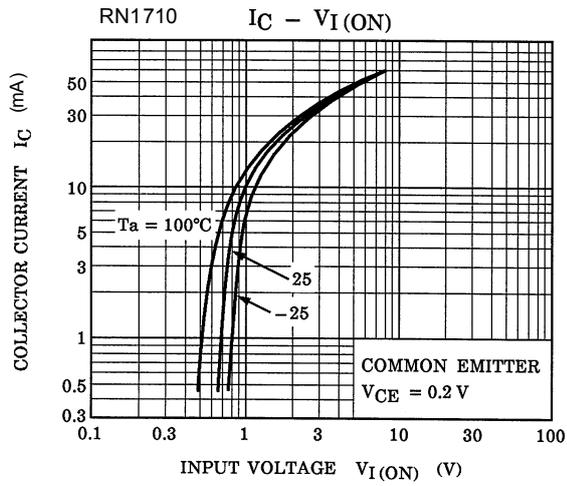


Start of commercial production
1998-02

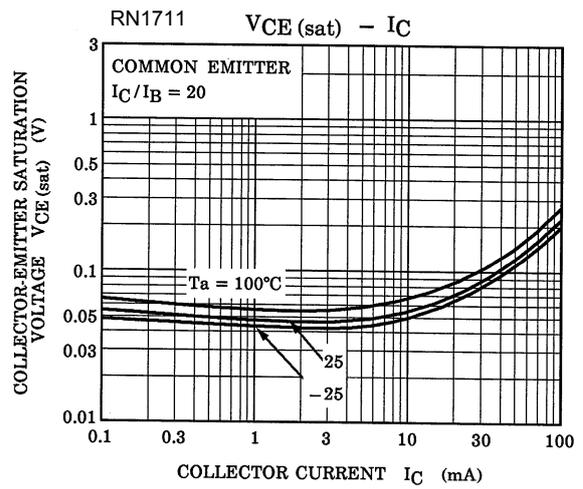
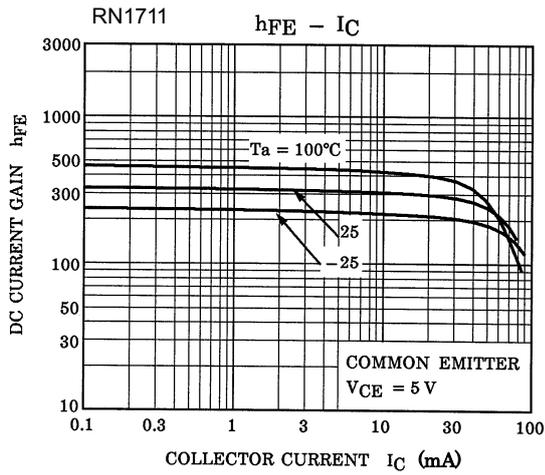
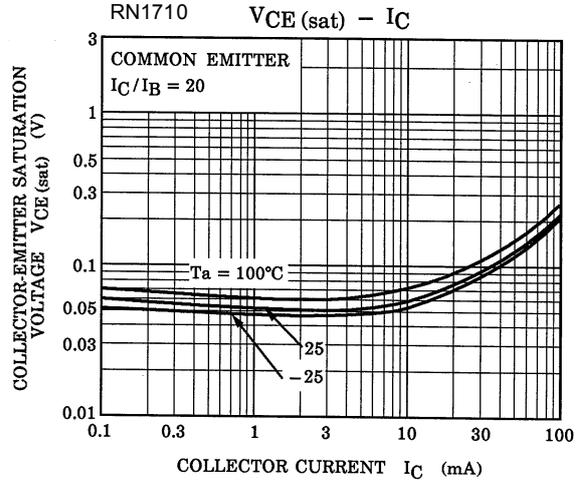
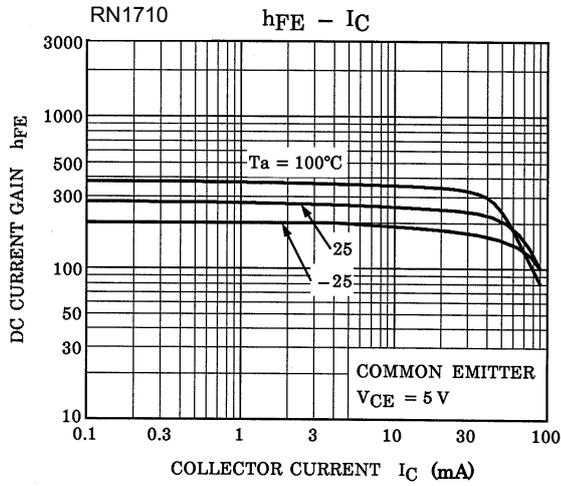
Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	I_{CBO}	—	$V_{CB} = 50V, I_E = 0$	—	—	100	nA
Emitter cut-off current	I_{EBO}	—	$V_{EB} = 5V, I_C = 0$	—	—	100	nA
DC current gain	h_{FE}	—	$V_{CE} = 5V, I_C = 1mA$	120	—	700	—
Collector-emitter saturation voltage	$V_{CE(sat)}$	—	$I_C = 5mA, I_B = 0.25mA$	—	0.1	0.3	V
Transition frequency	f_T	—	$V_{CE} = 10V, I_C = 5mA$	—	250	—	MHz
Collector output capacitance	C_{ob}	—	$V_{CB} = 10V, I_E = 0, f = 1MHz$	—	3	6	pF
Input resistor	RN1710	R1	—	3.29	4.7	6.11	kΩ
	RN1711			7	10	13	

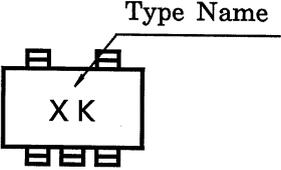
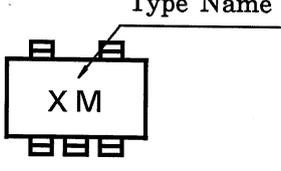
(Q1, Q2 Common)



(Q1, Q2 Common)



Marking

Type Name	Marking
RN1710	 <p>The diagram shows a rectangular component with two pins on the top edge and four pins on the bottom edge. The letters 'X K' are printed in the center. A line points from the text 'Type Name' above to the 'X' in the marking.</p>
RN1711	 <p>The diagram shows a rectangular component with two pins on the top edge and four pins on the bottom edge. The letters 'X M' are printed in the center. A line points from the text 'Type Name' above to the 'X' in the marking.</p>

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