TOSHIBA Photocoupler Photorelay

TLP4176G

PBX

Telecommunication

Modem·FAX Cards, Modems In PC

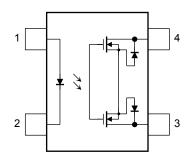
Measurement Instrumentation

The TOSHIBA TLP4176GA consists of an infrared emitting diode optically coupled to a photo-MOSFET in a SOP, which is suitable for surface mount assembly.

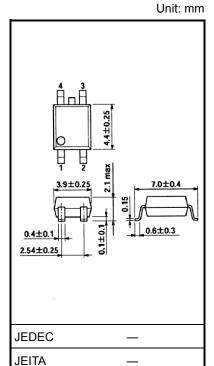
The TLP4176GA is suitable for the modem applications which require space savings.

- 4 pin SOP (2.54SOP4): 2.1 mm high, 2.54 mm pitch
- 1-form-B
- Peak off-state voltage: 350 V (min)
- Trigger LED current: 3 mA (max)
- On-state current: 120 mA (max)
- On-state resistance: 25Ω (max)
- Isolation voltage: 1500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349

Pin Configuration (top view)



- 1: ANODE
- 2: CATHODE
- 3: DRAIN
- 4: DRAIN



11-5H1

Weight: 0.1 g (typ.)

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Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔIF/°C	-0.5	mA/°C
	Peak forward current (100 μs pulse, 100 pps)	IFP	1	А
LED	Reverse voltage	VR	5	٧
	Diode power dissipation	P_D	50	mW
	Diode power dissipation derating (Ta ≥25°C)	ΔP _D /°C	-0.5	mW/°C
	Junction temperature	Tj	125	°C
	Off-state output terminal voltage	Voff	350	V
	On-state current	ION	120	mA
Detector	On-state current derating (Ta ≥ 25°C)	Δlon/°C	-1.2	mA/°C
Dete	Output power dissipation	Po	324	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP _O /°C	-3.24	mW / °C
	Junction temperature	Tj	125	°C
Operating temperature range		T _{opr}	−40 to 85	°C
Storage temperature range		T _{stg}	−55 to 125	°C
Lead	soldering temperature (10 s)	T _{sol}	260	°C
	tion voltage 60 s, R.H. ≤ 60 %) (Note 1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two-terminal device: LED side pins shorted together, and DETECTOR side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{DD}	_	_	280	V
Forward current	lF	5	_	25	mA
On-state current	Ion	_	_	120	mA
Operating temperature	Topr	-20	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



Individual Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	IR	V _R = 5 V	_	_	10	μА
	Capacitance	Ст	V _F = 0 V, f = 1 MHz	_	30	_	pF
ector	Off-state current	loff	V _{OFF} = 350 V, I _F = 5 mA			1	μА
Detector	Capacitance	C _{OFF}	V = 0 V, f = 1 MHz, I _F = 5 mA		65	_	pF



Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	IFC	I _{OFF} = 10 μA	_	1	3	mA
Return LED current	I _{FT}	I _{ON} = 120 mA	0.1	_	_	mA
On-state resistance	Ron	I _{ON} = 120 mA	_	15	25	Ω

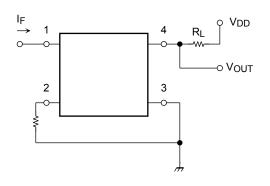
Isolation Characteristics (Ta = 25°C)

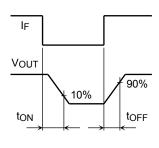
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60 %	5 × 10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	1500	_	_	Vrms

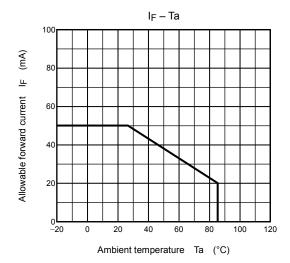
Switching Characteristics (Ta = 25°C)

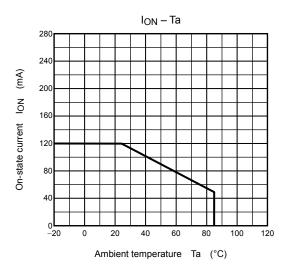
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	ton	$R_L = 200 \Omega$ (Note 2)	_	_	1	ms
Turn-off time	tOFF	$V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$	_	_	3	ms

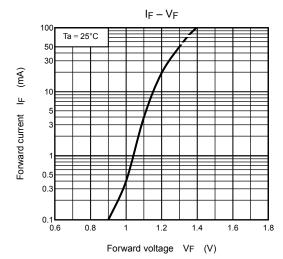
Note 2: Switching time test circuit

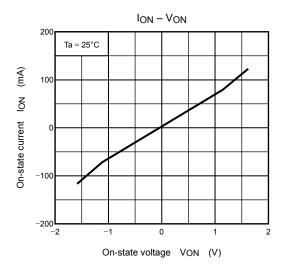


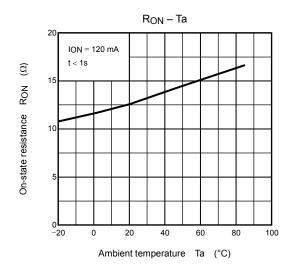


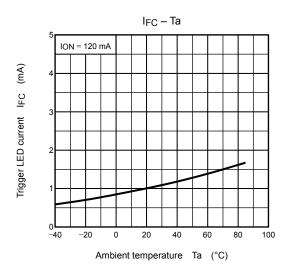




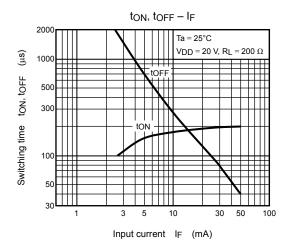


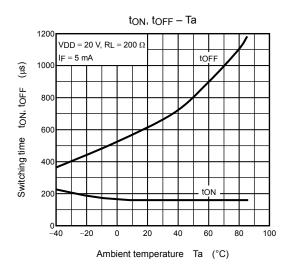


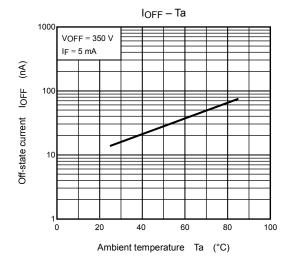




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.







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