# TLP665J(S)

TOSHIBA PHOTOCOUPLER IRED & PHOTO-TRIAC

# TLP665J(S)

Office Equipment Household Appliances Triac Drivers Solid State Relays

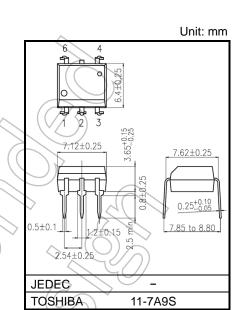
TOSHIBA

The TOSHIBA TLP665J(S) consists of an infrared emitting diode optically coupled to a triac-output photocoupler housed in a 6-pin DIP package.

- Peak Off-State Voltage: 600 V (min)
- Trigger LED Current:10 mA (max)
- On-State Current: 100 mA (max)
- Isolation Voltage: 5000 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- CQC-approved: GB4943.1,GB8898 Japan Factory
- VDE-approved: EN 60747-5-5 , EN 62368-1 (Note1)

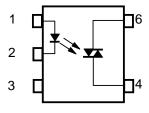
Note 1: When a VDE approved type is needed,

please designate the **Option**(D4).



Weight: 0.39 g (typ.)

# Pin Configuration (top view)



1: Anode 2: Csthode 3: N.C. 4:TriacTerminal 6:TriacTerminal

7.62 mm pitch Standard Type Creepage Distance 7.0 mm (min)

7.0 mm (min)

0.5 mm (min)

**Construction Mechanical Rating** 

Clearance

Insulation Thickness

TLPxxxxF Type 8.0 mm (min) 8.0 mm (min) 0.5 mm (min)

10.16 mm pitch

Start of commercial production 1987-06

	CHARACTERISTIC		SYMBOL	RATING	UNIT
	Forward Current	lF	50	mA	
	Forward Current Derating (Ta ≥ 53°C)	∆IF /°C	-0.7	mA /°C	
	Peak Forward Current (100 µs pulse, 100 pps)	I <sub>FP</sub>	×	А	
LED	Reverse Voltage	VR	5	V	
	Power Dissipation	PD	100	mW	
	Power Dissipation Derating (Ta ≥ 53°C)	$\Delta P_D / C$	1.4	mW/°C	
	Junction Temperature	Â,	125	°C	
	Off-State Output Terminal Voltage	VDRM	600	V	
		Ta=25°C		100	
	On-State RMS Current	Ta=70°C	IT(RMS)	50	mA
TOR	On-State Current Derating (Ta ≥ 25°C)	ΔΙτ/°C	-1.1	mA /°C	
ETECTOR	Peak On-State Current (100 µs pulse, 120 pps)	Цтр	2	A	
DEJ	Peak Nonrepetitive Surge Current (Pw=10 ms)	Итям	1,2	(A)	
	Output Power Dissipation	> Po	300	mW	
	Output Power Dissipation Derating (Ta $\ge$ 25°C)	∆Po /°C	-3.0	mW/°C	
	Junction Temperature	Tj	115	°C	
Stor	rage Temperature Range		Tstg	-55 to 125	°C
Ope	erating Temperature Range	Торг	-40 to 100	°C	
Lea	d Soldering Temperature (10 s)	T <sub>sol</sub>	260	°C	
Tota	al Package Power Dissipation	Рт	330	mW	
Tota	al Package Power Dissipation Derating (Ta $\ge 25^{\circ}$ C)	∆P <sub>T</sub> /°C	-4.4	mW /°C	
Isola	ation Voltage (AC, 60 s, R.H.≤ 60 %)	(Note 2)	BVs	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2) Device considered a two terminal device : Pins1, 2 and 3 shorted together and pin 4 and pin 6 shorted together.

#### **Recommended Operating Conditions**

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	VAC			240	Vac
Forward Current	lF	15	20	25	mA
Peak On-State Current	I <sub>TP</sub>			1	А
Operating Temperature	T <sub>opr</sub>	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

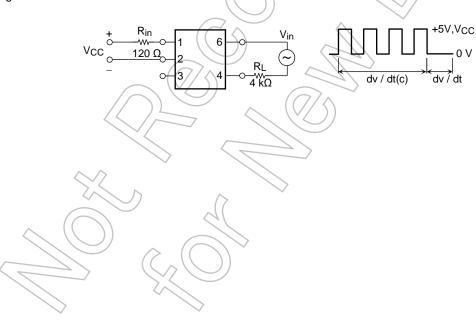
### Individual Electrical Characteristics (Ta=25°C)

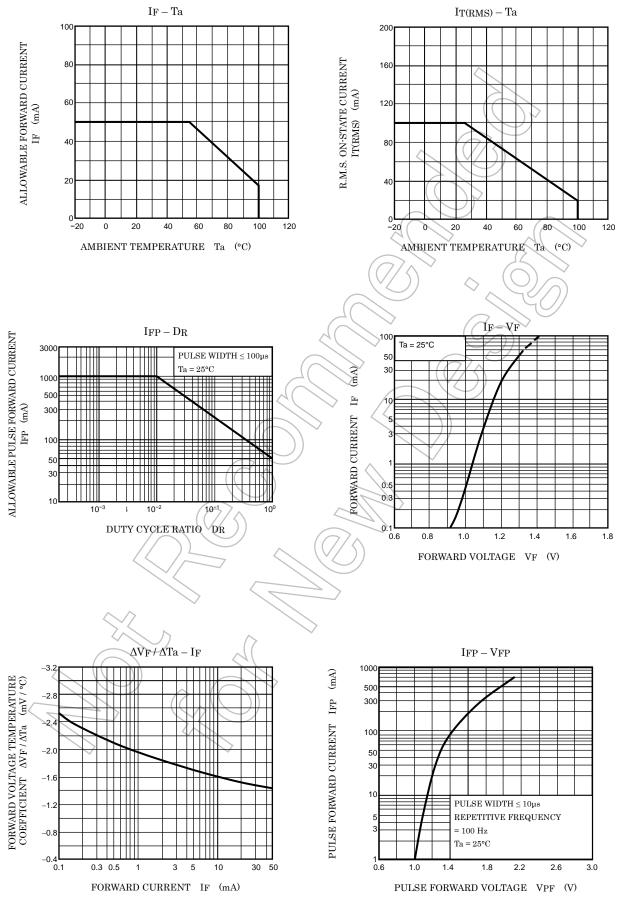
	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	VF	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
	Reverse Current	IR	V <sub>R</sub> = 5 V	—	_	10	μA
	Capacitance	Ст	VF = 0 V, f=1 MHz	_<	30	_	pF
DETECTOR	Peak Off-State Current	IDRM	V <sub>DRM</sub> = 600 V	- (	10	1000	nA
	Peak On-State Voltage	VTM	I <sub>TM</sub> = 100 mA	_ \	1.7	3.0	V
	Holding Current	Iн	—	$(\overline{1})$	1.0	—	mA
	Critical Rate of Rise of Off-State Voltage	dv/dt	V <sub>in</sub> = 240 Vrms, Ta=85 °C (Fig.1)	X	500	_	V/µs
	Critical Rate of Rise of Commutating Voltage	dv/dt(c)	V <sub>in</sub> = 60 Vrms, I <sub>T</sub> =15 mA (Fig. 1)	TP	0.2	_	V/µs

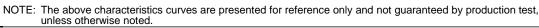
### Coupled Electrical Characteristics (Ta=25°C)

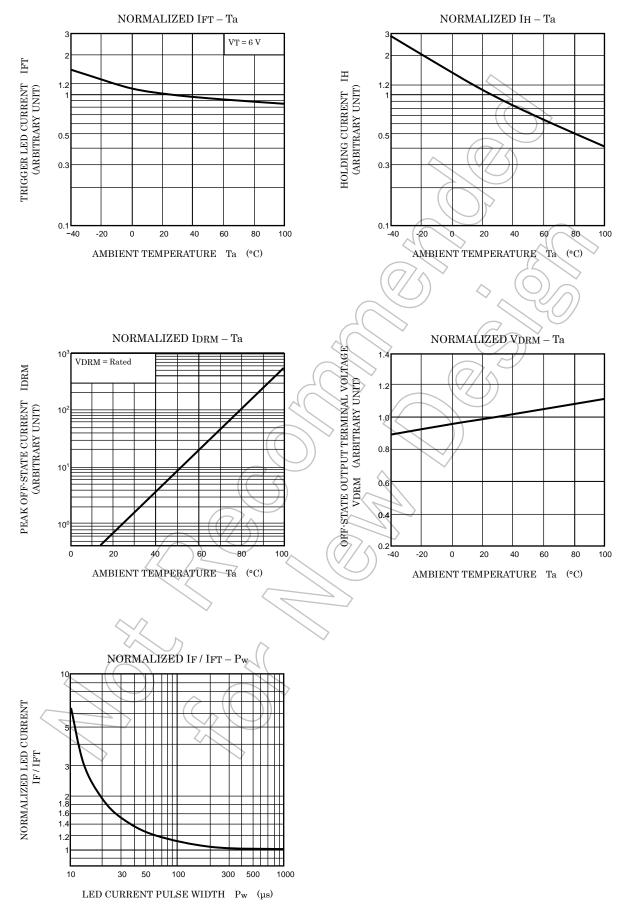
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN TYP.	MAX	UNIT
Trigger LED Current	I <sub>FT</sub>	VT=6 V	- 5	10	mA
Capacitance (Input to Output)	CS	VS=0 V, f=1 MHz	- 0.8	—	pF
Isolation Resistance	Rs	VS=500 V ,R.H.≤ 60 %	5×10 <sup>10</sup> 10 <sup>14</sup>	—	Ω
Isolation Voltage	BVs	AC, 60 s	5000 —	_	Vrms

#### Fig. 1 dv / dt test circuit









NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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