TOSHIBA Photocoupler IRED & Photo-Triac

TLP165J

Triac Drive Programmable Controllers AC-Output Module Solid State Relay

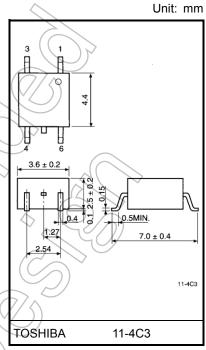
The TOSHIBA mini-flat coupler TLP165J is a small outline coupler, suitable for surface mount assembly.

The TLP165J consists of a photo triac, optically coupled to an infrared emitting diode.

- Peak off-state voltage: 600 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 70 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A
 File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

Note 1: When a VDE approved type is needed,

please designate the Option(V4).



Weight: 0.09 g (typ.)

Trigger LED Current

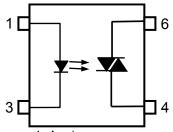
Type (Note 1)	Trigger LED Current (mA)				Marking of Classification			
	V _T =6V, Ta=25°C							
	Min		Max		Olassification			
(IFT7)	\^-		7		M			
None	7/- 7/		10	^	T7, blank			

Note 1: Ex. rank IFT7: TLP165J (IFT7)

Note: Application type name for certification test, please use standard product type name, i.e.

TLP165J(IFT7): TLP165J

Pin Configuration



- 1 . Anode
- 3 . Cathode4 . Triac Terminal
- 6 . Triac Terminal

Start of commercial production 1994-11

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
	Forward current	lF	50	mA	
	Forward current derating (Ta ≥ 5	ΔI _F / °C	-0.7	mA / °C	
	Peak forward current (100µs pul	IFP	1	Á	
LED	Reverse voltage		VR	5	V (
	Diode power dissipation		PD	100	mW
	Diode power dissipation derating	ΔP _D /°C	-1.4	mW/°C	
	Junction temperature	Tj	125	$\langle \mathcal{C} \rangle$	
	Off– state output terminal voltage	VDRM	600	X	
	On-state RMS current	Ta=25°C	IT(D) (O)	70) mA
		Ta=70°C	IT(RMS)	40	TIIA
_	On-state current derating (Ta ≥ 2	ΔIT / °C	-0.67	mA / °C	
Detector	Peak on-state current (100µs pu	ITP	(7/2)	Α	
Det	Peak non-repetitive surge currer (Pw=10ms)	ITSM	12	A	
	Output power dissipation	Po	200	mW	
	Output power dissipation derating	ΔP _O /°C	-2.0	mW / °C	
	Junction temperature	(I)	115),¢	
Storage temperature range			T _{stg}	-55 to 125	(°e)
Operating temperature range			Topr	-40 to 100	°C
Lead soldering temperature (10 s)			T _{sol}	260	°C
Isolatio	on voltage (AC, 60 s, R.H. ≤ 60 %))) BV _S	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 2: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	240	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	ITP	_	_	1	Α
Operating temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

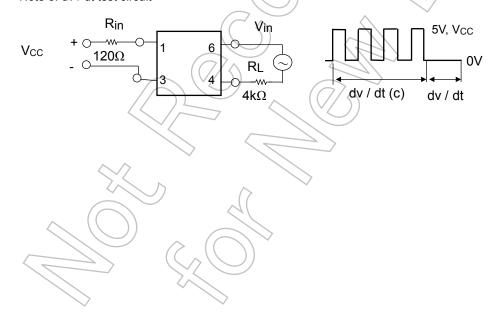
Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	IR	V _R = 5 V	_	_	10	μΑ
	Capacitance	СТ	VF = 0 V, f = 1MHz	<u> </u>	30	_	pF
Detector	Peak off-state current	IDRM	V _{DRM} = 600 V		10	1000	nA
	Peak on-state voltage	Vтм	I _{TM} = 70 mA		1.7	2.8	V
	Holding current	lн	(2)) } 	1.0	_	mA
	Critical rate of rise of off–state voltage	dv / dt	V _{in} = 240 Vrms, Ta = 85 °C(Note 3)))	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	I _T = 15 mA, V _{in} = 60 Vrms (Note 3)	_	0.2	_	V / µs

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Trigger LED current	I _{FT}	V _T = 6 V		5	10	mA
Capacitance input to output	Cs	V _S = 0 V, f = 1MHz		0.8	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	2500	_	_	Vrms
Turn-on time	ton	$V_D = 6 \rightarrow 4 \text{ V, R}_L = 100 \Omega$ I _F = Rated I _{FT} ×1.5	_	_	100	μs

Note 3: dv / dt test circuit



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