# TLP330

TOSHIBA Photocoupler IRED & Photo-Transistor

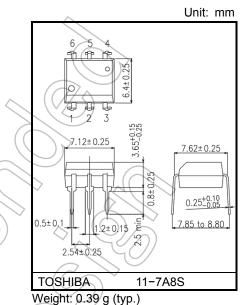
# **TLP330**

#### Programmable Controllers AC / DC–Input Module Telecommunication

TOSHIBA

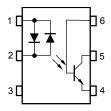
The TOSHIBA TLP330 consists of a photo-transistor optically coupled to infrared emitting diode connected inverse parallel in a six lead plastic DIP package. This is suitable for application of AC input current up to 150mA.

- If maximum rating: ±150mA
- Collector-Emitter voltage: 55V(min.)
- Current transfer ratio: 25% (min.)( $I_F = \pm 20$ mA)
- Isolation voltage: 5000Vrms (min.)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349





#### Pin Configurations (top view)



2: Cathode, anode 3: NC 4: Emitter

1: Anode, cathode

5: Collector 6: Base

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit
	Forward current	lF	±150	mA
	Forward current derating (Ta ≥ 25°C)	ΔI <sub>F</sub> /°C	-1.5	mA /°C
БD	Peak forward current (100µs pulse,100pps)	IFP	±1	A
ш	Diode power dissipation	PD	200	Wm
	Diode power dissipation derating (Ta ≥25°C)	∆P <sub>D</sub> /°C	-2.0	mW/°C
	Junction temperature	Tj	125	°C
	Collector-emitter voltage	VCEO	55	() v
	Collector-base voltage	Vсво	80	v
	Emitter-collector voltage	V <sub>ECO</sub>	(L)P	V
ctor	Emitter-base voltage	V <sub>EBO</sub>	Z	V
Detector	Collector current	IC	80	mA
	Power dissipation	Pc	150	mW
	Power dissipation derating (Ta ≥ 25°C)	ΔP <sub>C</sub> /°C	) -1.5 🔷	mW_/°C
	Junction temperature	Tí	125	~°C-()
Storage temperature range		Tstg	-55 to 125	°C
Operating temperature range		Topr	-55 to 100	∕)°C
Lead soldering temperature (10 s)		T <sub>sol</sub>	260	°C
Total package power dissipation		PŤ	250	mW
Total package power dissipation derating (Ta≥25°C)		ΔP <sub>T</sub> /°C	-2,5	mW /°C
Isola	ation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)	BVs	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1 Device considered a two terminal device: Pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

#### **Recommended Operating Conditions**

Characteristics	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	✓ v <sub>cc</sub>	—	5	24	V
Forward current	IF(RMS)	_	20	120	mA
Collector current	IC	_	1	10	mA
Operating temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

## Individual Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Condition	Min.	Тур.	Max.	Unit
	Forward voltage	VF	I <sub>F</sub> = ±100 mA	_	1.4	1.7	V
LED	Forward current	lF	$V_F = \pm 0.7 V$	-	2.5	20	μA
	Capacitance	Ст	V = 0 V, f = 1 MHz	X	100		pF
	Collector-emitter breakdown voltage	V(BR) CEO	I <sub>C</sub> = 0.5 mA	55		_	V
	Emitter–collector breakdown voltage	V(BR) ECO	IE = 0.1 mA		-	Ι	V
	Collector-base breakdown voltage	V(BR) CBO	IC = 0.1 mA	80	_		V
ctor	Emitter-base breakdown voltage	V(BR) EBO	IE = 0.1 mA	7	-	_	V
Detector	Collector dark current	1050	VCE = 24 V	_	10	100	nA
		ICEO	V <sub>CE</sub> = 24 V, Ta = 85 °C	- 12	2	50	μA
	Collector dark current	ICER	Vce = 24 V, Ta = 85 °C R <sub>BE</sub> = 1MΩ	-(C	0.5	10	μA
	Collector dark current	Ісво	Vce = 10V	T	0.1	_	nA
	DC forward current gain	hFE	VCE = 5 V, IC = 0.5mA	2	400	_	—
	Capacitance (collector to emitter)	CCE	V = 0 V, f = 1 MHz	$\mathcal{A}$	10	_	pF

# Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Condition	Mln.	Тур.	Max.	Unit
	Ic/IF	I <sub>F</sub> = ±20 mA V <sub>CE</sub> = 1 V	25	_	—	%
Current transfer ratio	IC / IF(high)	I <sub>F</sub> = ±100 mA V <sub>CE</sub> = 1 V	20	—	80	%
Base photo-current	Ірв	I <sub>F</sub> = ±5 mA, V <sub>CB</sub> = 5 V	-	10	_	μA
Collector-emitter saturation voltage	VCF (sat)	I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = 20 mA	-	_	0.4	V
		IC = 2.4 mA, IF = ±100 mA	_	_	0.4	
Off-state collector current	I <sub>C(off)</sub>	VF = ± 0.7 V, V <sub>CE</sub> = 24 V	_	1	10	μA
CTR symmetry	IC (ratio)	IC (IF = −20 mA) / IC (IF = +20 mA)	0.5	1	2	

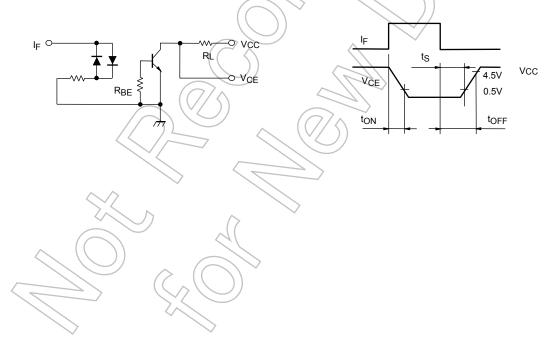
## Isolation Characteristics (Ta = 25°C)

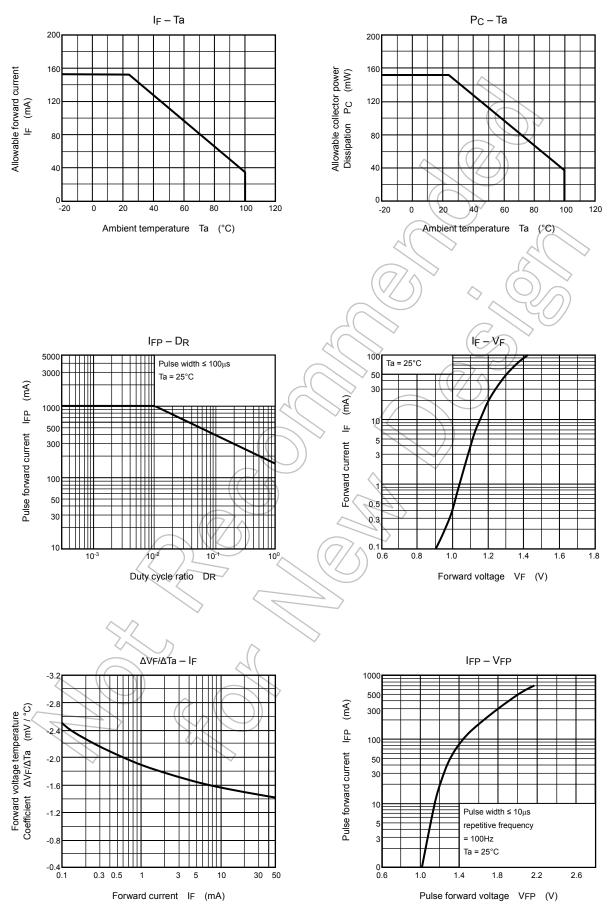
Characteristics	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance (input to output)	Cs	Vs = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVs	AC, 60 s	5000	_	_	Vrms

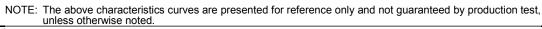
#### Switching Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition Min. Typ. Max. Unit
Rise time	tr	
Fall time	tf	
Turn–on time	ton	$I_{C} = 2 \text{ mA}$ $R_{L} = 100 \Omega$ $\mu s$
Turn-off time	t <sub>off</sub>	- 3 -
Turn-on time	ton	$R_L = 1.9 k\Omega$ (Fig.1)
Storage time	ts	$R_{BE} = OPEN$ $ \mu s$
Turn-off time	tOFF	V <sub>CC</sub> = 5 V, I <sub>F</sub> = ±16 mA - 25 -
Turn-on time	ton	$R_L = 1.9 k\Omega$ (Fig.1) $\rightarrow 2$ $-$
Storage time	ts	$R_{BE} = 220 k\Omega$ – 12 – µs
Turn-off time	toff	Vcc = 5 V, IF = ±16 mA

#### Fig. 1 Switching time test circuit







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