Unit: mm

TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL IGBT

GT30J322

FOURTH-GENERATION IGBT

CURRENT RESONANCE INVERTER SWITCHING APPLICATIONS

• FRD included between emitter and collector

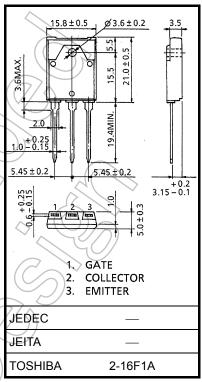
• Enhancement mode type

• High speed : $t_f = 0.25 \mu s$ (Typ.) (IC = 50A)

• Low saturation voltage : $V_{CE (sat)} = 2.1 V (Typ.) (I_C = 50A)$

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		V _{CES}	600	$(\nearrow \land)$	
Gate-Emitter Voltage		V _{GES}	±20	V	
Collector Current	DC	IC	30	À	
	1ms	I _{CP}	100		
Emitter-Collector Forward Current	DC	lF	30	Α	
	1ms	IFP	60		
Collector Power Dissipation (Tc = 25°C)		P _C	75	V	
Junction Temperature		T _j (T _j 150		
Storage Temperature Range		T _{stg}	-55 to 150	°C	



Weight: 5.8 g (typ.)

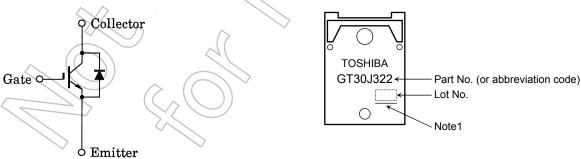
Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

EQUIVALENT CIRCUIT

MARKING



Note1: A line under a Lot No. identifies the indication of product Labels. [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

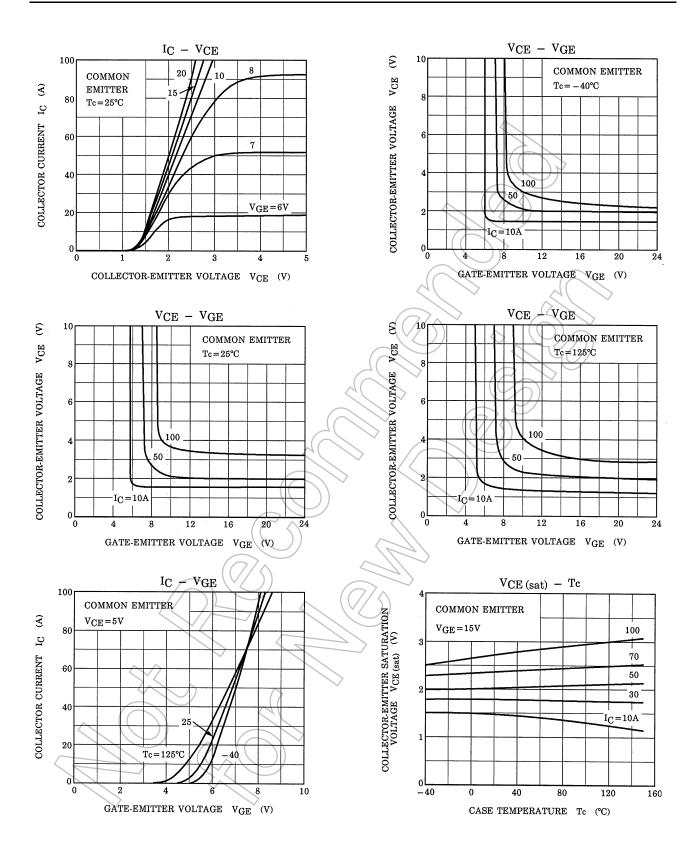
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

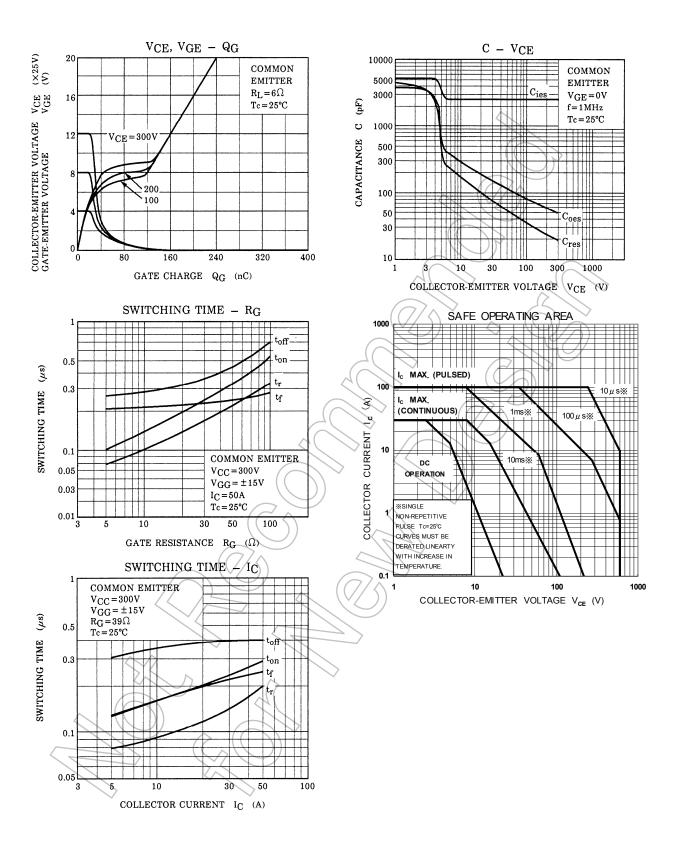
The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

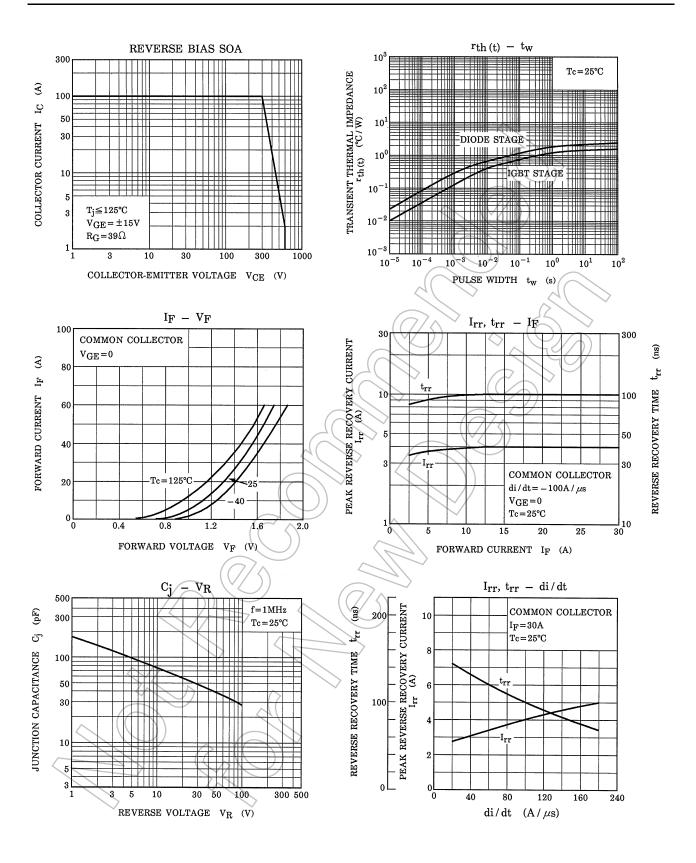
Start of commercial production 1998-11

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARA	CTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Gate Leakage Curi	rent	I _{GES}	V _{GE} = ±20V, V _{CE} = 0	_	_	±500	nA
Collector Cut-Off (Current	I _{CES}	V _{CE} = 600V, V _{GE} = 0	_	_	1.0	mA
Gate-Emitter Cut-	Off Voltage	V _{GE} (OFF)	I _C = 50mA, V _{CE} = 5V	3.0	_	6.0	V
Collector-Emitter S	Saturation Voltage	V _{CE} (sat)	I _C = 50A, V _{GE} = 15V		2.1	2.8	V
Input Capacitance		C _{ies}	V _{CE} = 10V, V _{GE} = 0, f = 1MHz	1	2500	_	pF
Switching Time	Rise Time	t _r	15V 0 1 2 39Ω 1 300V) 	0.20		
	Turn-On Time	t _{on}		\bigcirc	0.30		
	Fall Time	t _f		_	0.25	0.40	μs
	Turn-Off Time	t _{off}		_	0.40	_	
Peak Forward Volt	age	VF	I _F = 30A, V _{GE} = 0	_		2.0	V
Reverse Recovery	Time	t _{rr}	I _F = 30A, V _{GE} = 0 di / dt = -100A / µs	(54/	0.2	μs
Thermal Resistance	e (IGBT)	R _{th (j-c)}		-(()	1.67	°C / W
Thermal Resistance (Diode)		R _{th (j-c)}		(7)	4	2.27	°C / W







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