TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC688AP, TC74HC688AF

8-Bit Equality Comparator

The TC74HC688A is a high speed CMOS 8-BIT EQUALITY COMPARATOR fabricated with silicon gate $\rm C^2MOS$ technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

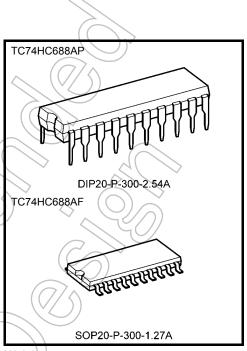
The TC74HC688A compares two 8-bit binary or BCD words applied inputs P0 thru P7, and inputs Q0 thru Q7, and indicates whether or not they are equal.

A signal active low enable is provided to facilitate cascading of several packages to compare of words greater than 8 bits.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

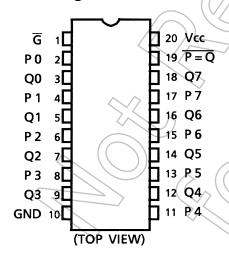
- High speed: $t_{pd} = 17 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \text{ (max)}$ at $T_a = 25^{\circ}C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Pin and function compatible with 74LS688



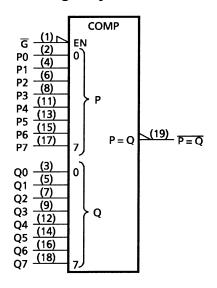
Weight

DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.)

Pin Assignment



IEC Logic Symbol

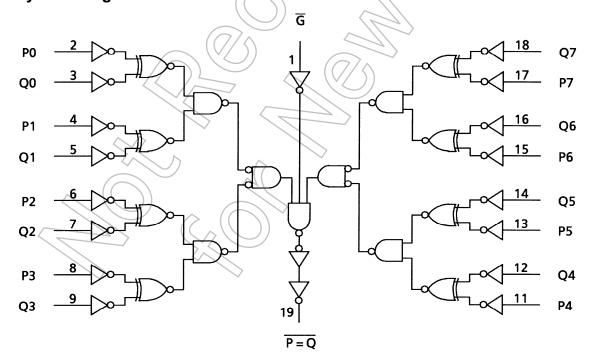


Truth Table

Inp	uts	Output				
P, Q	IG	$\overline{P} = \overline{Q}$				
P = Q	L	L				
P ≠ Q	L	Н				
Х	Н	Н				

X: Don't care

System Diagram



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Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	⟨v
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	_mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C °C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	VCC	2 to 6	V
Input voltage	$//\sqrt{\hat{v}_{jN}}$	0 to V _{CC}	٧
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Test Condition Ta = 25°C			Ta = -40 to 85°C		Unit	
				V _C C (V)	Min	Typ.	Max	Min	Max	
			_		1.50	_		1.50		
High-level input voltage	V _{IH}				3.15	_	(\leftarrow)	3.15	_	V
ŭ				6.0	4.20	-(4.20	_	
				2.0	\prec	((/	0,50	_	0.50	
Low-level input voltage	V_{IL}		_	4.5	->	7/,	1.35	_	1.35 V	V
					-((16	> 1.80	_	1.80	
		V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$	2.0	1.9	2.0	_	1.9	_	V
				4.5 <	4.4	4.5	_	44	7	
High-level output voltage	V _{OH}			6.0	5.9	6.0	- /	5.9	Ť	
			I _{OH} = -4 mA	4.5	4.18	4.31	-((4,13	< —	
			I _{OH} = -5.2 mA	6.0	5.68	5.80	4	5.63) —	
				2.0	_	0.0	0.1	50	0.1	
			I _{OL} = 20 μA	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage	V_{OL}	V _{IN} = V _{IH} or V _{IL}		6.0	_	0.0	<u>(0.1/</u>	_	0.1	V
			$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0		0.18	0.26	_	0.33	
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or	GND	6.0)	±0.1		±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or	GND	6.0			4.0	_	40.0	μА

AC Characteristics (C_L = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: t_r = t_f = 6 ns)

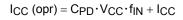
	\supset / \wedge					
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}		_	4	8	ns
Propagation delay time	t _{pLH}			17	29	ns
$(Pn, Qn-\overline{P}=\overline{Q})$	t _{pHL}			17	29	115
Propagation delay time	tpLH	*		10	18	ns
$(\overline{G} - \overline{P} = \overline{Q})$	tpHL	_		10	10	115

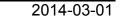
AC Characteristics (C $_{L}=50\ pF,$ input: $t_{r}=t_{f}=6\ ns)$

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	ļ		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Output transition time	t _{TLH} t _{THL}	_	2.0 4.5 6.0		30 8 7	75 15 13		95 19 16	ns
Propagation delay time (Pn, Qn- $\overline{P} = \overline{Q}$)	t _{pLH} t _{pHL}	_	2.0 4.5 6.0	-	60 21 17	170 34 29)) <u>~</u> 	215 43 37	ns
Propagation delay time (\overline{G} - $\overline{P=Q}$)	t _{pLH}	_	2.0 4.5 6.0	(40 13 10	110 22 19		140 28 24	ns
Input capacitance	C _{IN}	_	<	4	> ₅	10	4	10	pF
Power dissipation capacitance	C _{PD} (Note)	_		> <u></u>	32	-(5)	> _	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

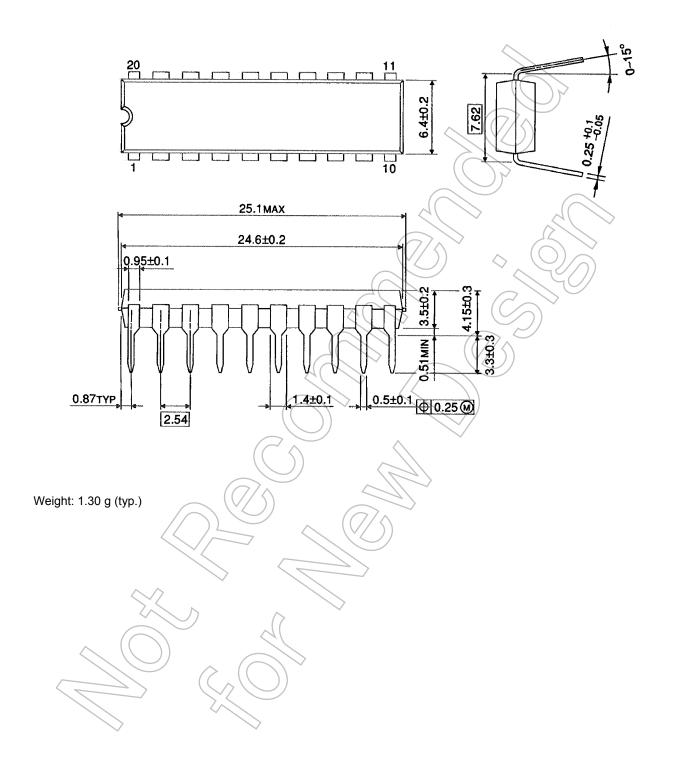
Average operating current can be obtained by the equation:





Package Dimensions

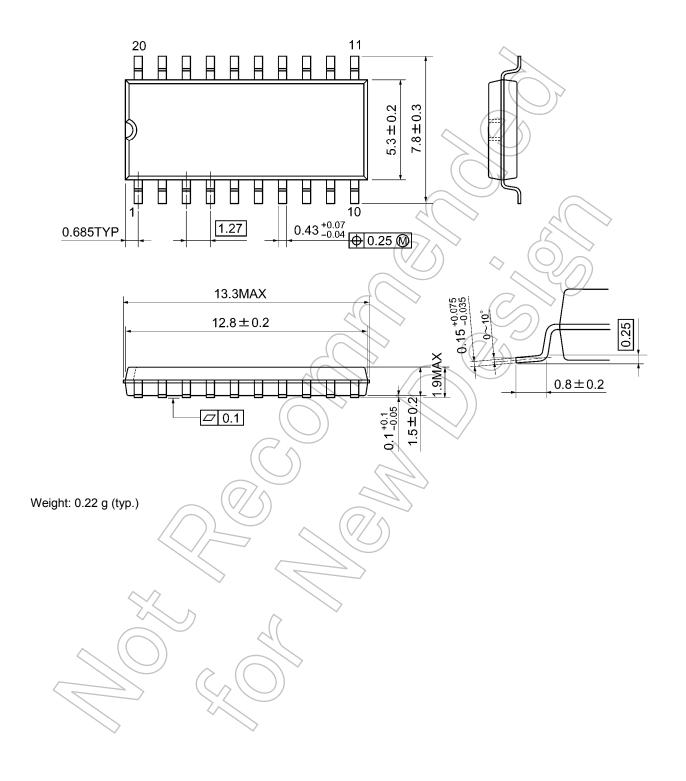
DIP20-P-300-2.54A Unit: mm



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Package Dimensions

SOP20-P-300-1.27A Unit: mm



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