

Press Pack IEGT Silicon N-Channel IEGT

# ST2000JXH35A

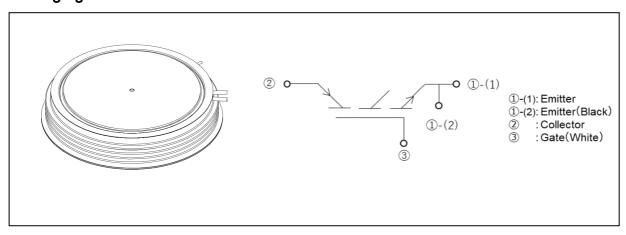
## 1. Applications

- · Electric power transmission and distribution
- · Motor Controllers
- · High-Power Switching

#### 2. Features

- (1) High reliability due to hermetic sealing structure.
- (2) Double side cooling type.

## 3. Packaging and Internal Circuit



1

Start of commercial production



## 4. Absolute Maximum Ratings (Note) (T<sub>c</sub> = 25 °C, unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Collector-emitter voltage	V <sub>CES</sub>			6500	V
Gate-emitter voltage	V <sub>GES</sub>			±20	V
Collector current (DC)	I <sub>C</sub>	(Note 1)		2000	Α
Collector current (pulsed)	I <sub>CP</sub>	(Note 2)		4000	Α
Collector power dissipation	Pc	(Note 3)	T <sub>f</sub> = 25 °C	17391	W
Junction temperature	Tj			-40 to 125	°C
Storage temperature	T <sub>stg</sub>			-40 to 125	°C
Mounting force	_	·		59 to 70	kN

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: T<sub>f</sub> = 87 °C

Note 2: Pulse width and repetition rate should be such that junction temperature (T<sub>i</sub>) does not exceed maximum T<sub>i</sub> rating.

Note 3: Refer to the application notes.

#### 5. Thermal Characteristics (Note)

Characteristics	Symbol	Note	Test Condition	Max	Unit
Thermal resistance (junction-to-fin)	R <sub>th(j-f)</sub>	(Note4)	Double side	5.75	K/kW

Note: Customers must also refer to and comply with the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and the instructions for the application with which the Product will be used with or for.

Note4: Conductive thermal compound is added.



## 6. Electrical Characteristics

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate-emitter leakage current	I <sub>GES</sub>	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0 \text{ V}, T_j = 25 \text{ °C}$	_	_	±100	nA
Collector-emitter cut-off current	I <sub>CES</sub>	V <sub>CE</sub> = 6500 V, V <sub>GE</sub> = 0 V, T <sub>j</sub> = 25 °C	_	_	1	mA
Gate-emitter cut-off voltage	V <sub>GE(off)</sub>	$I_C = 2.0 \text{ A}, V_{CE} = 5 \text{ V}, T_j = 25 ^{\circ}\text{C}$	6.10	6.60	7.10	V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 2000 A, V <sub>GE</sub> = 15 V, T <sub>j</sub> = 25 °C	_	2.30	_	V
		I <sub>C</sub> = 2000 A, V <sub>GE</sub> = 15 V, T <sub>j</sub> = 125 °C	_	2.80	3.25	
Input capacitance	C <sub>ies</sub>	$V_{CE} = 10 \text{ V}, V_{GE} = 0 \text{ V}, f = 100 \text{ kHz},$ $T_j = 25 ^{\circ}\text{C}$	_	514		nF
Switching time (rise time)	t <sub>r</sub>	$\begin{array}{c} V_{CC} = 3600 \text{ V, } I_{C} = 2000 \text{ A,} \\ R_{G(on)} = 2.0 \ \Omega, \ R_{G(off)} = 30 \ \Omega, \\ V_{GE} = \pm 15 \text{ V, } T_{j} = 125 \ ^{\circ}\text{C} \\ \text{Diode:} 2000 \text{ JXHH35} \\ T_{j} = 125 \ ^{\circ}\text{C} \\ \text{(Inductive load, } L_{s} \approx 300 \text{ nH)} \\ \text{See Fig. 6.1 and Fig. 6.2} \end{array}$	_	0.31	_	μS
Switching time (turn-on delay time)	t <sub>d(on)</sub>		_	0.48	_	μS
Switching time (turn-on time)	t <sub>on</sub>		_	0.79		μS
Switching time (fall time)	t <sub>f</sub>		_	1.94		μS
Switching time (turn-off delay time)	t <sub>d(off)</sub>		_	13.70		μS
Switching time (turn-off time)	t <sub>off</sub>		_	15.64		μS
Turn-on switching loss	Eon		_	12.30		J
Turn-off switching loss	E <sub>off</sub>			15.60	_	J
Short-circuit pulse width	t <sub>psc</sub>	$V_{CC}$ = 4500 V, $V_{GE}$ = ±15 V, $R_{G(on)}$ = 2.0 $\Omega$ , $R_{G(off)}$ = 30 $\Omega$ , $L_s \approx$ 150 nH, $T_j$ = 125 °C	_	_	10	μS



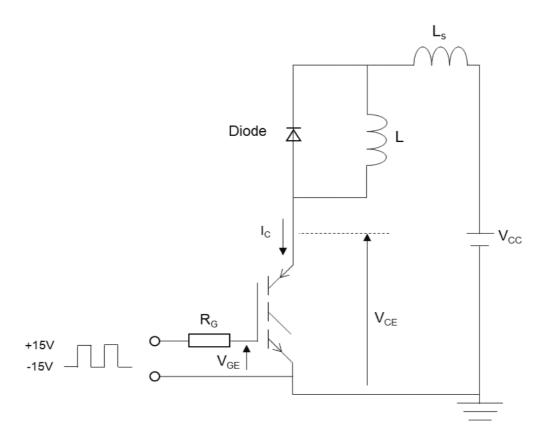


Fig. 6.1 Test Circuit

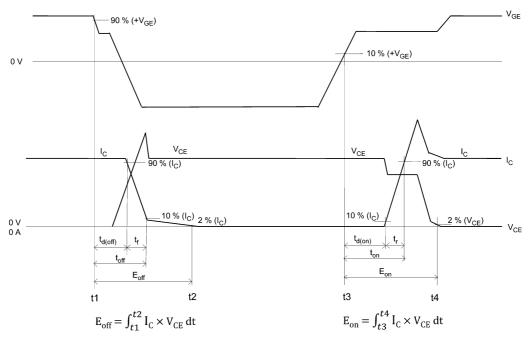


Fig. 6.2 Timing Chart



#### 7. Characteristics Curves (Note)

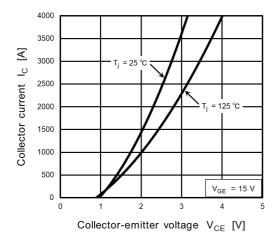


Fig. 7.1 I<sub>C</sub> - V<sub>CE</sub>

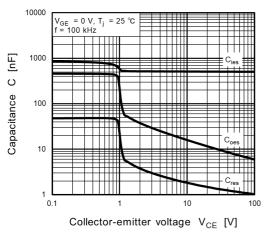


Fig. 7.3 Capacitance - V<sub>CE</sub>

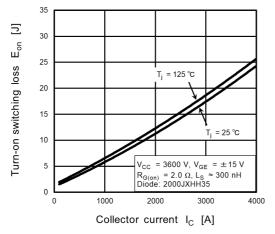


Fig. 7.5 E<sub>on</sub> - I<sub>C</sub>

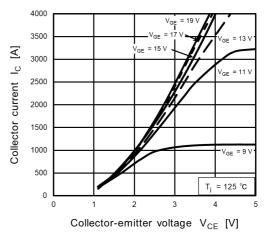
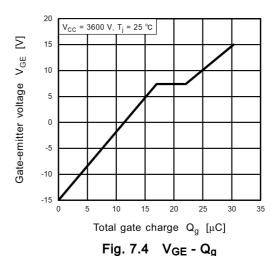


Fig. 7.2 I<sub>C</sub> - V<sub>CE</sub>



Collector current I<sub>C</sub> [A] Fig. 7.6 E<sub>off</sub> - I<sub>C</sub>



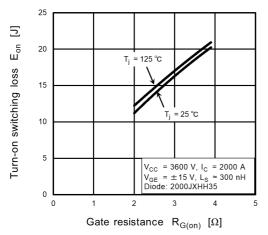


Fig. 7.7 E<sub>on</sub> - R<sub>G(on)</sub>

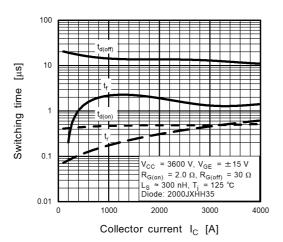


Fig. 7.9 Switching time - I<sub>C</sub>

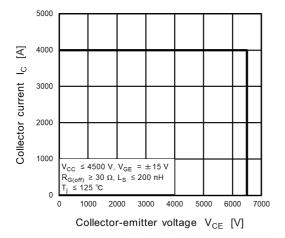


Fig. 7.11 RBSOA (Guaranteed value)

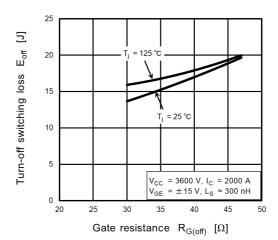


Fig. 7.8 E<sub>off</sub> - R<sub>G(off)</sub>

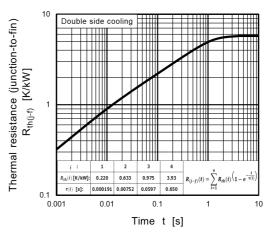


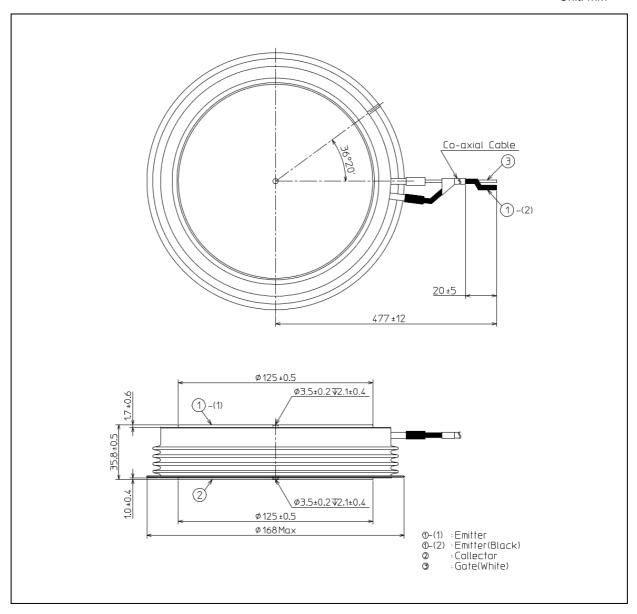
Fig. 7.10 R<sub>th(i-f)</sub> - t (Guaranteed value)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 3750 g (typ.)

	Package Name(s)
TOSHIBA: 2-168B1S	

Rev.1.0



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