TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC4075AP, TC74HC4075AF

Triple 3-Input OR Gate

The TC74HC4075A is a high speed CMOS 3-INPUT OR GATE fabricated with silicon gate C^2 MOS technology.

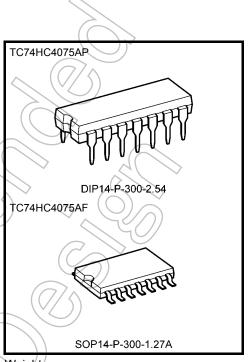
It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 4 stages including a buffered outputs, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

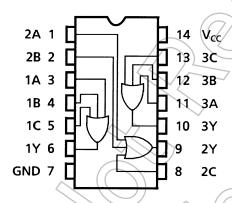
- High speed: $t_{pd} = 8 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 4075B



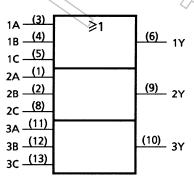
Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.)

Pin Assignment



IEC Logic Symbol



Start of commercial production 1988-04

Truth Table

Α	В	С	Υ
Н	Х	Х	Н
Х	Н	Х	Н
Х	Х	Н	Н
L	L	L	L

X: Don't care

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to $V_{CC} + 0.5$	V 0
DC output voltage	V _{OUT}	−0.5 to V _{CC} + 0.5	V
Input diode current	l _{IK}	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	_mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	∫ mW
Storage temperature	T _{stg}	-65 to 150	∕ °C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	VIN	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t_r , t_f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

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Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit	
Sharastonistics	Cymbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	
				2.0	1.50	_ <	/_	1.50	_	
High-level input voltage	V _{IH}		_	4.5	3.15	_		3.15	_	V
				6.0	4.20	_	1	4.20	_	
				2.0	_	10	0.50	_	0.50	
Low-level input voltage	V_{IL}		_	4.5	4	/ / //	1)35	_	1.35	V
				6.0	-2		1.80	_	1.80	
				2.0	1.9	2.0	^{>} —	1.9	_	
		V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage	V _{OH}			6.0 <	5.9	6.0	_	5.9	\rightarrow	V
			$I_{OH} = -4 \text{ mA}$	4,5	4.18	4.31	{_	4.13	> —	
			$I_{OH} = -5.2 \text{ mA}$	6.0//	5.68	5.80	-(5.63	_	
		V _{IN} = V _{IH} or V _{IL}	(2.0		0.0	0.1	4	0.1	
			I _{OL} = 20 μA	4.5	_	0.0	⊋0.1	>_	0.1	
Low-level output voltage	V _{OL}		40	6.0	_	0.0	(0.1)	_	0.1	V
			I _{OL} = 4 mA	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 5.2 mA	6.0	1	0.18	0.26	_	0.33	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or	GND	6.0			±0.1		±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or	GND	6.0		/	1.0	_	10.0	μА

AC Characteristics ($C_L = 15 \text{ pF}$, $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Sŷmbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}	<u> </u>	_	4	8	ns
Propagation delay time	t _{pLH}	_	_	8	15	ns

AC Characteristics ($C_L = 50$ pF, input: $t_r = t_f = 6$ ns)

Characteristics Symb	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
	4		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	_	4.5	_	8	15	_	19	ns
	t _{THL}		6.0	_	7	13	_	16	
	t _{pLH}	_	2.0	_	35	90	12	115	
timo			4.5	_	11	18	<i>y</i> _	23	ns
	t _{pHL}		6.0	_	9	/15	_	20	
Input capacitance	C _{IN}	_		-/	5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)			-(27)	> —	_	_	pF

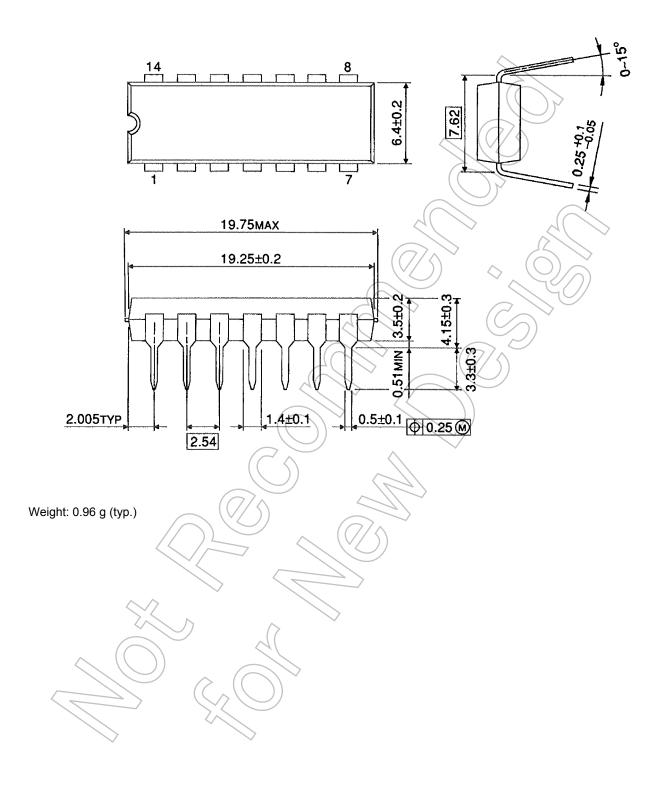
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ (per gate)

Package Dimensions

DIP14-P-300-2.54 Unit: mm

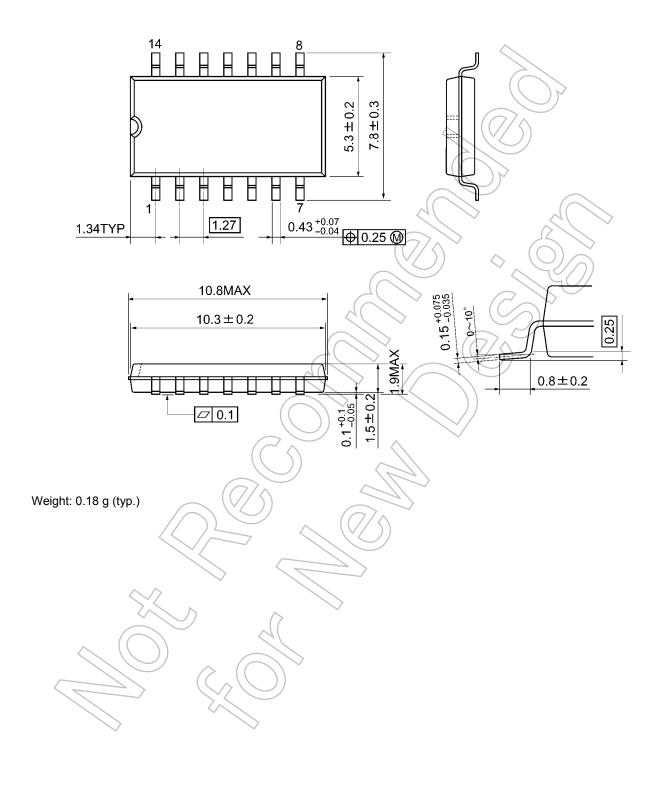


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Package Dimensions

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SOP14-P-300-1.27A Unit: mm



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