TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS III)

TPCF8102

Notebook PC Applications Portable Equipment Applications

• Low drain-source ON resistance: $R_{DS (ON)} = 24 \text{ m}\Omega \text{ (typ.)}$

• High forward transfer admittance: $|Y_{fS}| = 14 \text{ S (typ.)}$

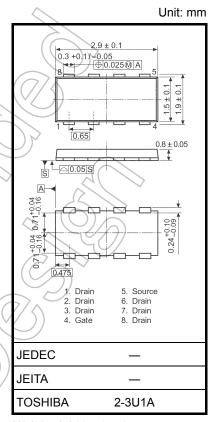
• Low leakage current : $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -20 \text{ V)}$

• Enhancement mode : V_{th} = −0.5 to −1.2 V

 $(V_{DS} = -10 \text{ V}, I_{D} = -200 \mu\text{A})$

Absolute Maximum Ratings (Ta = 25°C)

				////
Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	-20	V
Drain-gate voltage (R	$GS = 20 \text{ k}\Omega$)	V _{DGR}	-20	> V
Gate-source voltage		V _{GSS}	±8	V
Drain aurrant	DC (Note 1) I _D	-6	Λ
Drain current	Pulsed (Note 1	I _{DP}	-24	
Drain power dissipation	on (t = 5 s (Note 2a		2.5	W
Drain power dissipation	on (t = 5 s (Note 2b		0.7	w
Single pulse avalanch	ne energy (Note 3	EAS	5.9	mJ
Avalanche current		IAR	-3	A
Repetitive avalanche	energy (Note 4	EAR	0.25	mJ
Channel temperature	//) [T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55~150	°C



Weight: 0.011 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

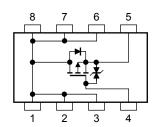
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	R _{th (ch-a)}	50.0	°C/W
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2b)	R _{th (ch-a)}	178.6	°C/W

Note: (Note 1), (Note 2), (Note 3) and (Note 4): See the third page.

This transistor is an electrostatic-sensitive device. Please handle with caution.

Circuit Configuration



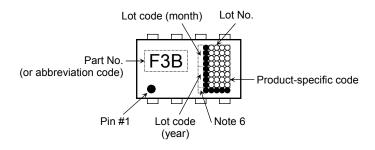
Electrical Characteristics (Ta = 25°C)

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА	
Drain cut-off curre	ent	I _{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА	
Drain-source breakdown voltage		V _{(BR)DSS}	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-20	_		V	
		V _{(BR) DSX}	$I_D = -10 \text{ mA}, V_{GS} = 8 \text{ V}$	-12	_	_	V	
Gate threshold vo	oltage	V_{th}	$V_{DS} = -10 \text{ V}, I_D = -200 \mu\text{A}$	-0.5))	-1.2	٧	
		R _{DS} (ON)	$V_{GS} = -1.8 \text{ V}, I_D = -1.5 \text{ A}$) // /	67	90		
Drain-source ON	Drain-source ON resistance		$V_{GS} = -2.5 \text{ V}, I_D = -3.0 \text{ A}$	\mathcal{L}	36	41	$m\Omega$	
		R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -3.0 \text{ A}$		24	30		
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -3.0 \text{ A}$	7	14	_	S	
Input capacitance	•	C _{iss}			1550	_		
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		215	\rightarrow	pF	
Output capacitance		Coss		-	265	> —		
Switching time	Rise time	t _r	V _{GS} 0 V 1 _D = -3.0 A C V _{OUT} C C C C C C C C C C C C C C C C C C C	2 W	40) —		
	Turn-on time	t _{on}		7	13		20	
	Fall time	t _f	4.7.7.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.		21		ns	
	Turn-off time	t _{off}	V _{DD} ≃ −10 V Duty ≤ 1%, t _w = 10 μs	_	68			
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -16 \text{ V}, V_{GS} = -5 \text{ V},$	_	19	_		
Gate-source charge		Qgs	$I_D = -6.0 \text{ A}$	_	14	_	nC	
Gate-drain ("miller") charge		Q _{gd}		_	5	_		

Source-Drain Ratings and Characteristics (Ta = 25°C)

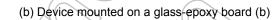
Charact	eristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	<u> </u>	_	_	-24	Α
Forward voltage	(diode)	V_{DSF}	$I_{DR} = -6.0 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

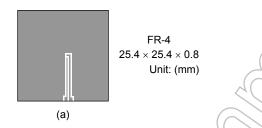
Marking (Note 5)

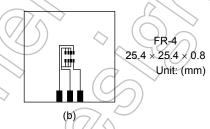


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)







Note 3: $V_{DD} = -16 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = -3.0 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: A dot on the lower left of the marking indicates Pin 1.

Note 6: A dot marking for identifying the indication of product Labels.

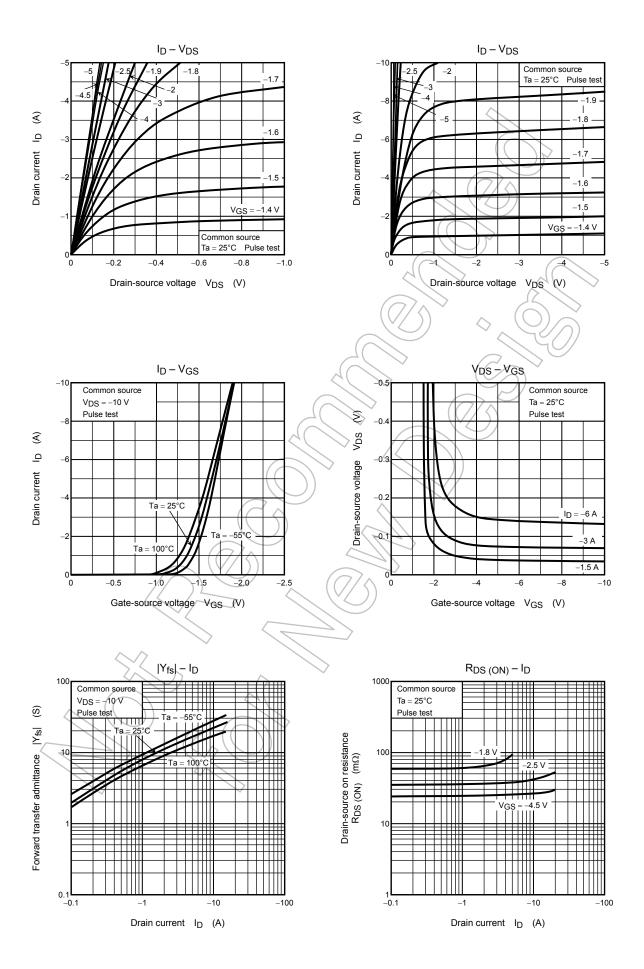
Without a dot: [[Pb]]/INCLUDES > MCV

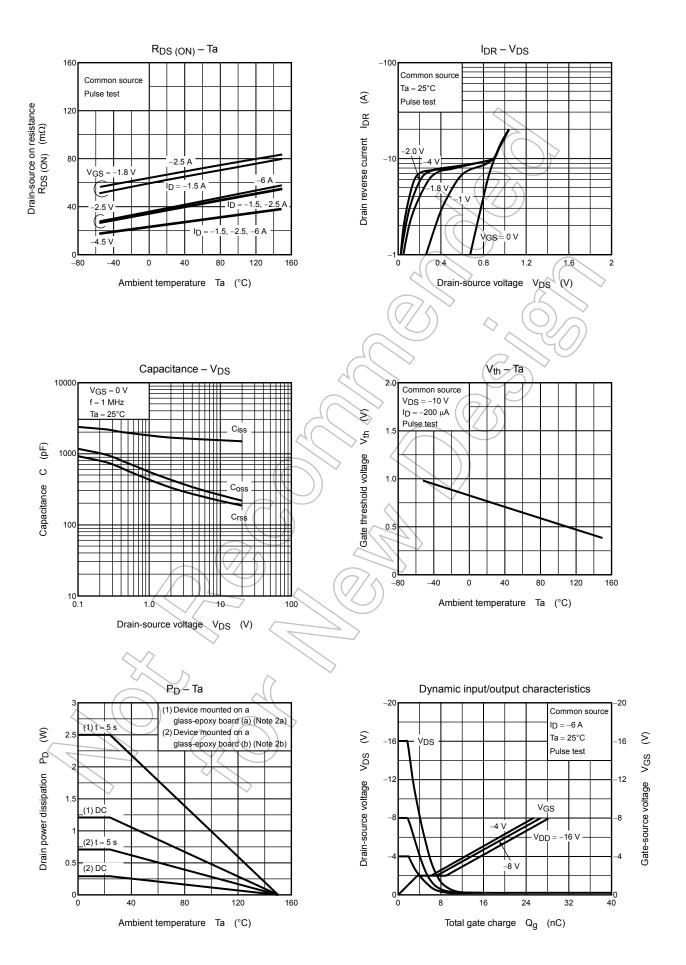
With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS ([Pb])

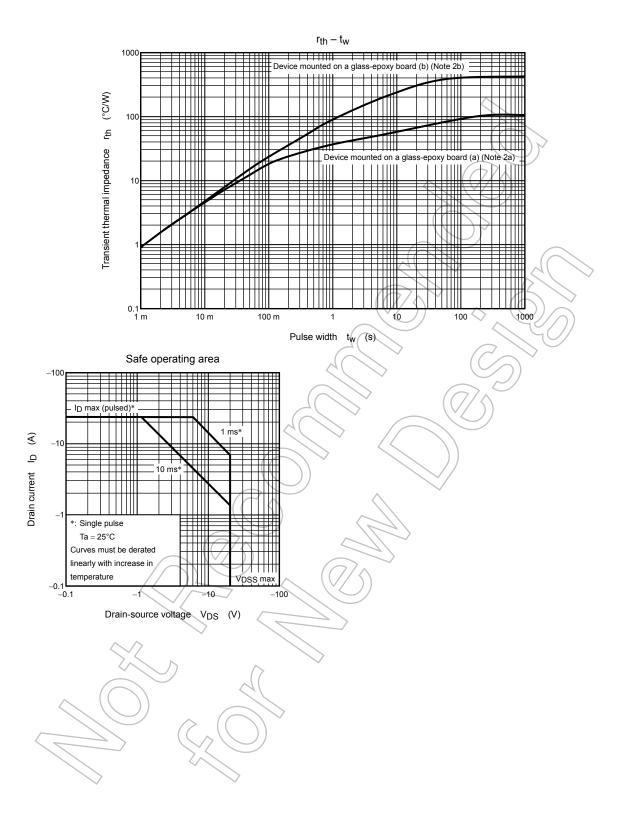
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