Photocouplers Photorelay

TLP3127

1. Applications

- High-Speed Memory Testers
- High-Speed Logic IC Testers
- Factory Automation (FA)
- Power supplies
- Mechanical relay replacements

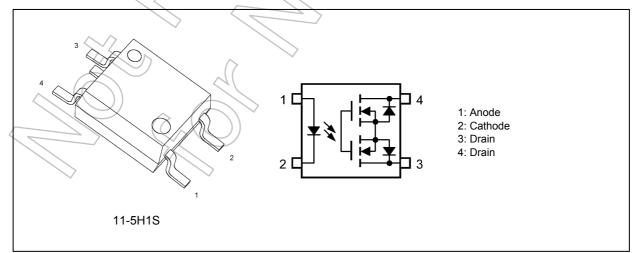
2. General

The TLP3127 photorelay consists of a photo MOSFET optically coupled to an infrared LED. It is housed in a 4pin package with 2.54-mm lead pitch and 2.1-mm height. The TLP3127 is a bi-directional switch, which can replace mechanical relays in many applications. And its high on-state current maximum rating and low on-state resistance is suitable to control a power line.

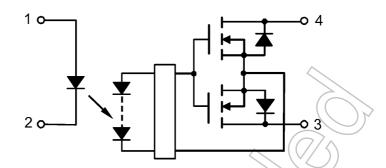
3. Features

- (1) Package: SOP(2.54SOP4) (Height 2.1 mm, pitch 2.54 mm)
- (2) Normally opened (1-Form-A)
- (3) OFF-state output terminal voltage: 60 V (min)
- (4) Trigger LED current: 3 mA (max)
- (5) ON-state current: 1.7 A (max) ($T_a = 25 \text{ °C}$), 1.3 A (max) ($T_a = 50 \text{ °C}$)
- (6) ON-state resistance: 0.13Ω (max)
- (7) Off-state capacitance: 250 pF (typ.)
- (8) Off-state current: 10 nA (max)
- (9) Isolation voltage: 1500 Vrms (min)
- (10) Safety standards
 UL-recognized: UL 1577, File No.E67349
 cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349

4. Packaging and Pin Assignment



5. Internal Circuit



6. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25$ °C)

	Characteristi	cs	Symbol	Note	Rating	Unit
LED	Input forward current		l _F		30	mA
	Input forward current derating	(T _a ≥ 25 °C)	$\Delta I_F / \Delta T_a$		-0.3	mA/°C
	Input forward current (pulsed)	(100 μs pulse, 100 pps)))I _{FP} <	2 ((D)	Α
	Input reverse voltage		V _R		5	V
	Input power dissipation	$\langle \langle \rangle$	PD	\mathbb{Z}	50	mW
	Input power dissipation derating	(T _a ≥25 °C)	$\Delta P_{D} / \Delta T_{a}$	$\langle \rangle$	-0.5	mW/°C
	Junction temperature		Tj	\mathcal{S}	125	°C
Detector	OFF-state output terminal voltage		VOFF	$\hat{\mathbf{N}}$	60	V
	ON-state current		ION	٧	1.7	Α
	ON-state current	(T _a = 50 °C)	ION		1.3	Α
	ON-state current derating	(T _a ≥ 25 °C)	$\Delta I_{ON} / \Delta T_a$		-17.0	mA/°C
	ON-state current (pulsed)	(t = 100 ms)	IONP		5.0	Α
	Output power dissipation		Po		375.7	mW
	Output power dissipation derating	(T _a ≥ 25 °C)	$\Delta P_0 / \Delta T_a$		-3.76	mW/°C
	Junction temperature		Tj		125	°C
Common	Storage temperature		T _{stg}		-55 to 125]
	Operating temperature	$\sim (7/5)$	T _{opr}		-40 to 85	1
	Lead soldering temperature	(10.s)	T _{sol}		260	°C
	Isolation voltage	AC, 60 s, R.H. ≤ 60 %	BVS	(Note 1)	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

7. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Тур.	Max	Unit
Supply voltage	V _{DD}		_	—	48	V
Input forward current	١ _F		5	10	25	mA
ON-state current	I _{ON}	4		_	1.3	А
Operating temperature	T _{opr}		-20		65	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this data sheet should also be considered.

8. Electrical Characteristics (Unless otherwise specified, $T_a = 25$ °C)

	Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
LED	Input forward voltage	V _F		I _F = 10 mA	1.18	1.33	1.48	V
	Input reverse current	I _R		V _R = 5 V	_	A	10	μA
	Input capacitance	Ct		V = 0 V, f = 1 MHz	- /	2 70		pF
Detector	OFF-state current	I _{OFF}		V _{OFF} = 60 V	, –(())1	10	nA
	Output capacitance	C _{OFF}		V = 0 V, f = 1 MHz	X	250) —	pF

9. Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	G	I _{ON} = 100 mA)) _	0.6	3	mA
Return LED current	I _{FC}	40	I _{OFF} = 100 μA	0.1	_	_	mA
ON-state resistance	R _{ON}		I _{ON} = 1.7 A, I _F = 5 mA, t < 1 s	_	0.08	0.13	Ω

10. Isolation Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	Cs	(Note 1)	V _S = 0 V, f = 1 MHz	_	0.8	—	pF
Isolation resistance	Rs	(Note 1)	V _S = 500 V, R.H. ≤ 60 %	$5 imes 10^{10}$	1014	—	Ω
Isolation voltage	BVs	(Note 1)	AC, 60 s	1500			Vrms

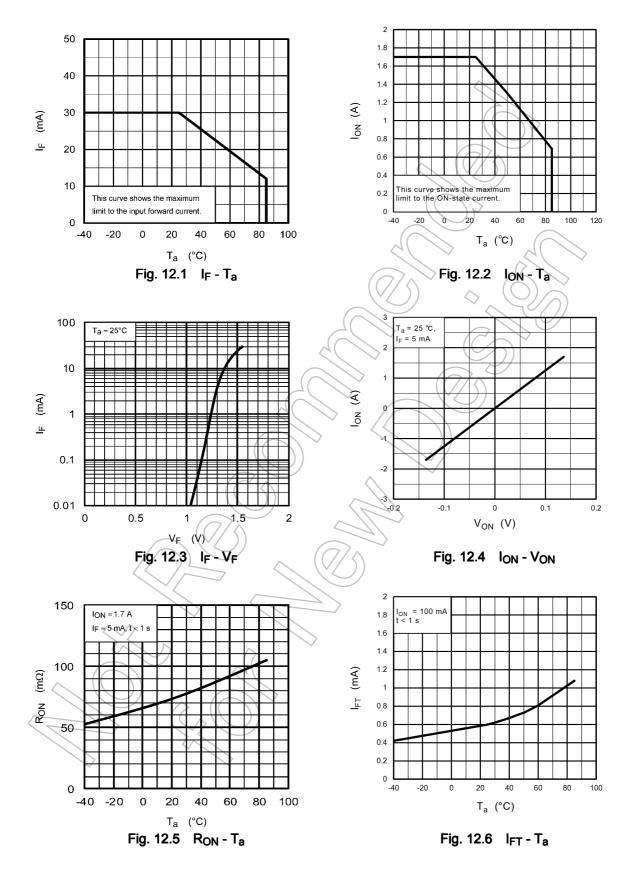
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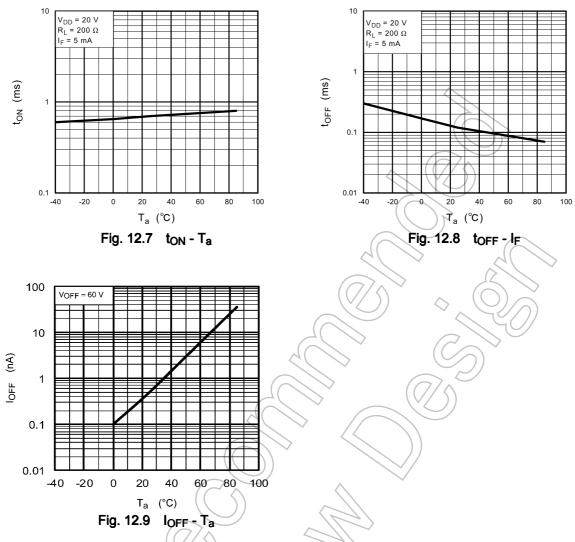


11. Switching Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
n-on time	t _{ON}		See Fig. 11.1. R _L = 200 Ω, V _{DD} = 20 V, I _F = 5 mA	—	0.7	3	ms
n-off time	t _{OFF}		See Fig. 11.1. R _L = 200 Ω, V _{DD} = 20 V, I _F = 5 mA	_	0.1	0.5	ms
IF 0-0- 2 0 F F	ig. 11.1			form			

12. Characteristics Curves (Note)





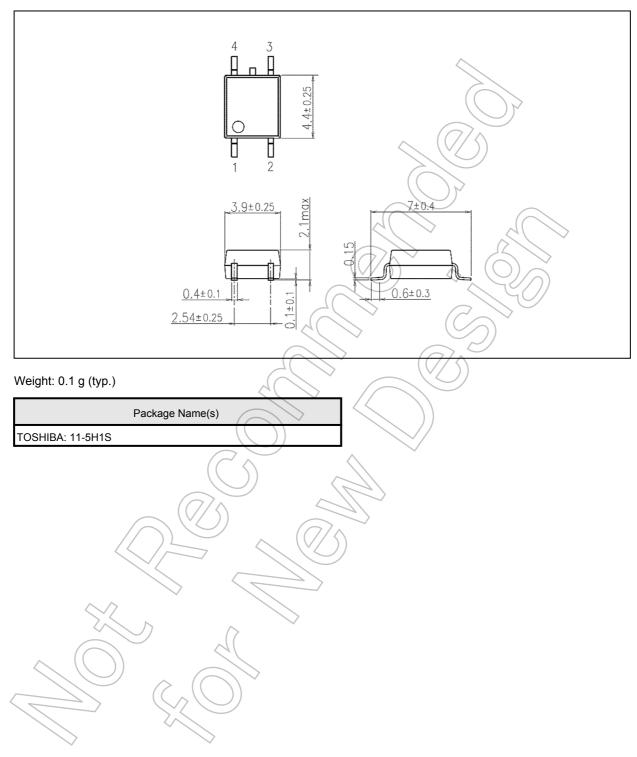
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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TLP3127

Package Dimensions

Unit: mm



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