

# TC74VHC139FK

## 1. Functional Description

- Dual 2-to-4 Line Decoder

## 2. General

The TC74VHC139FK is an advanced high speed CMOS 2 to 4 LINE DECODER/DEMULTIPLEXER fabricated with silicon gate C2MOS technology.

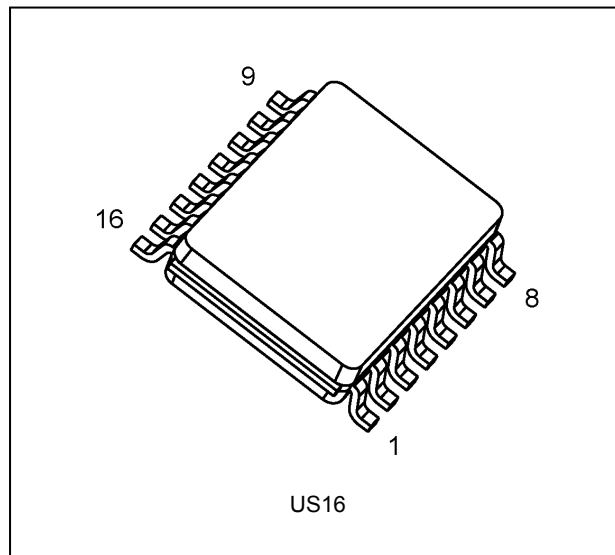
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The active low enable input can be used for gating or it can be used as a data input for demultiplexing applications. When the enable input is held High, all four outputs are fixed at a high logic level independent of the other inputs. An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

## 3. Features

- (1) High speed:  $t_{pd} = 5.0 \text{ ns (typ.)}$  at  $V_{CC} = 5.0 \text{ V}$
- (2) Low power dissipation:  $I_{CC} = 4.0 \mu\text{A (max)}$  at  $T_a = 25 \text{ }^\circ\text{C}$
- (3) High noise immunity:  $V_{NIH} = V_{NIL} = 28 \% V_{CC} \text{ (min)}$
- (4) Power down protection is provided on all inputs.
- (5) Balanced propagation delays:  $t_{PLH} \approx t_{PHL}$
- (6) Wide operating voltage range:  $V_{CC(opr)} = 2.0 \text{ V to } 5.5 \text{ V}$
- (7) Pin and function compatible with 74 series(AC/HC/AHC/LV etc.)139 type.

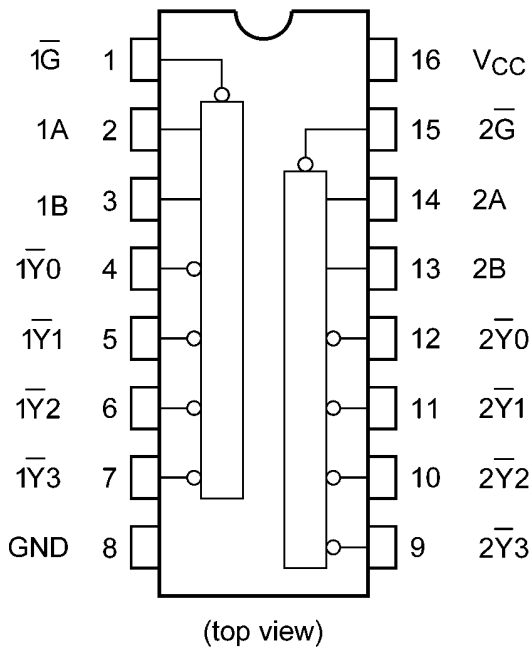
## 4. Packaging



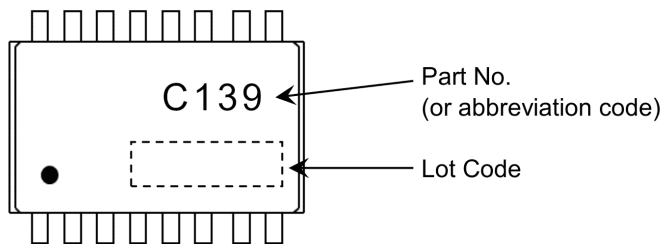
Start of commercial production

1991-11

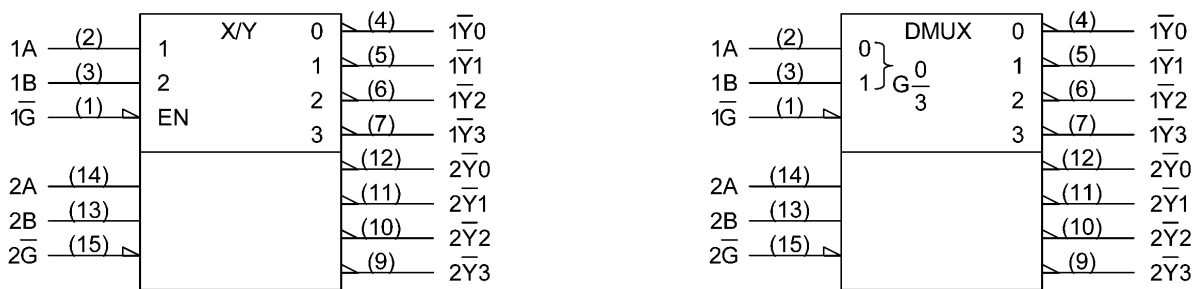
5. Pin Assignment



6. Marking



7. IEC Logic Symbol

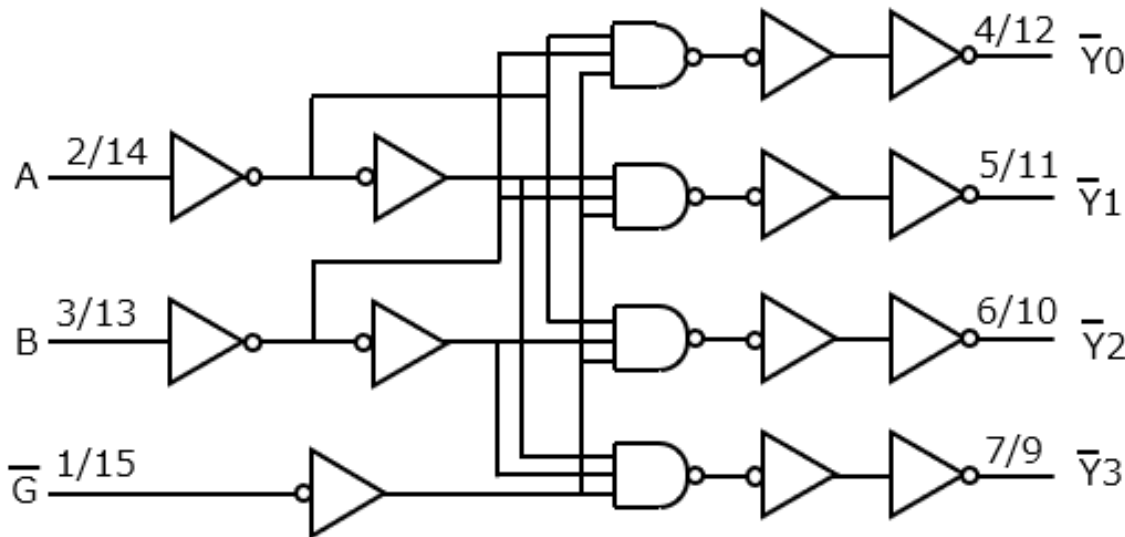


8. Truth Table

Inputs			Outputs				Selected Output
Enable	Select		$\overline{Y}0$	$\overline{Y}1$	$\overline{Y}2$	$\overline{Y}3$	
$\overline{G}$	B	A					
H	X	X	H	H	H	H	None
L	L	L	L	H	H	H	$\overline{Y}0$
L	L	H	H	L	H	H	$\overline{Y}1$
L	H	L	H	H	L	H	$\overline{Y}2$
L	H	H	H	H	H	L	$\overline{Y}3$

X: Don't care

9. Logic Diagram



### 10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	-0.5 to 7.0	V
Input voltage	$V_{IN}$	-0.5 to 7.0	V
Output voltage	$V_{OUT}$	-0.5 to $V_{CC} + 0.5$	V
Input diode current	$I_{IK}$	-20	mA
Output diode current	$I_{OK}$	$\pm 20$	mA
Output current	$I_{OUT}$	$\pm 25$	mA
$V_{CC}$ /ground current	$I_{CC}$	$\pm 75$	mA
Power dissipation	$P_D$	180	mW
Storage temperature	$T_{stg}$	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### 11. Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Rating	Unit
Supply voltage	$V_{CC}$		2.0 to 5.5	V
Input voltage	$V_{IN}$		0 to 5.5	V
Output voltage	$V_{OUT}$		0 to $V_{CC}$	V
Operating temperature	$T_{opr}$		-40 to 85	°C
Input rise and fall times	$dt/dv$	$V_{CC} = 3.3 \pm 0.3$ V	0 to 100	ns/V
		$V_{CC} = 5.0 \pm 0.5$ V	0 to 20	

Note: The operating ranges are required to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

### 12. Electrical Characteristics

#### 12.1. DC Characteristics (Unless otherwise specified, $T_a = 25\text{ }^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition		$V_{CC}$ (V)	Min	Typ.	Max	Unit
High-level input voltage	$V_{IH}$	—		2.0	1.50	—	—	V
				3.0 to 5.5	$V_{CC} \times 0.7$	—	—	
Low-level input voltage	$V_{IL}$	—		2.0	—	—	0.50	V
				3.0 to 5.5	—	—	$V_{CC} \times 0.3$	
High-level output voltage	$V_{OH}$	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -50\text{ }\mu\text{A}$	2.0	1.9	2.0	—	V
				3.0	2.9	3.0	—	
				4.5	4.4	4.5	—	
			$I_{OH} = -4\text{ mA}$	3.0	2.58	—	—	
			$I_{OH} = -8\text{ mA}$	4.5	3.94	—	—	
Low-level output voltage	$V_{OL}$	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 50\text{ }\mu\text{A}$	2.0	—	0.0	0.1	V
				3.0	—	0.0	0.1	
				4.5	—	0.0	0.1	
			$I_{OL} = 4\text{ mA}$	3.0	—	—	0.36	
			$I_{OL} = 8\text{ mA}$	4.5	—	—	0.36	
Input leakage current	$I_{IN}$	$V_{IN} = 5.5\text{ V or GND}$		0 to 5.5	—	—	$\pm 0.1$	$\mu\text{A}$
Quiescent supply current	$I_{CC}$	$V_{IN} = V_{CC} \text{ or GND}$		5.5	—	—	4.0	$\mu\text{A}$

#### 12.2. DC Characteristics (Unless otherwise specified, $T_a = -40\text{ to }85\text{ }^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition		$V_{CC}$ (V)	Min	Max	Unit
High-level input voltage	$V_{IH}$	—		2.0	1.50	—	V
				3.0 to 5.5	$V_{CC} \times 0.7$	—	
Low-level input voltage	$V_{IL}$	—		2.0	—	0.50	V
				3.0 to 5.5	—	$V_{CC} \times 0.3$	
High-level output voltage	$V_{OH}$	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -50\text{ }\mu\text{A}$	2.0	1.9	—	V
				3.0	2.9	—	
				4.5	4.4	—	
			$I_{OH} = -4\text{ mA}$	3.0	2.48	—	
			$I_{OH} = -8\text{ mA}$	4.5	3.80	—	
Low-level output voltage	$V_{OL}$	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 50\text{ }\mu\text{A}$	2.0	—	0.1	V
				3.0	—	0.1	
				4.5	—	0.1	
			$I_{OL} = 4\text{ mA}$	3.0	—	0.44	
			$I_{OL} = 8\text{ mA}$	4.5	—	0.44	
Input leakage current	$I_{IN}$	$V_{IN} = 5.5\text{ V or GND}$		0 to 5.5	—	$\pm 1.0$	$\mu\text{A}$
Quiescent supply current	$I_{CC}$	$V_{IN} = V_{CC} \text{ or GND}$		5.5	—	40.0	$\mu\text{A}$

### 12.3. AC Characteristics (Unless otherwise specified, $T_a = 25\text{ }^{\circ}\text{C}$ , Input: $t_r = t_f = 3\text{ ns}$ )

Characteristics	Symbol	Note	$V_{CC}$ (V)	$C_L$ (pF)	Min	Typ.	Max	Unit
Propagation delay time (A, B - $\bar{Y}$ )	$t_{PLH}, t_{PHL}$		$3.3 \pm 0.3$	15	—	7.2	11.0	ns
				50	—	9.7	14.5	
			$5.0 \pm 0.5$	15	—	5.0	7.2	
				50	—	6.5	9.2	
Propagation delay time ( $\bar{G}$ - $\bar{Y}$ )	$t_{PLH}, t_{PHL}$		$3.3 \pm 0.3$	15	—	6.4	9.2	ns
				50	—	8.9	12.7	
			$5.0 \pm 0.5$	15	—	4.4	6.3	
				50	—	5.9	8.3	
Input capacitance	$C_{IN}$		—	—	—	4	10	pF
Power dissipation capacitance	$C_{PD}$	(Note 1)	—	—	—	26	—	pF

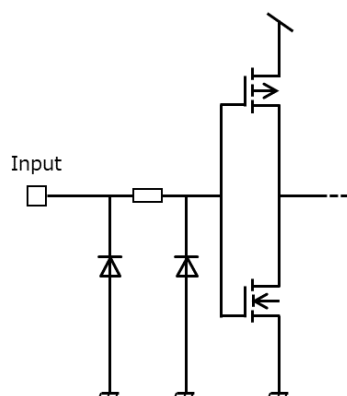
Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

$$I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/2 \text{ (per circuit)}$$

### 12.4. AC Characteristics (Unless otherwise specified, $T_a = -40\text{ to }85\text{ }^{\circ}\text{C}$ , Input: $t_r = t_f = 3\text{ ns}$ )

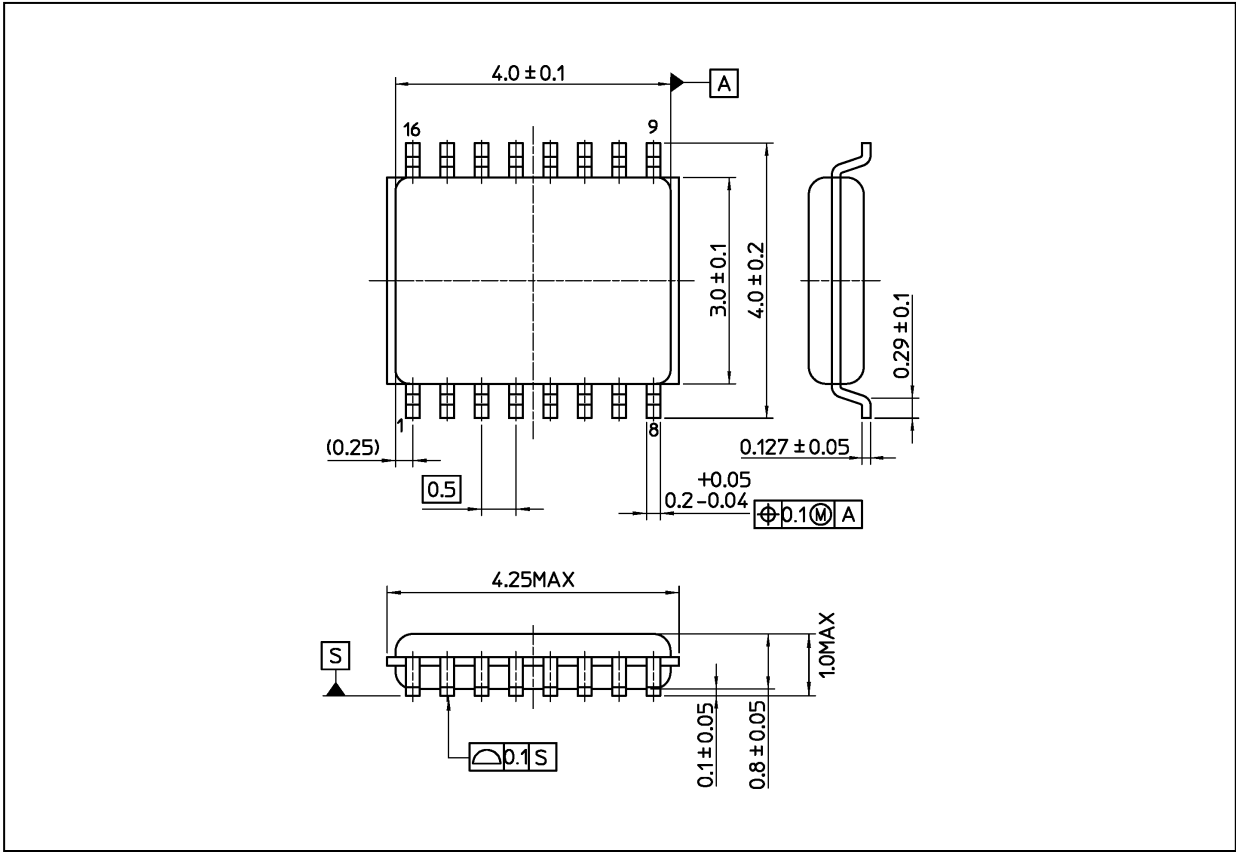
Characteristics	Symbol	$V_{CC}$ (V)	$C_L$ (pF)	Min	Max	Unit
Propagation delay time (A, B - $\bar{Y}$ )	$t_{PLH}, t_{PHL}$	$3.3 \pm 0.3$	15	1.0	13.0	ns
			50	1.0	16.5	
		$5.0 \pm 0.5$	15	1.0	8.5	
			50	1.0	10.5	
Propagation delay time ( $\bar{G}$ - $\bar{Y}$ )	$t_{PLH}, t_{PHL}$	$3.3 \pm 0.3$	15	1.0	11.0	ns
			50	1.0	14.5	
		$5.0 \pm 0.5$	15	1.0	7.5	
			50	1.0	9.5	
Input capacitance	$C_{IN}$	—	—	—	10	pF

### 13. Input Equivalent Circuit



Package Dimensions

Unit: mm



Weight: 0.02 g (typ.)

Package Name(s)
Nickname: US16

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