CMOS Digital Integrated Circuits Silicon Monolithic

TC74VHC138FK

1. Functional Description

3-to-8 Line Decoder

2. General

The TC74VHC138FK is an advanced high speed CMOS 3-to-8 DECODER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

When the device is enabled, 3 Binary Select inputs (A, B and C) determine which one of the outputs ($\overline{Y}0 - \overline{Y}7$) will go low.

When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go high.

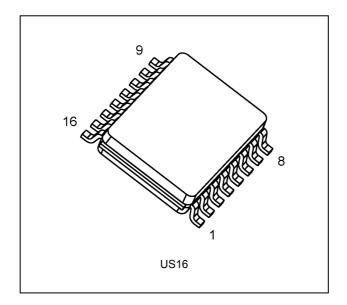
G1, $\overline{G}2A$, and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

3. Features

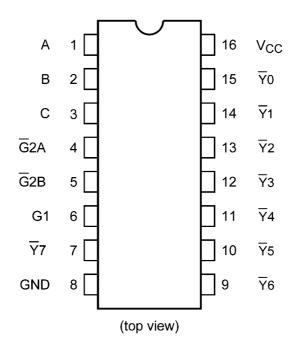
- (1) High speed: t_{pd} = 5.7 ns (typ.) at V_{CC} = 5.0 V
- (2) Low power dissipation: I_{CC} = 4.0 μ A (max) at T_a = 25 °C
- (3) High noise immunity: $V_{NIH} = V_{NIL} = 28 \% V_{CC}$ (min)
- (4) Power down protection is provided on all inputs.
- (5) Balanced propagation delays: $t_{PLH} \approx t_{PHL}$
- (6) Wide operating voltage range: $V_{CC(opr)} = 2.0 \text{ V to } 5.5 \text{ V}$
- (7) Pin and function compatible with 74 series(AC/HC/AHC/LV etc.)138 type.

4. Packaging

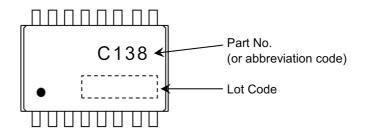


Start of commercial production 1991-11 2023-12-15

5. Pin Assignment



6. Marking



7. IEC Logic Symbol

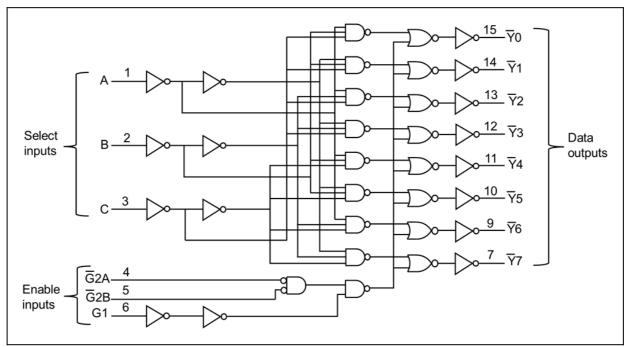
A (1) B (2) C (3) 4 BIN/OCT 2 2 4	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c c} A & (1) \\ B & (2) \\ C & (3) \\ \end{array} \begin{array}{c} DMUX \\ 0 \\ 2 \\ \end{array} \begin{array}{c} DMUX \\ G \frac{0}{7} \\ \end{array} $	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
G1 (6) G2A (4) G2B (5) EN	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	G1 (6) & G2A (4) G2B (5) (5) G2B	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

8. Truth Table

		Inp	uts			Outputs								
	Enable			Select		<u> </u>	T ₁	Ϋ́2	¥3	¥4	¥5	¥6	T7	Selected Output
G1	G2A	G2B	С	В	А	10		12	15	14	15	10	17	
L	x	х	Х	x	X	н	н	н	н	н	н	н	н	None
Х	н	Х	Х	Х	Х	н	н	н	н	н	н	н	н	None
Х	X	н	Х	Х	Х	н	н	н	н	н	н	н	н	None
н	L	L	L	L	L	L	н	н	н	н	н	н	н	Ψ0
н	L	L	L	L	н	н	L	н	н	н	н	н	н	Ϋ́1
н	L	L	L	н	L	н	н	L	н	н	н	н	н	۲2
н	L	L	L	н	н	н	н	н	L	н	н	н	н	¥3
н	L	L	Н	L	L	н	н	н	н	L	н	н	н	¥4
н	L	L	Н	L	н	н	н	н	н	Н	L	Н	Н	¥5
н	L	L	Н	н	L	н	н	н	н	н	н	L	н	₹6
н	L	L	Н	н	н	н	Н	Н	Н	Н	н	Н	L	¥7

X: Don't care

9. Logic Diagram



10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 7.0	V
Input voltage	V _{IN}	-0.5 to 7.0	V
Output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20	mA
Output current	I _{OUT}	±25	mA
V _{CC} /ground current	I _{CC}	±75	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

11. Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Rating	Unit
Supply voltage	V _{CC}	—	2.0 to 5.5	V
Input voltage	V _{IN}	—	0 to 5.5	V
Output voltage	V _{OUT}	—	0 to V _{CC}	V
Operating temperature	T _{opr}	—	-40 to 85	°C
Input rise and fall times	dt/dv	V_{CC} = 3.3 \pm 0.3 V	0 to 100	ns/V
		V_{CC} = 5.0 \pm 0.5 V	0 to 20	

Note: The operating ranges are required to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

12. Electrical Characteristics

12.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Unit
High-level input voltage	V _{IH}	—		2.0	1.50	_	—	V
				3.0 to 5.5	$V_{CC} \times 0.7$	_	—	
Low-level input voltage	V _{IL}	—		2.0	—	_	0.50	V
				3.0 to 5.5	—	-	$V_{CC} \times 0.3$	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OH} = -50 μA	2.0	1.9	2.0	—	V
				3.0	2.9	3.0	—	
				4.5	4.4	4.5	—	
			I _{OH} = -4 mA	3.0	2.58		—	
			I _{OH} = -8 mA	4.5	3.94		—	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 50 μA	2.0	—	0.0	0.1	V
				3.0	—	0.0	0.1	
				4.5	—	0.0	0.1	
			I _{OL} = 4 mA	3.0	—	_	0.36	
			I _{OL} = 8 mA	4.5	_		0.36	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	—		±0.1	μA
Quiescent supply current	I _{CC}	V_{IN} = V_{CC} or GND		5.5	_	_	4.0	μA

12.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		2.0	1.50	_	V
				3.0 to 5.5	$V_{CC} \times 0.7$	_	
Low-level input voltage	VIL	_		2.0	_	0.50	V
				3.0 to 5.5	_	$V_{CC} \times 0.3$	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OH} = -50 μA	2.0	1.9	_	V
				3.0	2.9	_	
				4.5	4.4	—	
			I _{OH} = -4 mA	3.0	2.48	_	
			I _{OH} = -8 mA	4.5	3.80	—	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 50 μA	2.0	—	0.1	V
				3.0	—	0.1	
				4.5	_	0.1	
			I _{OL} = 4 mA	3.0	_	0.44	
			I _{OL} = 8 mA	4.5	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5		±1.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		5.5	_	40.0	μA

12.3. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		$\textbf{3.3}\pm\textbf{0.3}$	15	_	8.2	11.4	ns
(A, B, C-Y)				50	_	10.0	15.8	
			5.0 ± 0.5	15	_	5.7	8.1	
				50	_	7.2	10.1	
Prop <u>ag</u> ation delay time	t _{PLH} ,t _{PHL}		$\textbf{3.3}\pm\textbf{0.3}$	15	_	8.1	12.8	ns
(G1-Y)				50		10.6	16.3	
			5.0 ± 0.5	15	_	5.6	8.1	
				50	_	7.1	10.1	
Propagation delay time	t _{PLH} ,t _{PHL}		3.3 ± 0.3	15		8.2	11.4	ns
(G2-Y)				50	_	10.7	14.9	
			5.0 ± 0.5	15	_	5.8	8.1	
				50	_	7.3	10.1	
Input capacitance	C _{IN}		_	•	_	4	10	pF
Power dissipation capacitance	C _{PD}	(Note 1)	_		_	34	_	pF

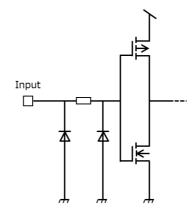
Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}$

12.4. AC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C, Input: t_r = t_f = 3 ns)

Characteristics	Symbol	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	3.3 ± 0.3	15	1.0	13.5	ns
(A, B, C-Y)			50	1.0	18.0	
		5.0 ± 0.5	15	1.0	9.5	
			50	1.0	11.5	
Propagation delay time	t _{PLH} ,t _{PHL}	$\textbf{3.3}\pm\textbf{0.3}$	15	1.0	15.0	ns
(G1-Y)			50	1.0	18.5	
		5.0 ± 0.5	15	1.0	9.5	
			50	1.0	11.5	1
Propagation delay time	t _{PLH} ,t _{PHL}	$\textbf{3.3}\pm\textbf{0.3}$	15	1.0	13.5	ns
(G2-Y)			50	1.0	17.0	
		5.0 ± 0.5	15	1.0	9.5	1
			50	1.0	11.5	
Input capacitance	C _{IN}	_	•		10	pF

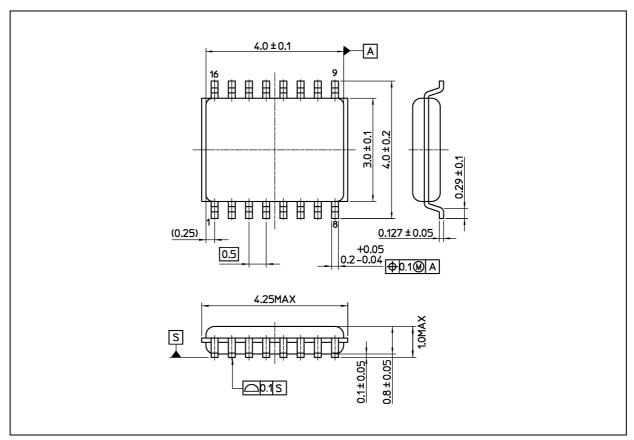
13. Input Equivalent Circuit



TC74VHC138FK

Package Dimensions

Unit: mm



Weight: 0.02 g (typ.)

	Package Name(s)
Nickname: US16	

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