TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVI)

# 2SK2989

Chopper Regulator, DC-DC Converter and Motor Drive Applications

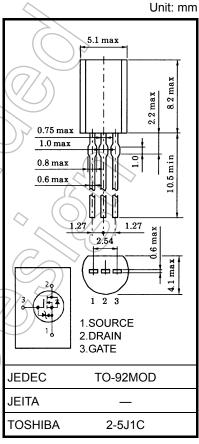
Low drain-source ON resistance : R<sub>DS</sub> (ON) = 120 mΩ (typ.)

High forward transfer admittance : |Y<sub>fS</sub>| = 2.6 S (typ.)
 Low leakage current : I<sub>DSS</sub> = 100 µA (max) (V<sub>DS</sub> = 50 V)

• Enhancement-mode :  $V_{th}$  = 1.0 to 2.2 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	50	V	
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	V <sub>DGR</sub>	50	V	
Gate-source voltage		V <sub>GSS</sub>	±20	/ (v	
Drain current	DC (Note 1)	ID	5	A	
	Pulse (Note 1)	IDP	)) 15		
Drain power dissipation	1	(PD \	0.9	(\ w	
Channel temperature		T <sub>ch</sub>	150	7,0	
Storage temperature ra	inge ((	<del>7</del> /√r <sub>stg</sub>	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	Rth (ch-a)	138	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

This transistor is an electrostatic sensitive device.

Please handle with caution.

#### **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	50	_	_	V
Gate threshold v	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.0	_	2.2	V
Drain-source ON resistance		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 1.3 A	1	240	330	- mΩ
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.5 A	) <u> </u>	120	150	
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.5 A	1.3	2.6	_	S
Input capacitano	e	C <sub>iss</sub>			145	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	25	-	pF
Output capacita	nce	Coss		_	75		
Switching time	Rise time	t <sub>r</sub>	VCS 10V ID=2.5A VOUT	- (	16	<u> </u>	
	Turn-on time	t <sub>on</sub>	$V_{\rm GS}$ $_{0V}$ $R_{\rm L}=10\Omega$		23	) —	
	Fall time	t <sub>f</sub>	v <sub>DD</sub> =25V		27	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{w}} = 10 \mu \mathbf{s}$	) –	110	_	
Total gate charg plus gate-drain)		Qg		_	6.5	_	
Gate-source ch	arge	Q <sub>gs</sub>	$V_{DD} \approx 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	_	5	_	nC
Gate-drain ("mil	ler") Charge	Q <sub>gd</sub>		_	1.5	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	-	ı	ı	15	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	l	1	-1.5	V

Marking

K2989 Part No. (or abbreviation code) Lot No. Note 2

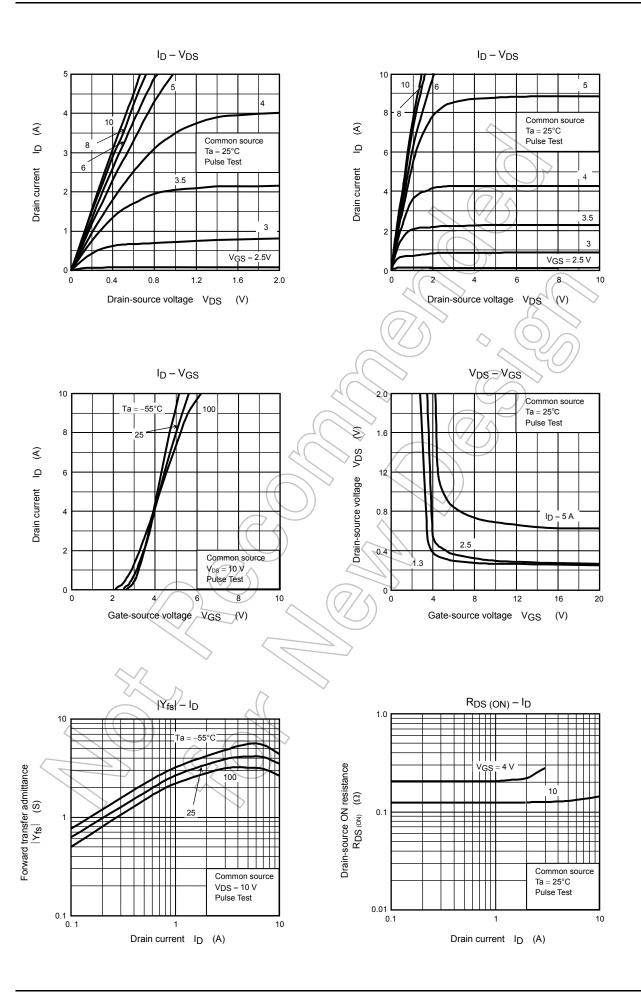
Note 2: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

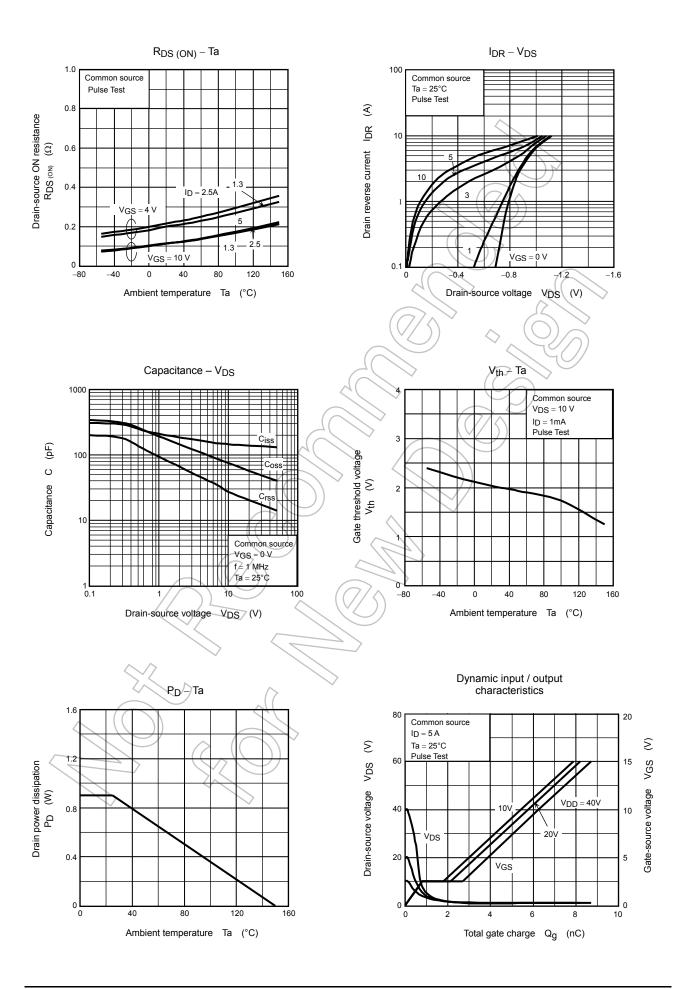
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

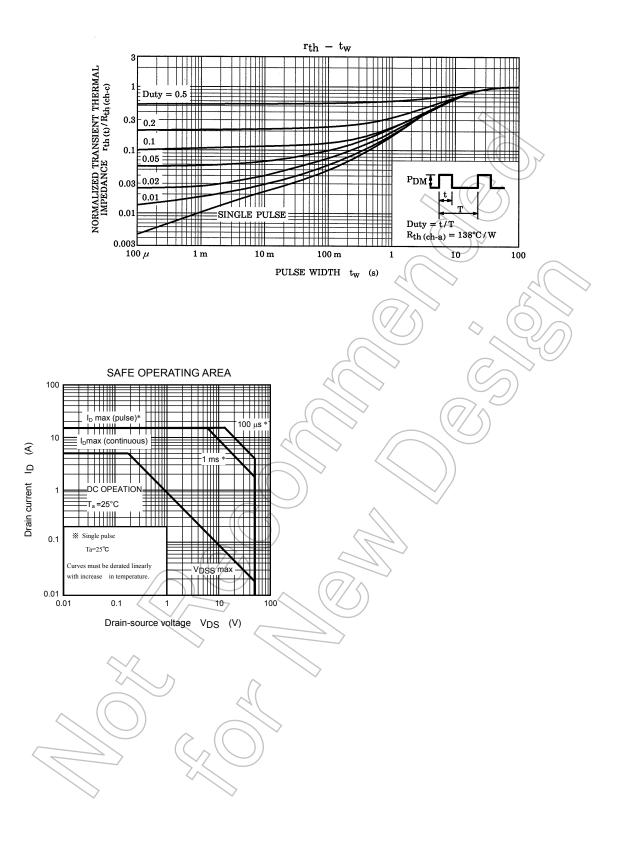
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