TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOSV)

2SK2920

Chopper Regulator, DC/DC Converter and Motor Drive Applications

• 4 V gate drive

• Low drain-source ON-resistance : $R_{DS(ON)} = 0.56 \Omega$ (typ.)

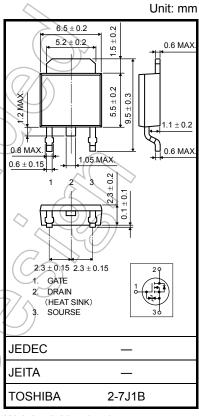
• High forward transfer admittance : |Yfs| = 4.5 S (typ.)

Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 200 V)

• Enhancement mode : $V_{th} = 1.5 \text{ to } 3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	200	V
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	200	V
Gate-source voltage		V_{GSS}	±20	> v
Drain current	DC (Note 1)	ID	5	Α
	Pulse (Note 1)	I _{DP}	20	A
Drain power dissipatio	n (Tc = 25°C)	PD	20	/w
Single-pulse avalanche	e energy (Note 2)	EAS	65	Ed.
Avalanche current		IAR	5	A
Repetitive avalanche	energy (Note 3)	(E _{AR})	2	Lm/
Channel temperature		Teh	150	°C
Storage temperature ra	ange	T _{\$tg}	−55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	6.25	°C/W
Thermal resistance, channel to ambient R _{th (ch-a)}	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 4.2 mH, R_G = 25 Ω , I_{AR} = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

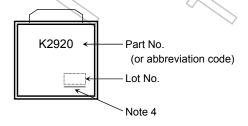
Electrical Characteristics (Ta = 25°C)

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cutoff curr	ent	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source bre	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	200	_	_	V
Gate threshold v	/oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source Of	N-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 2.5 A	1	0.56	0.8	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	2.0	4.5	_	S
Input capacitano	e	C _{iss}		()	440	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	35	_	pF
Output capacita	nce	Coss		· —	120	_	
Switching time	Rise time	t _r	V_{CS} V_{Out} V_{Out}	_	15	<u>\</u>	
	Turn-on time	ton	V_{GS} $_{0}$	-	20	<u>></u> –	no
	Fall time	t _f	$v_{DD} = 100 \text{ V}$	7	15	_	ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$		60	_	
Total gate charg plus gate-drain)	e (gate-source	Qg		_	10	_	
Gate-source cha	arge	Qgs	$V_{DD} \approx 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	_	6	_	nC
Gate-drain ("Mill	er") charge	Q _{gd}		_	4	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)		<u> </u>	_	_	5	Α
Pulse drain reverse current (Note 1)	\ IDRP \	-	_	_	20	Α
Forward voltage (diode)	V_{DSF}	$I_{DR} = 5 A$, $V_{GS} = 0 V$	_	_	-2.0	V
Reverse recovery time	t _{rr}	IDR = 5 A, VGS = 0 V, dIDR / dt = 100 A/µs	ı	150	1	ns
Reverse recovery charge	Qrr	1DR - 3 A, VGS - 0 V, αιDR / αι - 100 Α/μς	_	0.45	_	μC

Marking



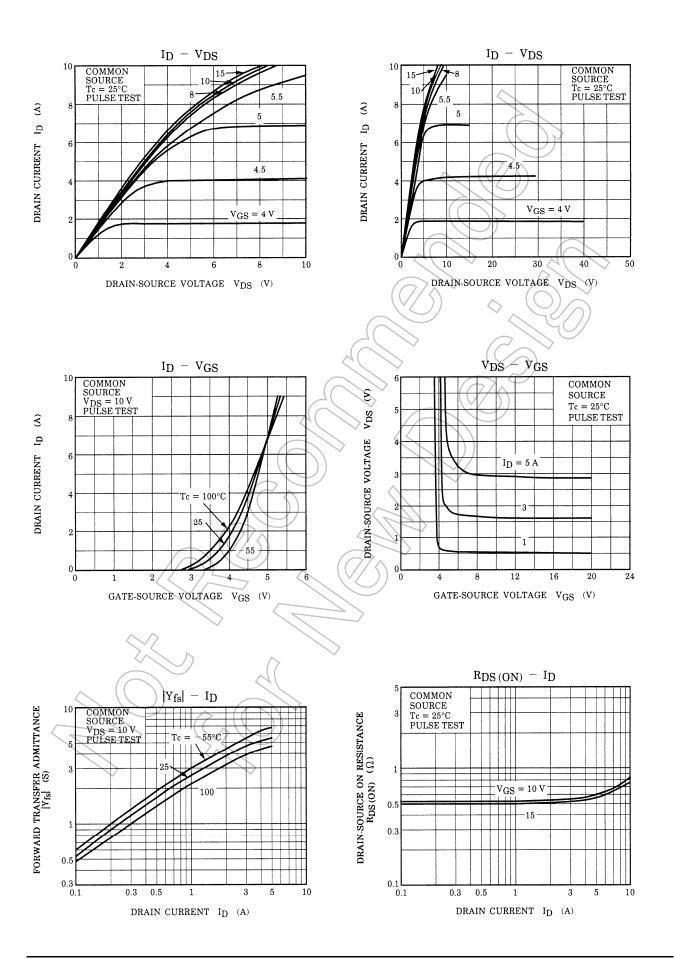
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

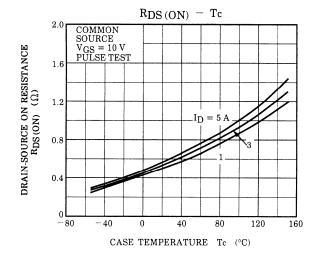
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

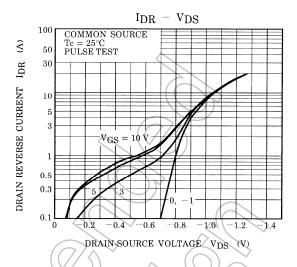
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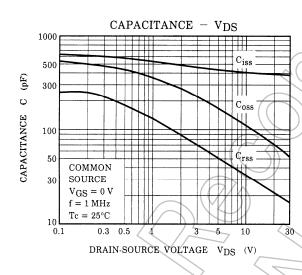
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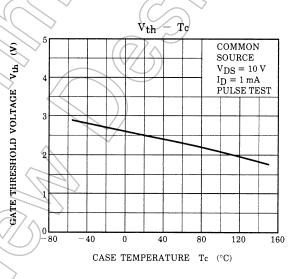


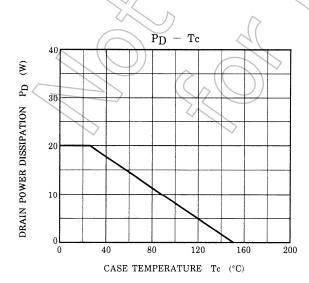
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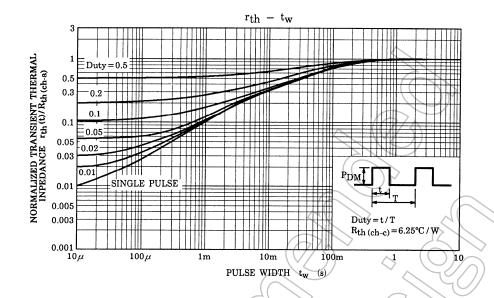


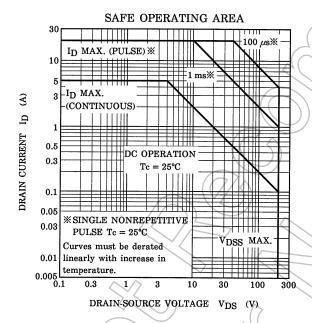


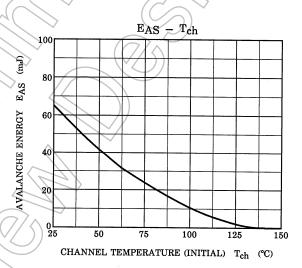


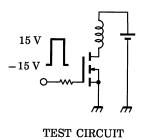


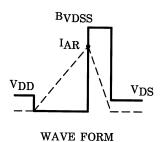
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 R_G = 25 Ω V_{DD} = 50 V, L = 4.2 mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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