TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

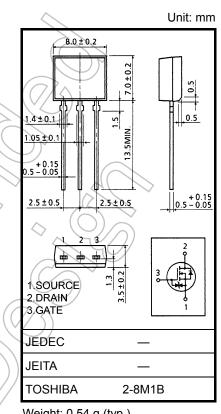
2SK2835

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance : $R_{DS (ON)} = 0.56 \Omega$ (typ.)
- High forward transfer admittance : |Y_{fs}| = 4.5 S (typ.)
- Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 200 V)
- Enhancement mode : V_{th} = 1.5 to 3.5 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	200	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	200	V	
Gate-source voltage		V _{GSS}	±20	> v	
Drain current	DC (Note 1)	۱ _D	5	А	
	Pulse (Note 1)	I _{DP}	20		
Drain power dissipation		P _D <	1.3	W	
Single pulse avalanche energy (Note 2)		EAS	65	Lm	
Avalanche current		IAR	5	Α	/
Repetitive avalanche energy (Note 3)		EAR	0.13	mJ	
Channel temperature		Tch	150	D°C	
Storage temperature ra	inge	∕∕T _{stg}	-55 to 150	°C	



Weight: 0.54 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambien	t Rth (ch-a)	96.1	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 4.2 mH, R_G = 25 Ω , I_{AR} = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

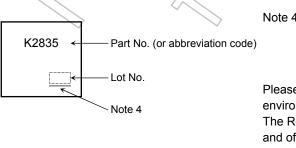
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	—	±10	μA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	200	—	—	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5		3.5	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 2.5 A	Æ	0.56	0.8	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	2.0	4.5		S
Input capacitance	ce	C _{iss}		\mathcal{O}	440		
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	35		pF
Output capacitance		C _{oss}		· _	120	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \prod_{U_{C}} \stackrel{I_{D}=2.5A}{_{0V}} V_{OUT}$	_	15	\searrow	ns
	Turn-on time	t _{on}			20	>_	
	Fall time	t _f			15	_	
	Turn-off time	t _{off}	V_{DD} ⇒ 100V Duty ≤1%, t _w = 10µs	2	60	_	
Total gate charg plus gate-drain)		Qg) —	10	_	
Gate-source ch	arge	Q _{gs}	$V_{DD} \approx 100 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$	_	6	_	nC
Gate-drain ("mi	ller") Charge	Q _{gd}		_	4	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		_	_	5	А
Pulse drain reverse current (Note 1)	IDRP		_	_	20	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	1 = -5.4 $1/22 = 0.1/24 = -100.4/100$		150	_	ns
Reverse recovery charge	Qrr	I _{DR} = 5 A, V _{GS} = 0 V, dI _{DR} / dt = 100 A / μs		0.45	_	μC

Marking

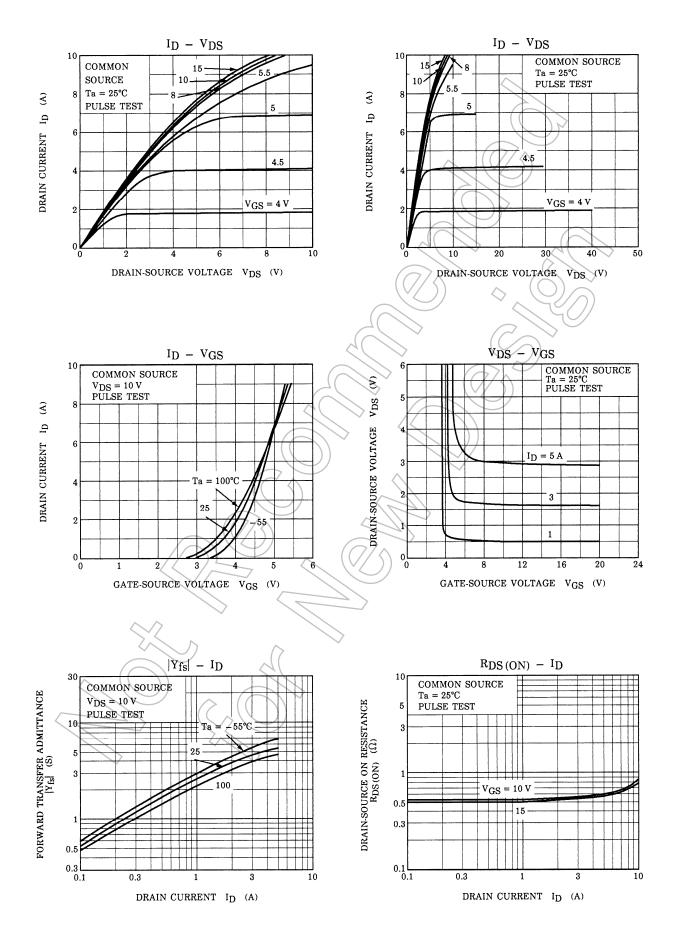


Note 4: A line under a Lot No. identifies the indication of product Labels.

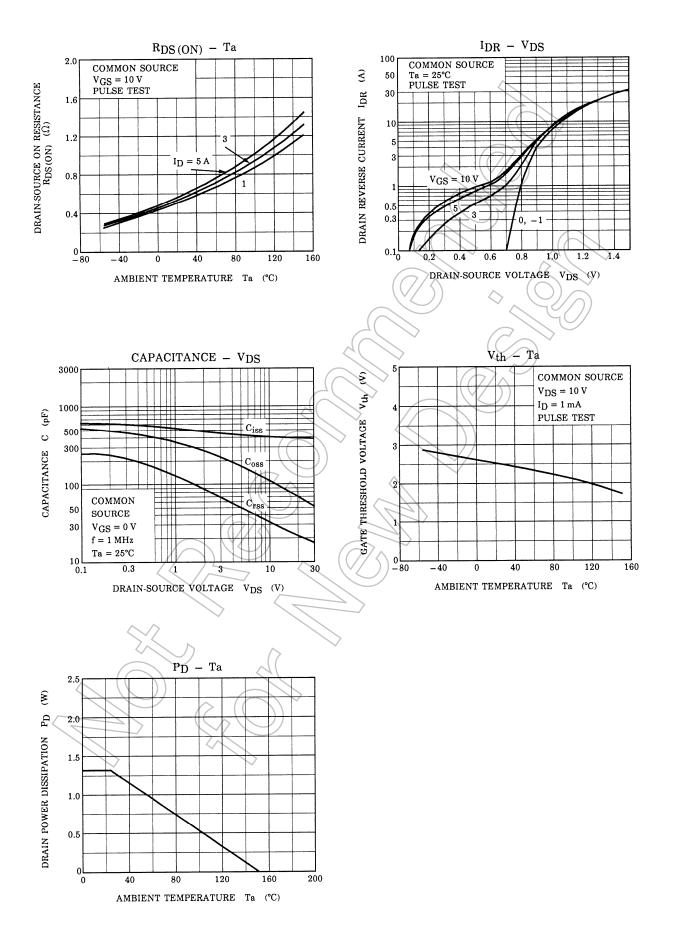
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

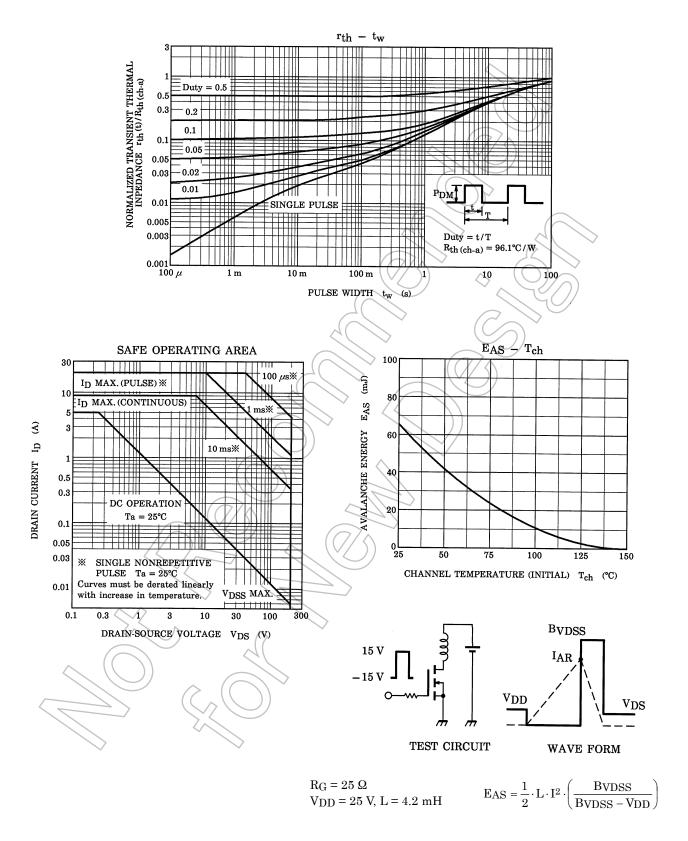
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