## TC78S600FTG Usage Considerations

The TC78S600FTG is a PWM constant-current type 2-phase bipolar stepping motor driver IC. The TC78S600FTG can be used in applications that require 1-2-phase, W1-2-phase, $2 \mathrm{~W} 1-2$ phase, and 4W1-2 phase excitation modes. The TC78S600FTG is capable of forward and reverse driving using only a clock signal.
(1) Operating range of power supply voltage

| Characteristics | Symbol | Operating power <br> supply voltage <br> range | Absolute <br> maximum <br> ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Control power supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | 2.7 to 5.5 | 6 | V |
| Motor power supply voltage | VM | 2.5 to 15 | 18 | V |

Note: The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings. Please use the IC within the specified operating ranges.

## (2) Power ON/OFF sequence with control input signals

When power supply is applied or turned off, STBY signal or ENABLE signal should be low. If STBY or ENABLE signal is high in turning on and shutdown the power supply, unexpected current may flow in the output pin depending on the conditions.

## 1. Output current

The absolute maximum rating is 1.0 A (peak). The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.

The average permissible current is restricted by total power dissipation. Please use the IC within the range of the power dissipation.

## 2. Power dissipation

$\mathrm{P}_{\mathrm{D}}-\mathrm{Ta}$ curve of the TC78S600FTG in each mounted condition is shown below.
(Upper limit of operating range of Ta is $85^{\circ} \mathrm{C}$.)


Power consumption in each excitation mode is calculated at a rough estimate as follows:

When Iout is 0.6 A , output saturated voltage ( $\mathrm{VSAT}(\mathrm{U}+\mathrm{L})$ ) is 0.96 V (Max).
When $\mathrm{V}_{\mathrm{CC}}$ is 3.3 V , consumption current $\mathrm{ICC}_{\mathrm{C}} 1$ is 6 mA (max).When VM is 5 V , $\mathrm{IM}_{1}$ is 2 mA (max).
In 1-2 phase excitation, W1-2 phase excitation, $2 \mathrm{~W} 1-2$ phase excitation, and $4 \mathrm{~W} 1-2$ phase excitation, 70 percent of Iout ( $100 \%$ ) is estimated roughly as an average value because the output wave form is pseudo sine wave form.

$$
\begin{aligned}
& \mathrm{P}_{\mathrm{D}}=\left((\mathrm{Iout} \times 0.7) \times \mathrm{V}_{\mathrm{SAT}}(\mathrm{U}+\mathrm{L})\right) \times 2+\mathrm{V}_{\mathrm{CC}} \times \mathrm{I}_{\mathrm{CC} 1}+\mathrm{VM} \times \mathrm{I}_{\mathrm{M} 1} \\
& =0.8064+0.0198+0.01 \\
& =0.84 \mathrm{~W}
\end{aligned}
$$

Please confirm the operation in the actual operation conditions because thermal characteristics changes widely depending on the discharge characteristics of the board and the transient characteristics in the mounted state.

## 3. Application circuit example



## (1) Capacitors for the power supply lines

Capacitors for the power supply lines between VCC and GND should be connected as close to the IC as possible.

Recommended value

| Characteristic | Recommended Value | Remarks |
| :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}-\mathrm{GND}$ | $10 \mu \mathrm{~F}$ to $100 \mu \mathrm{~F}$ | Electrolytic capacitor |
|  | $0.1 \mu \mathrm{~F}$ to $1 \mu \mathrm{~F}$ | Ceramic capacitor |

## (2) Capacitors for VM terminal

Capacitors for VM terminal between VM and GND should be connected as close to the IC as possible.

## Recommended Value

| Characteristic | Recommended Value | Remarks |
| :---: | :---: | :---: |
| VM-GND | $10 \mu \mathrm{~F}$ to $100 \mu \mathrm{~F}$ | Electrolytic capacitor |
|  | $0.1 \mu \mathrm{~F}$ to $1 \mu \mathrm{~F}$ | Ceramic capacitor |

## (3) GND

Metallic exposure part on the back of package is a part of the lead frame.
Since the chip is mounted on this lead frame, using a wide ground trace by soldering the metallic exposure part with the printed wiring board aides heat removal. If a large current flows through a ground trace, the ground trace must be designed to be wide as possible. This pattern on the printed wiring board should be connected to the GND of the IC or should not be connected anything electrically.

## (4) Capacitor for the OSC terminal

The internal oscillation frequency is determined by the value of the capacitor connected to the OSC terminal.
When Cosc is 220 pF , the internal oscillation frequency is 320 kHz (typ.).

## (5) Resistors for the RFA and RFB terminals

The value of the current detecting resistor (RNF) which is connected to the RFA and RFB terminals determines the current that flows through A- and B-phases of the motor.
When the upper limit of the current across the motor coil is assumed I (Limit),

$$
\mathrm{I}(\text { Limit })(\mathrm{A})=1 / 5 \times \operatorname{Vref}(\mathrm{V}) \div \mathrm{RNF}(\mathrm{~V})
$$

(Torque setting is $100 \%$, that is to say, TQ pin is high level.)
Ex.) When torque setting is $100 \%(\mathrm{TQ}=\mathrm{H})$, Vref is 2.5 V , and the maximum current is 0.5 A , external resistance is $1.0 \Omega$. Then, when torque setting is changed to $25 \%$ ( $\mathrm{TQ}=\mathrm{L}$ ) under the same condition, the maximum current is 0.125 A .

Vref should be set as follows; 0.5 V to 3.4 V , Vcc (max.) - 1.8 V .
In case TQ is High, Vref must not exceed 2.5 V in using.
Operating accuracy is inferior when Vref is set less than 0.5 V .
The resistance of $0.25 \Omega$ or more should be connected to RNF.

## 4. Excitation mode setting

The excitation mode can be selected from 2 phase, $1-2$ phase, $\mathrm{W} 1-2$ phase, and $2 \mathrm{~W} 1-2$ phase modes using the M1 and M2 inputs. It is capable of forward and reverse driving of a two-phase bipolar stepping motor with CW and CCW terminals using only a clock signal.

1-2 phase excitation (M1: H, M2: L, CW mode)


1-2 phase excitation (M1: H, M2: L, CCW mode)
$I_{A}$

IB


W1-2 phase excitation (M1: L, M2: H, CW mode)


W1-2 phase excitation (M1: L, M2: H, CCW mode)


2W1-2 phase excitation (M1: H, M2: H, CW mode)

$\begin{array}{llllllllll}t_{0} & t_{1} & t_{2} & t_{3} & t_{4} & t_{5} & t_{6} & t_{7} & t_{8} & t_{9} \\ t_{10} & t_{11} & t_{12} & t_{13} & t_{14} & t_{15} t_{16} & t_{17} & t_{18} & t_{19} & t_{20}\end{array} t_{21} t_{22} t_{23} t_{24} t_{25} t_{26} t_{27} t_{28} t_{29} t_{30} t_{31} t_{32}$

2W1-2 phase excitation (M1: H, M2: H, CCW mode)


4W1-2 phase excitation (M1: L, M2: L, CW mode)



4W1-2 phase excitation (M1: L, M2: L, CCW mode)

t Oh
6. Supplemental explanation

| Pin No. | Symbol | Pin name | Remarks | Supplemental explanation |
| :---: | :---: | :---: | :---: | :---: |
| 4,5 | Vcc | Power supply pin for logic block | $\mathrm{Vcc}(\mathrm{opr})=2.7$ to 5.5 V | Applied voltage to power supply pin should be within the operating range. |
| 6 | STBY | Standby input | See the Input Signals and Operating Modes table. | Power consumption is reduced by turning off almost internal circuits when the current in the motor coil is unnecessary. This signal command has higher priority than other input signals (ENABLE, RESET, and so on). See the Input Signals and Operating Modes table of the data sheet. |
| 7 | OSC | Connection pin for an external capacitor used for internal oscillation |  | Connect the capacitor (Cosc) of 220pF between the OSC pin and the GND pin. |
| 8 | M1 | Excitation mode setting input 1 | See the Excitation Mode Settings table. | The excitation mode is set according to the Excitation Mode Settings table of the data sheet. In case |
| 9 | M2 | Excitation mode setting input 2 | See the Excitation Mode Settings table. | 2- phase excitation should be selected. |
| 10, 11 | VM | Power supply pin for output | VM (opr) $=2.5$ to 13.5 V | Applied voltage to power supply pin should be within the operating range. |
| 12 | CW/CCW | Rotation direction select input | See the Input Signals and Operating Modes table. | The rotation direction is switched by changing the output level. |
| 13 | B02 | B- phase output 2 | Connect B02 to a motor coil pin. | Output pin for connecting the motor coil. Do not connect the equipment other than motor. |
| 14 | RFB | Connection pin for a B- phase output current detection resistor |  | IOUT (A) $=1 / 5 \times \operatorname{Vref} V$ )/RNF $(\Omega)$ The current is determined when TQ outputs high (torque setting is 100 \%). The resistance, motor IC, and GND should be connected as close as possible. |
| 15 | B01 | B- phase output 1 | Connect B01 to a motor coil pin. | Output pin for connecting the motor coil. Do not connect the equipment other than motor. |
| 16 | A02 | A-phase output 2 | Connect A02 to a motor coil pin. | Output pin for connecting the motor coil. Do not connect the equipment other than motor. |
| 17 | RFA | Connection pin for an A-phase output current detection resistor |  | IOUT (A) $=1 / 5 \times \operatorname{Vref} \mathrm{V}) / \mathrm{RNF}(\Omega)$ The current is determined when TQ outputs high (torque setting is 100 \%). The resistance, motor IC, and GND should be connected as close as possible. |
| 18 | A01 | A- phase output 1 | Connect A01 to a motor coil pin. | Output pin for connecting the motor coil. Do not connect the equipment other than motor. |
| 19 | RESET | Reset input | See the Input Signal and Operating Modes table. | RESET $=\mathrm{L} \Rightarrow$ Initial mode (Output is fixed :A-phase current $100 \%$, B - phase current $100 \%$. $\overline{\mathrm{MO}}$ pin is monitored. Motor operates from the initial point of electrical angle by using RESET pin. See the "Relationship between the RESET and the outputs (OUT and $\overline{\mathrm{MO}}$ )"in page 17 of the data sheet. |
| 20, 21 | GND | Ground |  |  |
| 22 | $\overline{\mathrm{MO}}$ | Monitor output | Initial state: $\overline{\mathrm{MO}}=$ Low (open drain, pulled up by an external resistor) | Motor starts operating by confirming the initial point of the electrical angle by combining the ENABLE pin and the RESET pin in monitoring MO signal. See the "Relationship between the ENABLE and the outputs (OUT and $\overline{\mathrm{MO}}$ ) in page 16 and "Relationship between the RESET and the outputs (OUT and $\overline{\mathrm{MO}}$ ) in page 17 of the datasheet. |
| 23 | TQ | Vref setting input | See the Vref Voltage Setting table. | The current of $100 \%$ and $25 \%$ is set according to the table "V ref setting". The current setting of $25 \%$ can be used to fix the rotation axis of the motor without rotating the motor. |
| 1 | Vref | External set terminal for A-phase and B- phase reference voltage |  | IOUT (A) $=1 / 5 \times$ Vref V )/RNF ( $\Omega$ ) The current is determined when TQ outputs high (torque setting is 100 \%). Applied voltage to the power supply pin should be within the operating range. |
| 2 | ENABLE | Enable input | See the Input Signal and Operating Modes table. | Whether to apply the current to output pins (A01, A02, B01, and B02) or not is decided by high or low level of the ENABLE pin. The CLK of the internal IC is counted according to the CK signal when ENABLE is low and output is off. See the "Relationship between the ENABLE and the outputs (OUT and $\overline{\mathrm{MO}}$ ) in page 16 of the data sheet. |
| 3 | CK | Clock input |  | Steps of current level are proceeded by inputting the square wave. |

## 7. Current setting

When the motor supply voltage, the resistance between motor coil pins, the wiring resistance of the PCB board, ON resistance of the motor IC are assumed VM, Rm, Rp, and Ron each, conducting maximum current (Imax) is $60 \%$ to $80 \%$ of the value which is calculated from $\mathrm{VM} \div(\mathrm{Rm}+\mathrm{Rp}+\mathrm{Ron})$.
When setting of I (Limit) which is written in page 4 exceeds this Imax, its output current waveform corresponds to the pseudo sine wave without peak in saturated. Though motor can rotate, its operating performance falls because the pseudo sine waveform of the output current is not clear and the rotating step angle has variability.
To solve this problem, increase VM or decrease Rm .
As for our other stepping motor driver ICs, Imax is usually $90 \%$ to $95 \%$ of the value which is calculated from VM $\div$ ( $\mathrm{Rm}+\mathrm{Rp}+\mathrm{Ron}$ ). This IC realizes the stabilization of the PWM frequency and the equalizing of the pulse flow of the current waveform by making discharge a priority for PWM control. So, above phenomenon occurs because of charge deficiency.

When VM exceeds 12 V , the current waveform might be disordered. It does not influence on the motor operation. To confirm the silent operation, please examine it under the usage condition.

## 8. Test of short-circuiting

(1)Test of short-circuiting between outputs, air contamination faults, or faults due to improper grounding The results of the test are shown below. There is no problem.

$$
\begin{aligned}
& \text { Test conditions: } \mathrm{VCC}=5 \mathrm{~V}, \mathrm{VM}=12 \mathrm{~V} \text {, } \mathrm{Vref}=1 \mathrm{~V} \\
& \mathrm{RFA}=\mathrm{RFB}=1.5 \Omega \\
& \mathrm{STBY}=\mathrm{RESET}=\mathrm{ENABLE}=\mathrm{TQ}=\mathrm{M} 1=\mathrm{M} 2=\mathrm{CW} / \mathrm{CCW}=\mathrm{H} \\
& \mathrm{OSC}=220 \mathrm{pF}, \mathrm{CK}=1 \mathrm{kHz}, \text { Motor load }
\end{aligned}
$$

Test results of short-circuiting between outputs, air contamination faults, or faults due to improper grounding

| Pin | Result | Judgment |
| :--- | :--- | :--- |
| $\mathrm{AO} 1 \Leftrightarrow \mathrm{AO} 2$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{AO} 1 \Leftrightarrow \mathrm{VM}$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{AO} 1 \Leftrightarrow \mathrm{GND}$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{AO} 2 \Leftrightarrow \mathrm{VM}$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{AO} 2 \Leftrightarrow \mathrm{GND}$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{BO} 1 \Leftrightarrow \mathrm{BO} 2$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{BO} 1 \Leftrightarrow \mathrm{VM}$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{BO} 1 \Leftrightarrow \mathrm{GND}$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{BO} 2 \quad \mathrm{VM}$ | Normal appearance, no smoke, normal operation after test | Pass |
| $\mathrm{BO} 2 \Leftrightarrow \mathrm{GND}$ | Normal appearance, no smoke, normal operation after test | Pass |

ISD (over current detection) is operating.
(2)Test of short-circuiting between contiguous pins

$$
\begin{aligned}
& \text { Test conditions: } \mathrm{VCC}=5 \mathrm{~V}, \mathrm{VM}=12 \mathrm{~V} \text {, } \mathrm{Vref}=1 \mathrm{~V} \\
& \text { RFA }=\mathrm{RFB}=1.5 \Omega \\
& \mathrm{STBY}=\mathrm{RESET}=\mathrm{ENABLE}=\mathrm{TQ}=\mathrm{M} 1=\mathrm{M} 2=\mathrm{CW} / \mathrm{CCW}=\mathrm{H} \\
& \mathrm{OSC}=220 \mathrm{pF}, \mathrm{CK}=1 \mathrm{kHz} \text {, Motor load } \\
& \text { Resistance of } 1 \mathrm{k} \Omega \text { is connected between } \mathrm{M} 2 \text { and } \mathrm{VCC}, \mathrm{CW} / \mathrm{CCW} \text { and } \mathrm{VCC} \text {. } \\
& \text { (See below test circuit.) }
\end{aligned}
$$

## Test circuit



Test result of short-circuiting between contiguous pins

| Pin No. | Pin name | Result | Judgment |
| :---: | :---: | :---: | :---: |
| 1 | Vref | Normal appearance, no smoke, normal operation after test | Pass |
| 2 | ENABLE |  |  |
|  | CK | Normal appearance, no smoke, normal operation after test | Pass |
| 4 | VCC | Normal appearance, no smoke, normal operation after test | Pass |
| 5 |  | Normal appearance, no smoke, normal operation after test | Pass |
| 6 | STBY_B |  |  |
| 7 | OSC | Normal appearance, no smoke, normal operation after test | Pass |
| 8 | M1 |  |  |
|  |  | Normal appearance, no smoke, normal operation after test | Pass |
| 9 | M2 | Normal appearance, no smoke. Changing excitation mode by M2 pin after test is impossible. Pin is destroyed by round over voltage (12V) from the VM pin. | Pass |
| 10 | VM |  |  |
| 11 |  | Normal appearance, no smoke. Changing forw ard and reverse rotation by CW/CCW pin after test is impossible. Pin is destroyed by round over voltage (12V) from the VM pin. | Pass |
| 12 | CW/CCW |  |  |
| 13 | BO2 | Normal appearance, no smoke, normal operation after test | Pass |
| 14 | RFB |  |  |
|  |  | Normal appearance, no smoke, normal operation after test | Pass |
| 15 | B01 | Normal appearance, no smoke, normal operation after test | Pass |
|  |  |  |  |
| 16 | AO2 |  |  |
|  |  | Normal appearance, no smoke, normal operation after test | Pass |
| 17 | RFA |  |  |
| 18 | AO1 | Normal appearance, no smoke, normal operation after test | Pass |
| 19 | RESET | Normal appearance, no smoke, normal operation after test | Pass |
| 20 | GND |  |  |
| 21 |  | Normal appearance, no smoke, normal operation after test | Pass |
| 22 | MO_B |  |  |
|  |  | Normal appearance, no smoke, normal operation after test | Pass |
| 23 | TQ |  | - |
| 24 | (NC) | --- |  |

## RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

