TOSHIBA PHOTOCOUPLER IRED & PHOTO-IC

TLP2098



Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Input Current, ON	l _{F(ON)}	5		15	mA
Input Voltage , OFF	V _{F(OFF)}	0	_	0.8	V
Supply Voltage*	VCC	3.0	_	20	V _
Operating Temperature	T _{opr}	-40	_	100	°C

* This item denotes operating range, not meaning of recommended operating conditions.

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Absolute Maximum Ratings (Ta = 25°C)

$\begin{tabular}{ c c c c c c c } \hline CHARACTERISTIC & SYMBOL & RATING & UNIT \\ \hline Forward Current & IF & \pm 20 & mA \\ \hline Forward Current Derating (Ta $$ 83°C) & \Delta IF / °C & -0.48 & mA'C \\ \hline Peak Transient Forward Current (Note 1) & IFPT & \pm 1 & A \\ \hline Input Power Dissipation & PD & 40 & mW \\ \hline Input Power Dissipation Derating (Ta $$ 83°C) & \Delta PD / °C & -0.96 & mW / °C \\ \hline & & & & \\ \hline & & & & \\ \hline & & & & \\ \hline & & & &$						\frown
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		CHARACTERISTIC	SYMBOL	RATING		\bigcirc
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		Forward Current	IF (±20	mA	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		Forward Current Derating (Ta ≥ 83°C)	ΔI _F /°C) -0.48 🔿	mA/°C)	
$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	LED	Peak Transient Forward Current (Note 1)	IFPT	±1	A	O
$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$		Input Power Dissipation	PD	40	mVV	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		Input Power Dissipation Derating (Ta \ge 83°C)	ΔP _D /°C	-0.96	mW/°C	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		Output Current 1 (Ta ≤ 25°C)		25/-15	mA	
$\begin{tabular}{ c c c c c c } \hline & & & & & & & & & & & & & & & & & & $	R	Output Current 2 (Ta ≤ 100°C)	LO2	5/-5	mA	
$\begin{tabular}{ c c c c c c } \hline & & & & & & & & & & & & & & & & & & $	сто	Output Voltage	vo	-0.5 to 20	V	
$\begin{tabular}{ c c c c c c } \hline $ $ $ $ $ $ $ $ $ $ $ $ $ $ $ $ $ $ $	ETE	Output Power Dissipation	Ро	75	mW	
Operating Temperature Range Topr -40 to 100 °C Storage Temperature Range Tstq -55 to 125 °C		Output Power Dissipation Derating (Ta $\ge 25^{\circ}$ C)	ΔP _O /°C	-0.75	mW/°C	
Storage Temperature Range T _{stq} -55 to 125 °C		Supply Voltage	V _{CC}	-0.5 to 20	V	
Sur Content and Co	Oper	ating Temperature Range	Topr	-40 to 100	°C	
	Storage Temperature Range		T _{stg}	-55 to 125	°C	
Lead Solder Temperature (10 s) T _{SOL} 260 °C	Lead	Solder Temperature (10 s)	T _{sol}	260	°C	
Isolation Voltage (AC, 60 s, R.H. ≤ 60 %) (Note 2) BV _s 3750 V _{rms}	Isola	ion Voltage (AC, 60 s, R.H. \leq 60 %) (Note 2)	BVs	3750	V _{rms}	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width \leq 1 μ s, 300 pps.

Note 2: Device considered a two terminal device: pins 1 and 3 shorted together and pins 4, 5 and 6 shorted together.

Electrical Characteristics (Unless otherwise specified, Ta = -40 to 100°C, V_{CC} = 3.0 to 20 V)

	•	•		,			
CHARACTERISTIC	SYMBOL	TEST CIRCUIT	CONDITION	MIN	TYP.	MAX	UNIT
Input Forward Voltage	VF	—	I _F = 10 mA , Ta = 25 °C	1.45	1.57	1.75	V
Temperature Coefficient of Forward Voltage	ΔV _F /ΔTa	—	IF = 10 mA		-2.0		mV/°C
Input Capacitance	CT	_	V = 0 V, f = 1 MHz , Ta = 25 °C	Ŕ	190		pF
Logic Low Output Voltage	VOL	1	I _{OL} = 0.02 mA, I _F = 5 mA	$(\overline{7})$	0.2	0.6	V
Logic High Output Voltage	VOH	2	$I_{OH} = -0.02 \text{ mA}, V_{CC} = 3.0 \text{ V}$ $V_{F} = 0.8 \text{ V} V_{CC} = 20 \text{ V}$	2.0	2.5 19.5		V
Logic Low Supply Current	ICCL	3	IF = 5 mA	-		3.0	mA
Logic High Supply Current	ІССН	4	V _F = 0 V	-	2	3.0	mA
Logic Low Short Circuit Output Current (Note 3)	IOSL	5	$I_{F} = 5 \text{ mA}$ $V_{CC} \neq V_{O} = 4.5 \text{ V}$ $V_{CC} = V_{O} = 20 \text{ V}$	15	80		mA
Logic High Short Circuit Output Current (Note 3)	IOSH	6	$V_{F} = 0 V \qquad V_{CC} = 4.5 V$ $V_{O} = GND \qquad V_{CC} = 20 V$	-5	-12 -20		mA
Input Current Logic Low Output	IFHL	_	$I_0 = 0.02 \text{ mA}, V_0 < 0.6 \text{ V}$	75-	1.0	3.0	mA
Input Voltage Logic High Output	VFLH	_ <	$V_{\rm O} = -0.02 \rm mA, V_{\rm O} > 2.0 \rm V$	0.8	_	_	V
Input Current Hysteresis	IHYS		V _{CC} = 5 V	_	0.05		mA

* All typical values are at VCC =5 V, Ta = 25 °C

Note 3: Duration of output short circuit time should not exceed 10 ms.

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition		MIN	TYP.	MAX	UNIT
Capacitance input to output	CS	Vs = 0 V, f = 1 MHz	(Note 2)	_	0.8	_	pF
Isolation resistance	RS	R.H. ≤ 60 %, V _S = 500 V	(Note 2)	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 60 s	(Note 2)	3750	_		V _{rms}

Switching Characteristics (Unless otherwise specified, Ta = -40 to 100° C, V_{CC} = 3.0 to 20 V)

_							
CHARACTERISTIC	SYMBOL	TEST CIRCUIT	CONDITION	MIN	TYP.	MAX	UNIT
Propagation Delay Time to Logic High output	^t pLH		IF = 5→0 mA	30	150	250	ns
Propagation Delay Time to Logic Low output	^t pHL	7, 8 (Note 4)	IF = 0→5 mA	30	150	250	ns
Switching Time Dispersion between ON and OFF	^t pHL ^{−t} pLH		-			220	ns
Rise Time (10 – 90 %)	tr		$I_F = 5 \rightarrow 0 \text{ mA}$, $V_{CC} = 5 \text{ V}$	KO (30	75	ns
Fall Time (90 – 10 %)	t _f		$I_F = 0 \rightarrow 5 \text{ mA}$, $V_{CC} = 5 \text{ V}$	X	30	75	ns
Common Mode transient Immunity at High Level Output	CMH		V _{CM} = 1000 V _{p-p} , I _F = 0 mA, V _{CC} = 20 V, Ta = 25 °C	-15	_ (kV/μs
Common Mode transient Immunity at Low Level Output	CML	9	V _{CM} = 1000 V _{p-p} , I _F =5 mA, V _{CC} = 20 V, Ta = 25 °C	15	A		kV/μs

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*All typical values are at Ta = 25 $^{\circ}$ C

Note 4. VCC = 4.5 to 20 V at test circuit 7.



TEST CIRCUIT 7: Switching Time Test Circuit



Soldering and Storage

1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as

much as possible by observing the following conditions.

1) Using solder reflow

·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

- 2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)
 - Please preheat it at 150°C between 60 and 120 seconds.
 - Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75% respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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Specification for Embossed–Tape Packing (TPL)(TPR) for Mini-flat Coupler

1. Applicable Package



2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.



→ [[G]]/RoHS COMPATIBLE (Note 5)

- Tape type
- Device name

3. Tape Dimensions

3.1 Specification Classification are as shown in Table 1

Table 1 Tape Type Classification

Tape type	Classification	Quantity (pcs / reel)
TPL	L direction	3000
TPR	R direction	3000

3.2 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.





3.3 Empty Device Recesses are as shown in Table 2.

Table 2 Empty Device Recesses

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0 device	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 devices (max) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and Table 3.



Table 3 Tape Dimensions

\sim	>	Table 3 Tape Dimensions						
22	\searrow	$\langle \rangle$	Unit: mm Unless otherwise specified: ±0.1					
(\frown)	Symbol	Dimension	Remark					
\bigcirc	А	4.2	_					
\searrow	В	(7.6)	_					
	D	5.5	Centre line of indented square hole and sprocket hole					
>	E	1.75	Distance between tape edge and hole center					
,	F	8.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes					
	G	4.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes					
	К ₀	2.8	Internal space					

3.6 Reel

- (1) Material: Plastic
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 4.



4. Packing

Packed in a shipping carton.

5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

When placing an order, please specify the product number, the tape type and the quantity as shown in the following example.

(Example)



Note 5 Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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