TOSHIBA Field Effect Transistor Silicon P Channel MOS Type ($L^2-\pi$ -MOSV)

2SJ349

DC-DC Converter, Relay Drive and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON-resistance $: RDS(ON) = 33 \text{ m}\Omega \text{ (typ.)}$

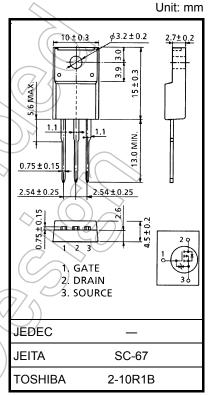
• High forward transfer admittance $|Y_{fs}| = 20 \text{ S (typ.)}$

• Low leakage current : $IDSS = -100 \mu A (max) (VDS = -60 V)$

• Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	etics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-60	V
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	-60	V
Gate-source voltage		V_{GSS}	±20	✓ v
Drain current	DC (Note 1)	I _D	-20	Α
	Pulse(Note 1)	I _{DP}	-80	A
Drain power dissipation	r (Tc = 25°C)	P _D	45	/ (w
Single pulse avalanche	energy (Note 2)	EAS	800	mJ
Avalanche current		JAR	-20	A
Repetitive avalanche e	nergy (Note 3)	EAR	4.5	μŊ
Channel temperature	(Tch	150	(e)
Storage temperature ra	inge	T _{stg}	−55 to 150	Ĵ~C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = -50 V, T_{ch} = 25°C (initial), L = 1.44 mH, R_G = 25 Ω , I_{AR} = -20 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Electrical Characteristics (Ta = 25°C)

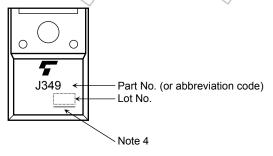
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I_{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cur	rent	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V	_	_	-100	μA
Drain-source br	eakdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-60	_	_	V
Gate threshold v	roltage	V_{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	/	-2.0	V
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = -4 V, I _D = -10 A	1) 50	90	mΩ
2.4 304/00 014 100/04/100		20 (0.1)	$V_{GS} = -10 \text{ V}, I_D = -10 \text{ A}$	7 <u>~</u>	33	45	
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -10 \text{ A}$	10	20	_	S
Input capacitano	e	C _{iss}			2800	1	
Reverse transfer	capacitance	C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	450	_	pF
Output capacitar	nce	Coss		_	1300	_	
Switching time F	Rise time	t _r	$\begin{array}{c c} V_{GS} \stackrel{0V}{\longrightarrow} & I_{D} = -10A \\ -10V & & & \\ R_{L} = \\ 3\Omega & & \\ \end{array}$	- (15	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	- ns
	Turn-on time	t _{on}			35	_	
	Fall time	t _f	$V_{DD} = -30V$		25	l	110
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{W} = 10 \mu s$) —	120	l	
Total gate charg plus gate-drain)		Qg		_	90	_	
Gate-source cha	arge	Q _{gs}	$V_{DD} \approx -48 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$	_	65	_	nC
Gate-drain ("mil	ler") charge	Qgd			25	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	-20	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	-80	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = -20 A, V _{GS} = 0 V	_	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = -20 A, V _{GS} = 0 V,	_	75	_	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 50 A / µs	_	83	_	nC

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Marking

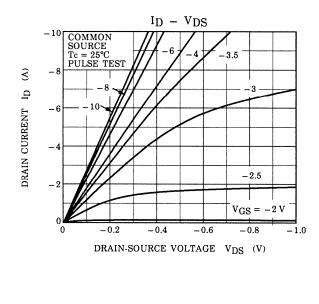


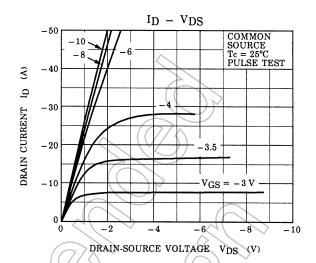
Note 4: A line under a Lot No. identifies the indication of product Labels.

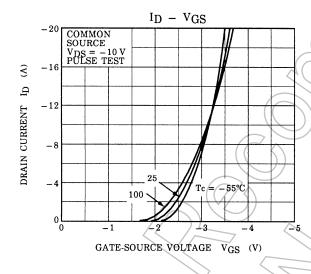
Not underlined: [[Pb]]/INCLUDES > MCV

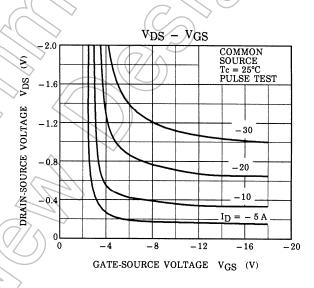
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

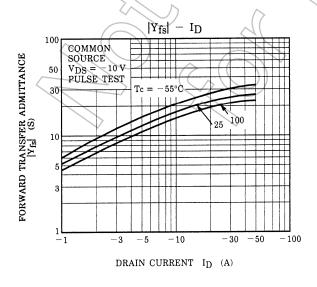
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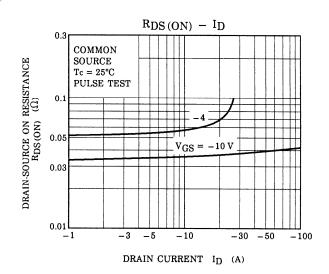




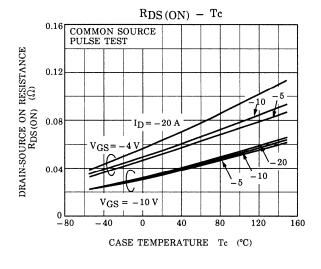


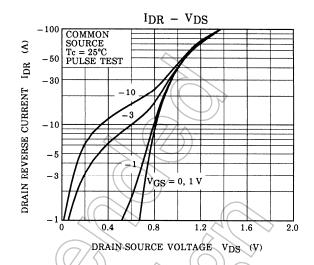


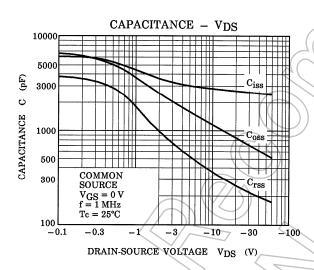


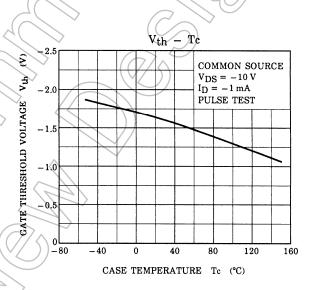


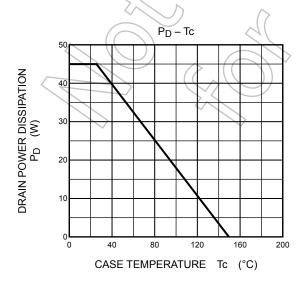
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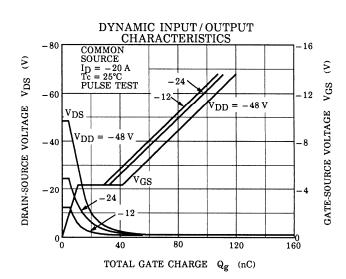




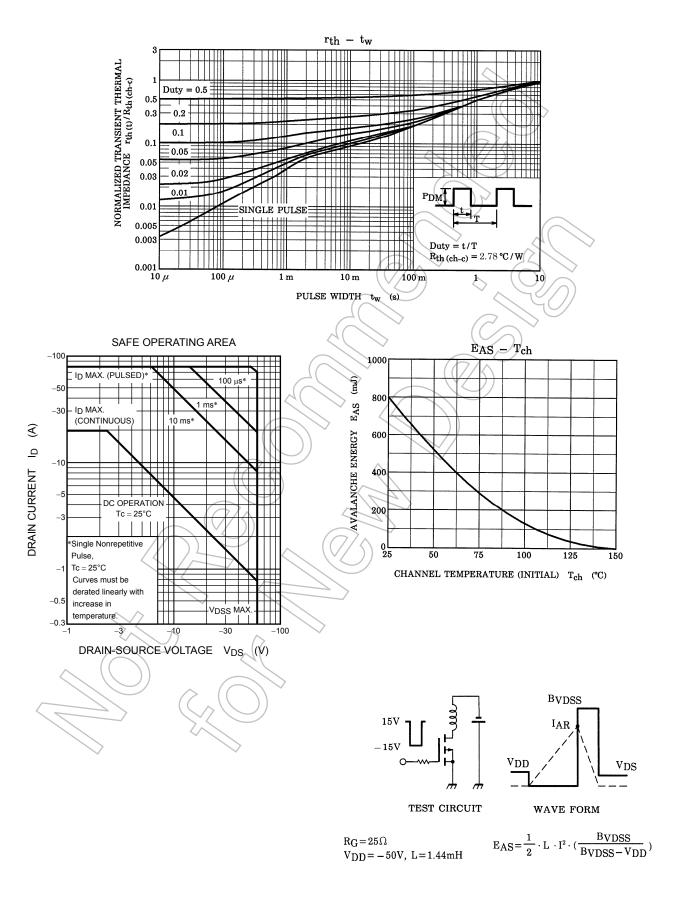








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