TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSIV)

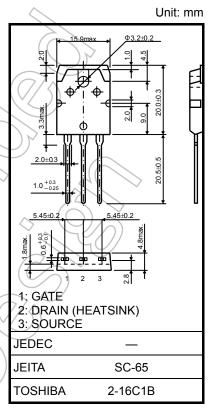
# 2SK4207

### **Swiching Regulator Applications**

- Low drain-source ON-resistance:  $R_{DS (ON)} = 0.78 \Omega (typ.)$
- High forward transfer admittance: |Yfs| = 11 S (typ.)
- Low leakage current: I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 720 V)
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	900	$\langle \rangle$
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	900	V
Gate-source voltage		$V_{GSS}$	±30	<b>\</b>
Drain current	DC (Note 1)	I <sub>D</sub>	13	A
	Pulse (Note 1)	I <sub>DP</sub>	39	Α
Drain power dissipation	r (Tc = 25°C)	PD	150	W
Single pulse avalanche	energy (Note 2)	E <sub>AS</sub>	491	mJ mJ
Avalanche current		I <sub>AR</sub>	)) 13	Α
Repetitive avalanche e	nergy (Note 3)	EAR	15	mJ
Channel temperature		T <sub>ch</sub>	150	)°C
Storage temperature ra	inge	Tstg	-55 to 150	\e



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

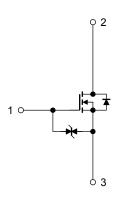
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th</sub> (ch-c)	0.833	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C / W



Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 5.3 mH,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



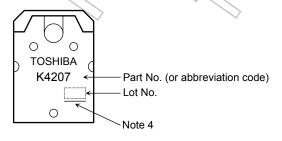
#### **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bro	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V	/	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	) /_	4.0	V
Drain-source O	N-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A	) <u> </u>	0.78	0.95	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6.5 A	5.0	11	_	S
Input capacitano	ce	C <sub>iss</sub>		_	2790	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	25	_	pF
Output capacitance		Coss		_	300	_	
Switching time	Rise time	t <sub>r</sub>	V <sub>GS</sub> <sub>OV</sub>	- (	53	\ \ \	
	Turn-on time	t <sub>on</sub>	50Ω RL=62Ω		88	) —	20
	Fall time	t <sub>f</sub>		7	43	_	ns
	Turn-off time	t <sub>off</sub>	$V_{DD}=400V$ $Duty \le 1\%, t_{W}=10\mu s$	) -	165	_	
Total gate charg		Qg		_	45	_	
Gate-source ch	arge	Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	32	_	nC
Gate-drain ("mi	ller") Charge	Q <sub>gd</sub>		_	13	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	<u> </u>	_	_	13	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	<del>-</del>	-	-	39	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V		1400		ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A /μs	_	24	_	μС

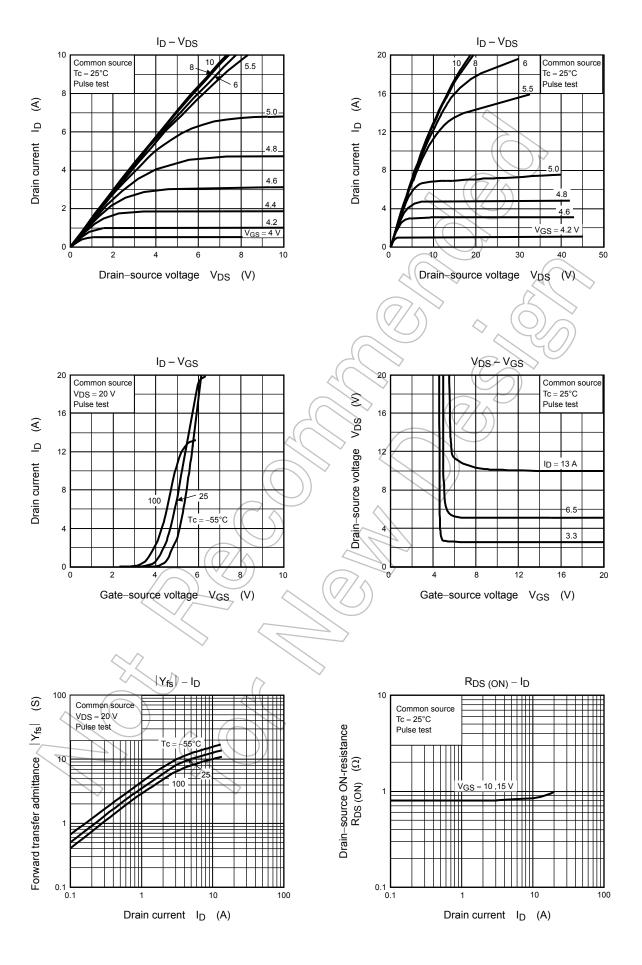
## Marking

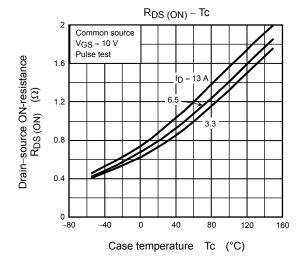


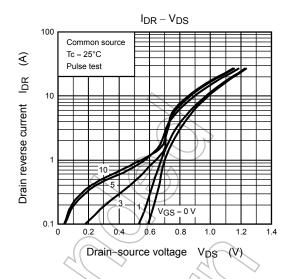
Note 4: A line under a Lot No. identifies the indication of product Labels.

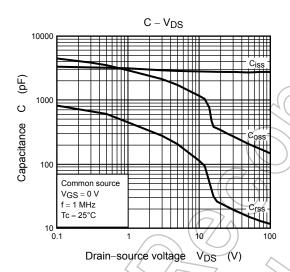
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

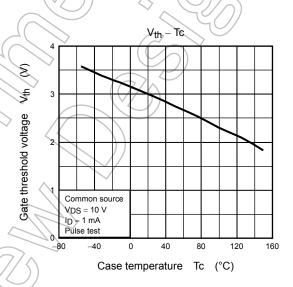
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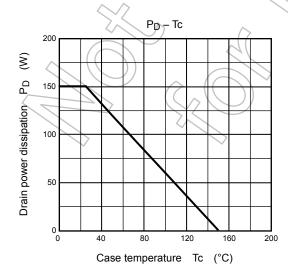


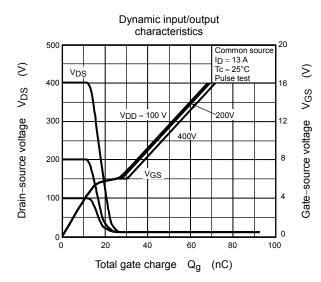


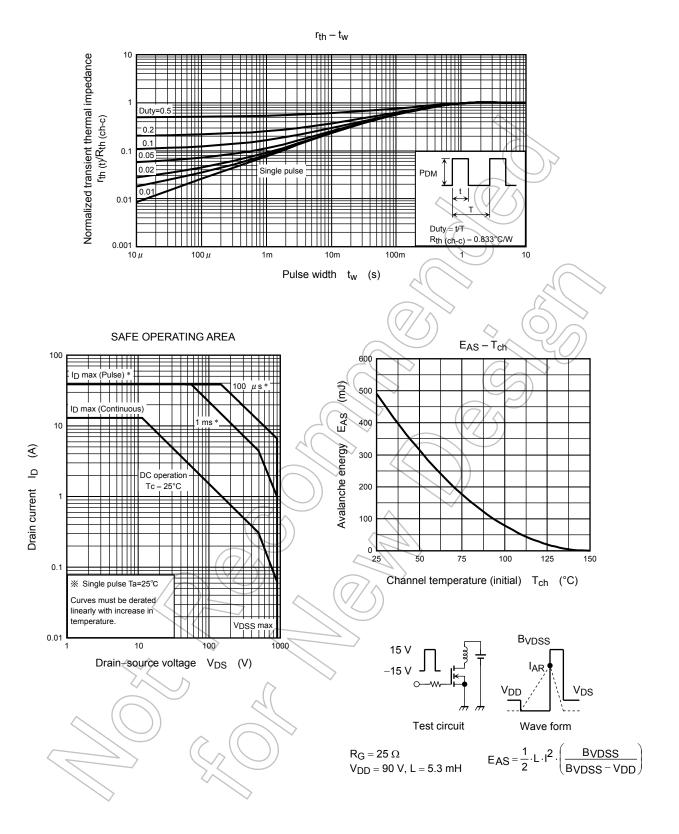












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