TOSHIBA Zener Diode Silicon Junction

# CMZB68 to CMZB82

# ○ Surge absorber

• Power dissipation : P = 1 W

• Zener voltage  $: V_Z = 68 \text{ to } 82 \text{ V}$ 

• Suitable for high-density board assembly due to the use of a small surface-mount package, M-FLAT<sup>TM</sup>

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Power dissipation	Р	1 (Note 1)	W
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g.

the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature / current / voltage, etc.) are within the absolute maximum ratings.

Unit: mm

2

1

1. Anode
2. Cathode

JEDEC

JEITA

TOSHIBA

3-4E1S

Weight: 0.023 g (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

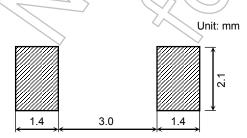
Note 1:  $Ta = 40^{\circ}C$ 

Device mounted on a glass-epoxy board
Board size : 50 mm × 50 mm

Land pattern : 6 mm × 6 mm

Board thickness : 1.6 mm

### Land Pattern Dimensions (for reference only)

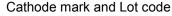


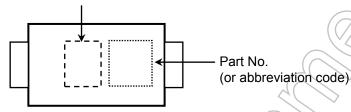
Start of commercial production 2010-09

#### **Electrical Characteristics (Ta = 25°C)**

	Zener Voltage Vz (V)			Dynamic Resistance $r_d\left(\Omega\right)$		Temperature Coefficient		Forward Voltage V <sub>F</sub> (V)		Reverse Current I <sub>R</sub> (μA)		
Type				Measure-		Measure-	αT (m	ıV/°C)		Measure-		Measure-
	Min	Тур.	Max	ment Current I <sub>Z</sub> (mA)	Max	ment Current I <sub>Z</sub> (mA)	Тур.	Max	Max	ment Current I <sub>F</sub> (A)	Max	ment Voltage V <sub>R</sub> (V)
CMZB68	61.2	68	74.8	4	120	4	57	90	1.2	0.2	10	54.4
CMZB75	67.5	75	82.5	4	150	4	66	104	1.2	0.2	10	60
CMZB82	73.8	82	90.2	3	170	3	71	113	(1.2/	0.2	10	65.6

## Marking





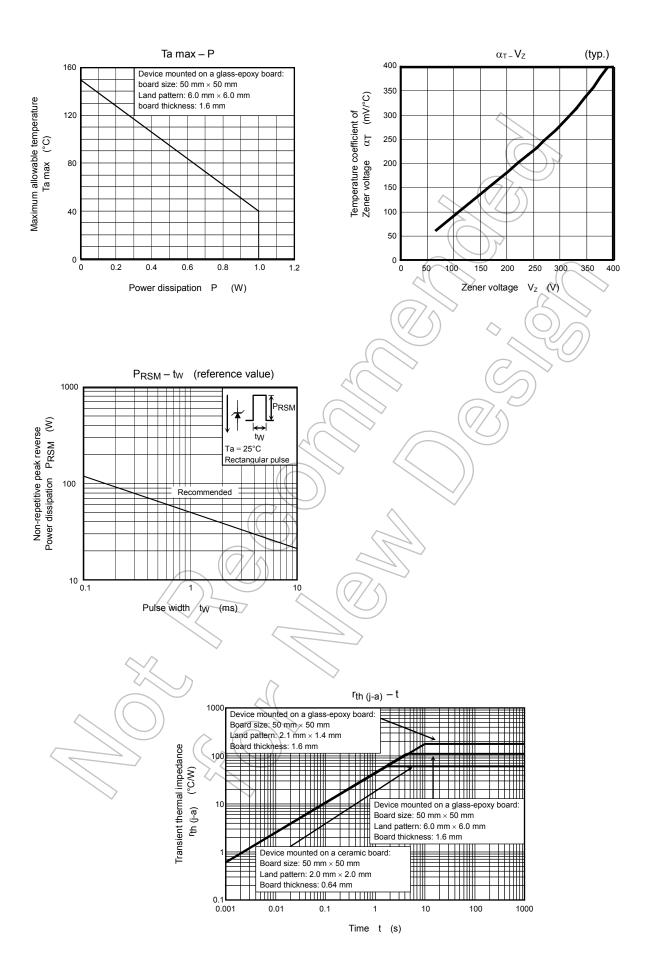
Part No.		
CMZB68		
CMZB75		
CMZB82		



# **Handling Precaution**

- 1) The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings. The following are the general derating methods that we recommend when you design a circuit with a device.
  - P: We recommend that the worst case power dissipation be no greater than 50% of the absolute maximum rating of power dissipation. Carry out adequate heat design.
  - PRSM: We recommend that a device be used within the recommended area in the figure, PRSM-tw.
  - Tj Derate this rating when using a device in order to ensure high reliability.

    We recommend that the device be used at Tj of below 120°C.
- 2) Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a land pattern to match the appropriate thermal resistance value
- 3) Please refer to the Rectifiers databook for further information.



3 2014-10-09

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