Sensorless Sinusoidal Brushless Motor Drive Circuit Using TC78B011FTG

Design guide

RD239-DGUIDE-01

Toshiba Electronic Devices & Storage Corporation

TOSHIBA

Table of Contents

1.	Introduction3
2.	Specifications and Block Diagram4
3.	Components Used5
3.1.	Brushless Motor Predriver TC78B011FTG5
3.2.	Power MOSFET TPH1R204PB6
3.3.	LDO TCR1HF50B7
4.	Circuit Design8
4.1.	Power Supply Circuit
4.2.	Inverter Circuit
4.3.	MCD Input Control Signal Circuit10
4.4.	MCD Output Signal Circuit12
4.5.	Motor Current Detection Circuit12
5.	Pattern Design13
5.1.	Kelvin Connection for Sensing Current13

1. Introduction

This Design Guide (hereafter referred to as this guide) describes the reference design (hereafter referred to as this design) of Sensorless Sinusoidal Brushless Motor Drive Circuit Using TC78B011FTG.

The use of brushless motors is increasing in various applications and as a result the requirement for a stable brushless motor control with less noise and customizable speed profile is also increasing. This design is based on Toshiba's <u>TC78B011FTG</u> (hereafter referred to as MCD (Motor Control Driver)) which is a 3-phase PWM chopper predriver, it uses the sensorless sinusoidal control method to run a brushless motor. Sinusoidal drive motor control makes it possible to run the motor with less noise and vibration. It also provides closed loop motor speed control without requiring any external microcontroller for a more stable motor speed control. The speed profile curve can be set in this MCD for precise motor control.

The motor control settings are done in MCD registers via I2C communication. Once these settings are done, the motor can be controlled via external signals which affect motor speed, direction, brake etc. The motor speed can be controlled via three different input methods, which includes PWM, analog voltage and I2C. The motor control setting of MCD registers can be stored in its internal non-volatile memory (NVM), and the registers are loaded when the MCD starts next time. Therefore, stable motor control can be achieved by simply performing various settings without an external microcontroller.

This design also uses Toshiba's <u>TPH1R204PB</u> power MOSFETs to drive the brushless motor and <u>TCR1HF50B</u> LDO to produce a 5 V power supply. We also developed a GUI to easily perform the MCD register setting via I2C communication. An USB-I2C converter is used for communication between the GUI (on PC) and the board (PCB) of this design is also prepared.

Applications of this design include server fans, blowers, pumps, vacuum cleaners, etc.

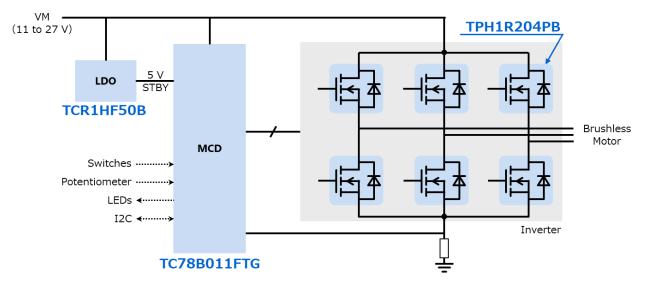
2. Specifications and Block Diagram

Table 2.1 lists the main specifications of the board (PCB) of this design.

Item	Specifications		
Input Power Supply Voltage	DC 11 to 27 V		
Input Control Signal Voltage	DC 5 V		
Rated Output Current	20 A (Max.)		
Driven Motor Type	Brushless Motor		
Motor Control Type	Sensorless Sinusoidal Drive		
MCD Setting Method	I2C		
Main Motor Control Signals	Brake, Direction, Speed signal		
Board Size	90 x 50 mm		
Dudi u Size	(Consisting Main Board of 39 x 50 mm)		
Board Layer Configuration	4-layer through-hole (outer layer 35 μ m, inner layer 70 μ m)		

Table 2.1 Specifications of This Design

Fig. 2.1 shows the block diagram of the board (PCB) of this design.





3. Components Used

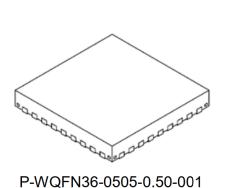
This section introduces the components that are used in this inverter. Toshiba has an extensive lineup of power semiconductors, driver couplers, and LDOs, including those used in this design.

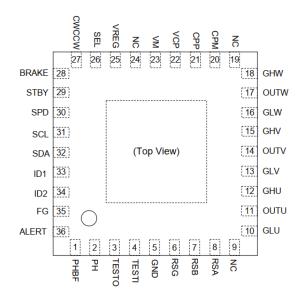
3.1. Brushless Motor Predriver TC78B011FTG

This design uses <u>TC78B011FTG</u> MCD for driving the brushless motor via an inverter circuit. **Features**

- Sine-wave drive
- Sensorless PWM drive
- Capable to drive Delta or Wye configured motors
- Operating voltage: 5.5 to 27 V (absolute maximum rating: 30 V)
- Predriver for high side and low side N-ch MOSFETs drive
- 8 selectable levels of gate drive current
- Built-in closed loop speed control with adjustable speed curve
- Motor speed control by analog voltage, PWM duty cycle, or I2C
- Serial interface (I2C) for various settings
- Standby mode (by STBY pin)
- Current monitor output (PHBF pin)
- CW/CCW control (CWCCW pin)
- Brake input pin (BRAKE pin)
- Rotation speed output (FG pin)
- Abnormality detection output (ALERT pin)
- Thermal shutdown (TSD)
- Under voltage lockout (UVLO)
- Charge pump low voltage detection (CPVSD)
- Output current limit (OCP)
- Over current detection (ISD)
- Lock protection
- Small QFN36 package

Appearance and Pin Layout





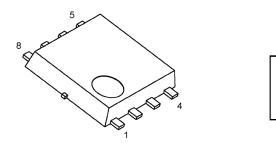
3.2. Power MOSFET TPH1R204PB

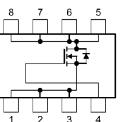
This design uses <u>TPH1R204PB</u> MOSFETs in the inverter circuit which is used for driving brushless motor.

Features

- High-speed switching
- Small gate charge: Q_{SW} = 21 nC (Typ.)
- Small output charge: Q_{OSS} = 56 nC (Typ.)
- Low on-resistance: $R_{DS(ON)} = 0.85 \text{ m}\Omega \text{ (Typ.)} (V_{GS} = 10 \text{ V})$
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (Max.) \ (V_{DS} = 40 \ V)$
- Enhancement mode: $V_{th} = 2.0$ to 3.0 V ($V_{DS} = 10$ V, $I_D = 0.5$ mA)

Appearance and Pin Layout





1, 2, 3: Source 4: Gate 5, 6, 7, 8: Drain

SOP Advance

3.3. LDO TCR1HF50B

This design uses <u>TCR1HF50B</u> as a stabilized power source for 5 V.

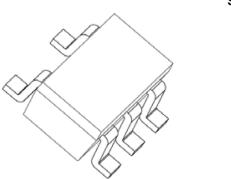
Features

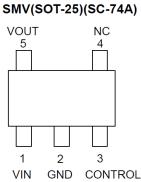
- High input voltage 40 V (Absolute Maximum ratings), 4 V to 36 V (Operation input voltage)
- Low quiescent current I_{BON} : 1 μ A (Typ.) @ $I_{OUT} = 0$ mA
- High response load transient

-60 mV / +50 mV, I_{OUT} = 0 mA \leftrightarrow 10 mA

- Output voltage V_{OUT} = 5.0 V
- High accuracy output voltage ± 1 % (Ta = 25 °C)
- Overcurrent protection
- Thermal shutdown
- In rush current reduction
- Pull up connection between CONTROL and VIN
- Ceramic capacitors can be used
- General package SMV (SOT-25) (2.8 mm x 2.9 mm x 1.1 mm)

Appearance and Pin Layout





4. Circuit Design

This section describes the gist of the circuit design. Refer to RD239-SCHEMATIC-xx.pdf for the schematic and to RD239-BOM-xx.pdf for the bill of material. The block diagram of this design is shown in Fig. 2.1.

4.1. Power Supply Circuit

Motor power supply (VM) is the main power supply in this design, and it is input via the motor power supply input connector CN1. This power supply is sent directly to the TC78B011FTG motor predriver IC and to the inverter circuit made up of TPH1R204PB MOSFETs. Fig. 4.1 shows the VM power supply input circuit. VM LED (DS1, Orange) turns on when VM power supply is available. VM input voltage range is from 11 to 27 V.

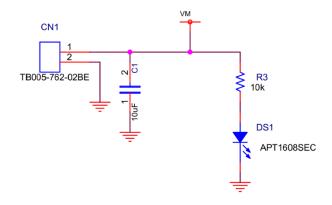
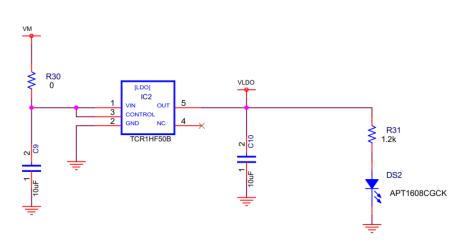


Fig. 4.1 VM Power Supply Input Circuit

A voltage of 5 V(VLDO) is required to pull up various signals like STBY, SPD, etc. in this design. This 5 V(VLDO) voltage is produced by TCR1HF50B LDO using VM as input voltage as shown in Fig. 4.2. TCR1HF50B LDO supports wide input voltage range from 4 to 36 V which is idea for use in this design as the range of input voltage VM is from 11 to 27 V. VLDO LED (DS2, Green) turns on when VLDO power supply is available.





4.2. Inverter Circuit

The inverter circuit is used to drive a 3-phase brushless motor by using the DC power supply (VM). Six high speed TPH1R204PB MOSFETs are used as switching devices in this inverter. These MOSFETs are controlled by TC78B011FTG. The inverter circuit of this design is shown in the following Fig. 4.3.

Pull down resistors like R18 are used between the gate and source of each MOSFET to turn off the MOSFET when the gate signal is not high. Series resistors like R17 are used between TC78B011FTG and gate of each MOSFET to control the charge and discharge rate of the gate terminal which affects the turn on and turn off time of the MOSFET. Therefore, these series resistors can be changed to adjust the turn on and turn off time of MOSFET according to the required application. A shunt resistor R27 is used to monitor the total current flowing through the inverter and the motor. This current is calculated by TC78B011FTG by measuring the voltage difference across this shunt resistor R27 via kelvin connection using RSB and RSG signals.

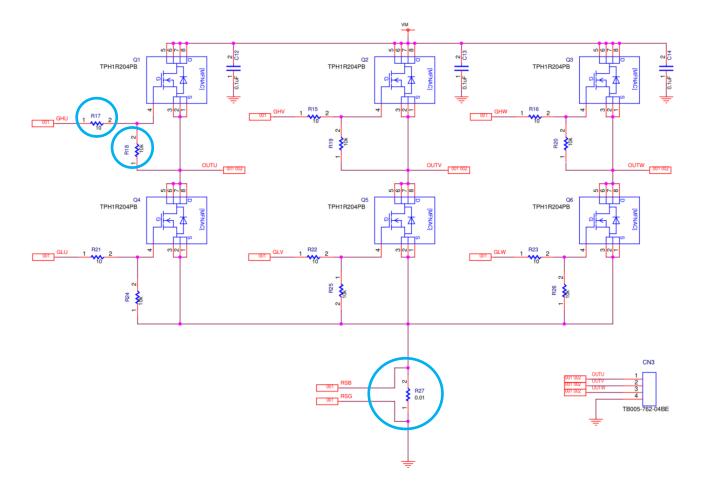


Fig. 4.3 Inverter Circuit

4.3. MCD Input Control Signal Circuit

MCD input control signals are used by the user to control the motor operation. These signals include ID1, ID2, STBY, SEL, CWCCW, BRAKE, SPD. This design allows the user to set these signals in three ways, first by using switches and potentiometer on the sub board, second by connecting an external MCU to the CN6 connector on the sub board, and third by using solder jumpers (SJP1 to SJP8) on the main board. Refer to the operation modes in reference guide document (RD239-RGUIDE-xx.pdf) for more information about these modes.

The main circuit of MCD input control signals is shown in following image. The circuit on the sub board is such that the signals from external MCU coming via CN6 takes priority over the signals coming from the switches and potentiometer on the sub board. This is achieved by using series resistors, for example resistor R39 in the case of BRAKE signal. When the external controller is not connected to the BRAKE signal on CN6, the BRAKE signal going to MCD is controlled by SW4 switch. Although whenever an external controller is connected to the BRAKE signal on CN6, the BRAKE signal going to MCD is controlled by external controller irrespective of the position of switch SW4. Similarly other signals are also controlled.

The jumper JP1 for SPD signal can be used to completely disconnect the potentiometer while using the high frequency PWM speed control from the external MCU.

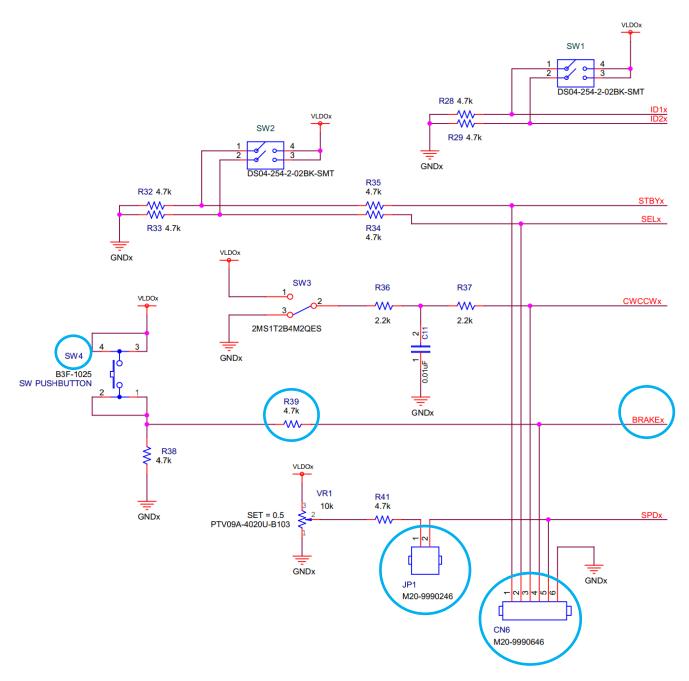
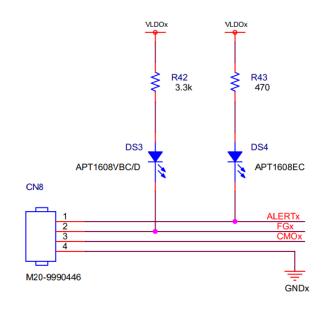


Fig. 4.4 MCD Input Control Signal Circuit

4.4. MCD Output Signal Circuit

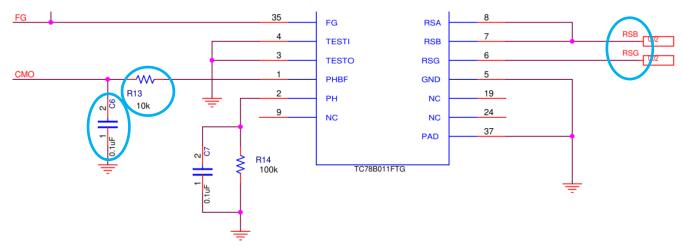
Output signals from MCD are ALERT, FG and CMO. LEDs DS3(blue) and DS4(red) are used to indicate the status of ALERT and FG signals. All the output signals can also be read by the external MCU connected to CN8. CMO signal represents the motor current; refer to section 4.5 for more details.





4.5. Motor Current Detection Circuit

The kelvin connection from shunt resistor R27 (RSB and RSG signals) is used to monitor motor current as described in section 4.2. In addition, the RSB signal is also input to RSA pin and is also used for output over-current detection function. In addition, the motor current value is amplified and output at the PHBF pin of TC78B011FTG for the user to monitor the motor current. C6 and R13 are used to form a low pass filter which provides a dc analog voltage output (CMO) corresponding the motor current value. PH pin is used to configure the internal peak hold circuit. It is recommended to use a resistor of 100 k Ω and a capacitor of 0.1 μ F in parallel between PH pin and GND.





5. Pattern Design

5.1. Kelvin Connection for Sensing Current

Kelvin connection is used for the reliable measurement of voltage difference across the motor current sensing shunt resistor R27. TC78B011FTG uses dedicated RSB and RSG inputs for sensing the motor current. The shunt resistor R27 and the pattern for RSB and RSG inputs are shown in the following Fig. 5.1.

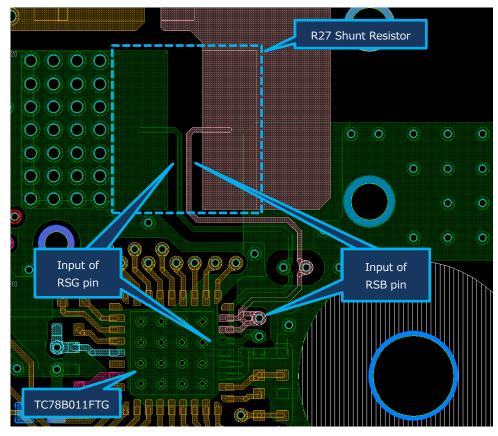


Fig. 5.1 Pattern for Kelvin Connection for Sensing Current

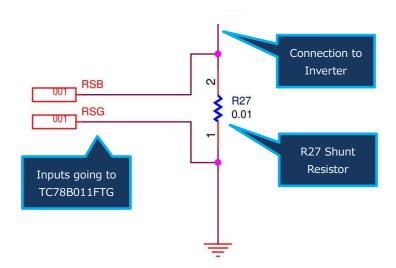


Fig. 5.2 Circuit of Kelvin Connection for Sensing Current

Terms of Use

This terms of use is made between Toshiba Electronic Devices and Storage Corporation ("We") and Customer who downloads or uses this Reference Design. Customer shall comply with this terms of use. This Reference Design means all documents and data in order to design electronics applications on which our semiconductor device is embedded.

Section 1. Restrictions on usage

 This Reference Design is provided solely as reference data for designing electronics applications. Customer shall not use this Reference Design for any other purpose, including without limitation, verification of reliability.
Customer shall not use this Reference Design for sale, lease or other transfer.

3. Customer shall not use this Reference Design for evaluation in high or low temperature, high humidity, or high electromagnetic environments.

4. This Reference Design shall not be used for or incorporated into any product or system whose manufacture, use, or sale is prohibited under any applicable laws or regulations.

Section 2. Limitations

1. We reserve the right to make changes to this Reference Design without notice.

2. This Reference Design should be treated as a reference only. WE ARE NOT RESPONSIBLE FOR ANY INCORRECT OR INCOMPLETE DATA AND INFORMATION.

 Semiconductor devices can malfunction or fail. When designing electronics applications by referring to this Reference Design, Customer is responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of semiconductor devices could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Customer must also refer to and comply with the latest versions of all relevant our information, including without limitation, specifications, data sheets and application notes for semiconductor devices, as well as the precautions and conditions set forth in the "Semiconductor Reliability Handbook".
Designing electronics applications by referring to this Reference Design, Customer must evaluate the whole system sufficiently. Customer is solely responsible for applying this Reference Design to Customer's own product design or applications. WE ASSUME NO LIABILITY FOR CUSTOMER'S PRODUCT DESIGN OR APPLICATIONS.
WE SHALL NOT BE RESPONSIBLE FOR ANY INFRINGEMENT OF PATENTS OR ANY OTHER INTELLECTUAL PROPERTY RIGHTS OF THIRD PARTIES THAT MAY RESULT FROM THE USE OF THIS REFERENCE DESIGN. NO LICENSE TO ANY INTELLECTUAL PROPERTY RIGHT IS GRANTED BY THIS TERMS OF USE, WHETHER EXPRESS OR

IMPLIED, BY ESTOPPEL OR OTHERWISE.

6. THIS REFERENCE DESIGN IS PROVIDED "AS IS". WE (a) ASSUME NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (b) DISCLAIM ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO THIS REFERENCE DESIGN, INCLUDING WITHOUT LIMITATION, WARRANTIES OR CONDITIONS OF FUNCTION AND WORKING, WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.

Section 3. Terms and Termination

It is assumed that Customer agrees to any and all this terms of use if Customer downloads or uses this Reference Design. We may, at its sole and exclusive discretion, change, alter, modify, add, and/or remove any part of this terms of use at any time without any prior notice. We may terminate this terms of use at any time and without any cause. Upon termination of this terms of use, Customer shall eliminate this Reference Design. Furthermore, upon our request, Customer shall submit to us a written confirmation to prove elimination of this Reference Design.

Section 4. Export Control

Customer shall not use or otherwise make available this Reference Design for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). This Reference Design may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Act and the U.S. Export Administration Regulations. Export and re-export of this Reference Design is strictly prohibited except in compliance with all applicable export laws and regulations.

Section 5. Governing Laws

This terms of use shall be governed and construed by laws of Japan, without reference to conflict of law principle.

Section 6. Jurisdiction

Unless otherwise specified, Tokyo District Court in Tokyo, Japan shall be exclusively the court of first jurisdiction for all disputes under this terms of use.